

AS393/393A

LOW POWER LOW OFFSET VOLTAGE DUAL COMPARATORS

Description

The AS393/393A consist of two independent precision voltage comparators with a typical offset voltage of 1.0mV and high gain. They are specifically designed to operate from a single power supply over wide range of voltages. Operation from split power supply is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

The AS393/393A series are compatible with industry standard 393. AS393A has more stringent input offset voltage than AS393.

The AS393 is available in standard PDIP-8, SO-8, TSSOP-8 and MSOP-8 packages, AS393A is available in standard PDIP-8 and SO-8 packages.

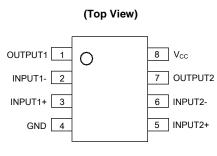
Features

- Wide Supply Voltage Range
 - Single Supply: 2V to 36V
 - Dual Supplies: ±1.0V to ±18V
- Low Supply Current Drain: 0.6mA
- Low Input Bias Current: 25nA (Typical)
- Low Input Offset Current: ±5.0nA (Typical)
- Low Input Offset Voltage: 1.0mV (Typical)
- Input Common Mode Voltage Range Includes Ground
- Differential Input Voltage Range Equals to the Power Supply Voltage
- Low Output Saturation Voltage: 200mV at 4mA
- Open Collector Output

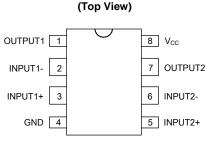
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- Lead-Free Packages: SO-8, TSSOP-8, PDIP-8
 - Totally Lead-Free; RoHS Compliant (Notes 1 & 2)
- Lead-Free Packages, Available in "Green" Molding Compound: SO-8, TSSOP-8, MSOP-8
 - Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
 - Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



SO-8/TSSOP-8/MSOP-8

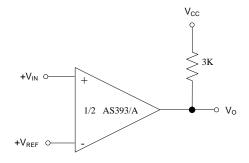


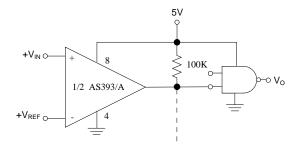
PDIP-8

Applications

- Battery Charger
- Cordless Telephone
- Switching Power Supply
- DC-DC Module
- PC Motherboard
- Communication Equipment
- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Typical Applications Circuit





Basic Comparator

Driving CMOS



Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating		Unit		
Vcc	Supply Voltage	40)	V		
V _{ID}	Differential Input Voltage	40		40		V
V _{IN}	Input Voltage	-0.3 to 40		V		
l _{IN}	Input Current (V _{IN} < -0.3V) (Note 5)	50		mA		
_	Output Short-Circuit Current to Ground	Continuous		_		
		PDIP-8	780			
2	SOIC-8	660				
PD	Power Dissipation ($T_A = +25^{\circ}C$)	TSSOP-8	570	mW		
		MSOP-8	450			
TJ	Operating Junction Temperature	+15	+150			
T _{STG}	Storage Temperature	-65 to +150		°C		
T _{LEAD}	Lead Temperature (Soldering, 10 Seconds)	+26	60	°C		

Notes: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

5. This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the comparators to go to the V+ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3 V_{DC} (at +25°C).

Recommended Operating Conditions

Symbol	Parameter	Min	Мах	Unit
V _{CC}	Supply Voltage	2	36	V
T _A	Operating Temperature Range	-40	+85	°C



Electrical Characteristics (Limits in standard typeface are for $T_A = +25$ °C, **bold** typeface applies over $T_A = -40$ to +85°C (Note 6), $V_{CC} = 5V$, GND = 0V, unless otherwise specified.)

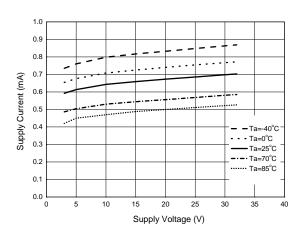
Parameter	Conditio	Min	Тур	Max	Unit	
		AS393	_	1.0	5.0	mV
	$V_{O} = 1.4V, R_{S} = 0\Omega,$			_	7	
Input Offset Voltage	$V_{CC} = 5$ to 30V			1.0	3.0	
	AS393A			_	5	
lanut Diag Querant	I _{IN} + or I _{IN} - with output in linear range,			25	250	
Input Bias Current	$V_{CM} = 0V$	_	_	—	400	nA
land Offent Current			_	5.0	50	_
Input Offset Current	$I_{IN}+-I_{IN}-, V_{CM}=0V$		_	—	200	nA
Input Common Mode Voltage Range (Note 7)	$V_{CC} = 30V$		0	—	V _{CC} -1.5	V
	$R_L = \infty$ $V_{CC} = 5V$	_	0.4	1.0		
		V ^{CC} = 2V	_	—	2	mA
Supply Current		V _{CC} = 30V	_	0.7	1.7	
			_	_	3	
Voltage Gain	V _{CC} = 15V, R _L ≥ 15kΩ	$V_{\rm O} = 1V \text{ to } 11V$	50	200	_	V/mV
Large Signal Response Time	$V_{\text{IN}} = \text{TTL Logic Swing}, V_{\text{REF}} = 1.4\text{V}, \\ V_{\text{RL}} = 5\text{V}, R_{\text{L}} = 5.1\text{k}$		_	200	_	ns
Response Time	$V_{RL} = 5V, R_L = 5.1k$			1.3	_	μs
Output Sink Current	V _{IN} - = 1V, V _{IN} + = 0V, V _O = 1.5V		6.0	16	—	mA
	V _{IN} - = 0V, V _{IN} + = 1V, V _O = 5V		_	0.1	_	nA
Output Leakage Current	$V_{IN} = 0V, V_{IN} = 1V,$	V _O = 30V	_	_	1	μA
			_	200	400	
Saturation Voltage	V_{IN} = 1V, V_{IN} = 0, Is	_{INK} ≤ 4mA	_	—	500	mV
	SO-8		_	9	_	°C/W
Thermal Resistance (Junction to Case)	TSSOP-8		_	15	_	
	MSOP-8		_	24	_	
	SO-8		_	108	_	
Thermal Resistance (Junction to Ambient)	TSSOP-8		_	179	_	
	MSOP-8		_	151	_	

Notes: 6. These specifications are limited to $-40^{\circ}C \le T_A \le +85^{\circ}C$. Limits over temperature are guaranteed by design, but not tested in production.

7. The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at +25°C). The upper end of the common-mode voltage range is V_{CC}-1.5V (at +25°C), but either or both inputs can go to +36V without damages, independent of the magnitude of the V_{CC}.

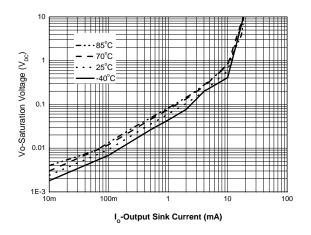


Performance Characteristics

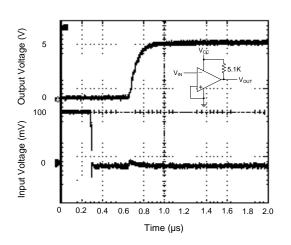


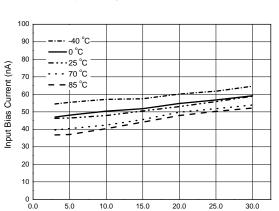
Supply Voltage vs. Supply Current

Output Sink Current vs. Saturation Voltage

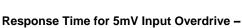


Response Time for 5mV Input Overdrive – Positive Transition



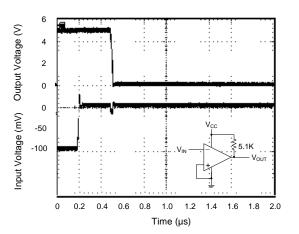


Supply Voltage vs. Input Bias Current

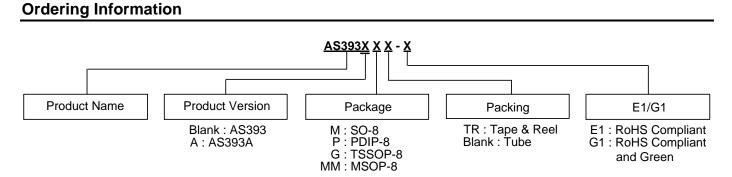


Negative Transition

Supply Voltage (V)







	Part Number	Package (Note 9)	RoHS Compliant Lead Free / Green	Marking ID	Packing	Quantity	Status (Note 8)	Alternative
Lead-Free	AS393M-E1		Lead Free	AS393M-E1	Tube	100	End of Life	AS393MTR- G1
Lead-Free Green	AS393M-G1		Green	AS393M-G1	Tube	100	End of Life	AS393MTR- G1
Lead-Free	AS393MTR-E1		Lead Free	AS393M-E1	Tape & Reel	4000	NRND	AS393MTR- G1
Lead-Free Green	AS393MTR-G1	<u> </u>	Green	AS393M-G1	Tape & Reel	4000	In Production	_
Lead-Free	AS393AM-E1	SO-8	Lead Free	AS393AM-E1	Tube	100	End of Life	AS393MTR- G1
Lead-Free Green	AS393AM-G1		Green	AS393AM-G1	Tube	100	End of Life	AS393MTR- G1
Lead-Free	AS393AMTR-E1		Lead Free	AS393AM-E1	Tape & Reel	4000	NRND	AS393AMT R-G1
Lead-Free Green	AS393AMTR-G1		Green	AS393AM-G1	Tape & Reel	4000	In Production	_
Lead-Free	AS393P-E1		Lead Free	AS393P-E1	Tube	50	In Production	_
Lead-Free Green	AS393P-G1		Green	AS393P-G1	Tube	50	End of Life	AS393P-E1
Lead-Free	AS393AP-E1	PDIP-8	Lead Free	AS393AP-E1	Tube	50	In Production	_
Lead-Free Green	AS393AP-G1		Green	AS393AP-G1	Tube	50	End of Life	AS393AP- E1
Lead-Free	AS393GTR-E1	T000D 0	Lead Free	EG3C	Tape & Reel	4000	NRND	AS393GTR- G1
Lead-Free Green	AS393GTR-G1	TSSOP-8	Green	GG3C	Tape & Reel	4000	In Production	_
Lead-Free Green	AS393MMTR- G1	MSOP-8	Green	AS393MM-G1	Tape & Reel	2500	In Production	_

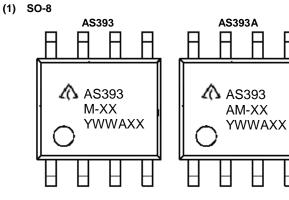
8. All variants in TDIP-8 package are End of life without replacements.

NRND: Not Recommended for New Design.

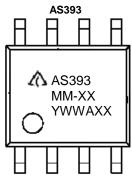
9. For packaging details, go to our website at: https://www.diodes.com/design/support/packaging/diodes-packaging/.

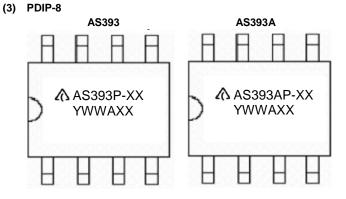
Notes:





(2) MSOP-8



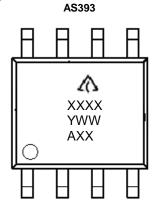


First and Second Lines: Logo and Marking ID (See Ordering Information) Third Line: Date Code Y: Year WW: Work Week of Molding A: Assembly House Code XX: Internal Code

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First Line: Logo and Marking ID (See Ordering Information) Second Line: Date Code Y: Year WW: Work Week of Molding A: Assembly House Code XX: Internal Code

(4) TSSOP-8

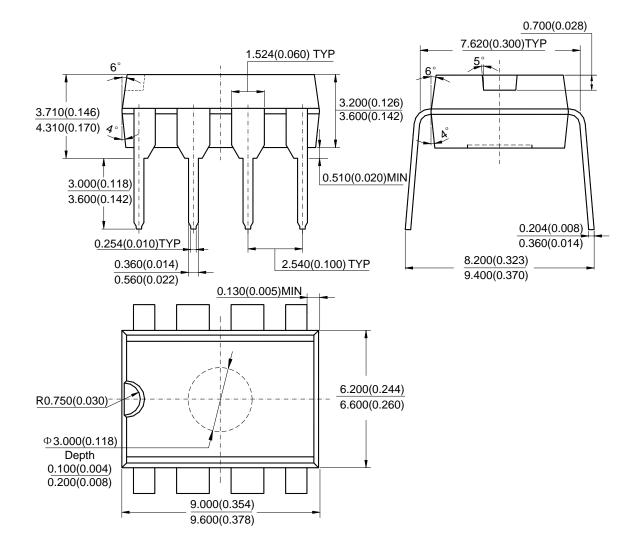


First Line: Logo Second Line: Marking ID (See Ordering Information) Third and Fourth Lines: Date Code Y: Year WW: Work Week of Molding A: Assembly House Code XX: Internal Code



Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: PDIP-8

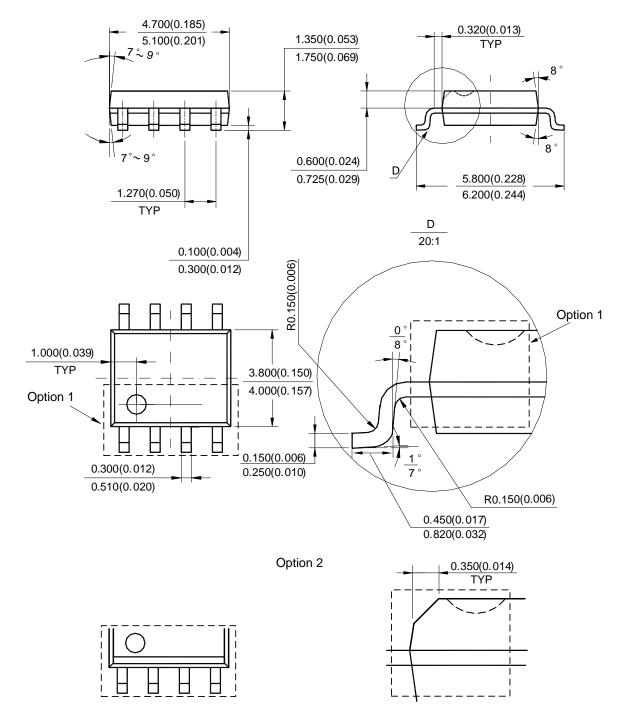


Note: Eject hole, oriented hole and mold mark is optional.



Package Outline Dimensions (Cont. All dimensions in mm(inch).)

(2) Package Type: SO-8

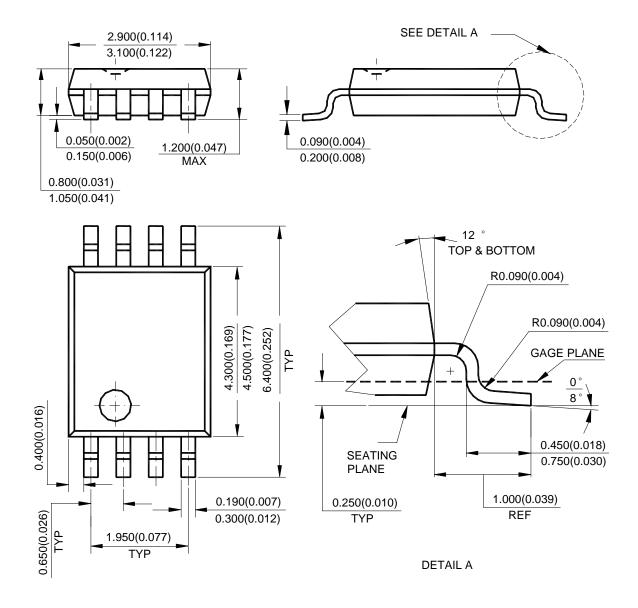


Note: Eject hole, oriented hole and mold mark is optional.



Package Outline Dimensions (Cont. All dimensions in mm(inch).)

(3) Package Type: TSSOP-8

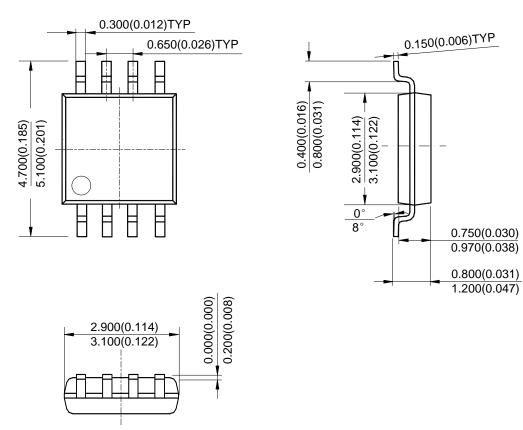


Note: Eject hole, oriented hole and mold mark is optional.



Package Outline Dimensions (Cont. All dimensions in mm(inch).)

(4) Package Type: MSOP-8

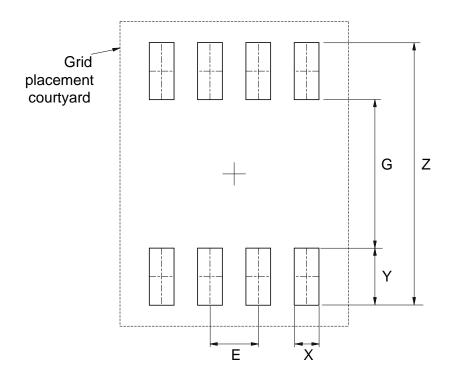


Note: Eject hole, oriented hole and mold mark is optional.



Suggested Pad Layout

(1) Package Type: SO-8

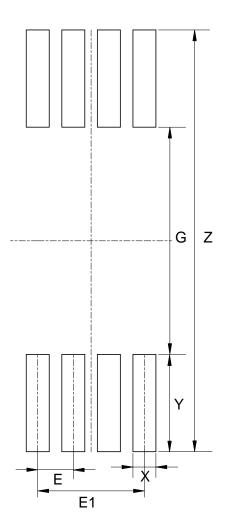


Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050



Suggested Pad Layout (Cont.)

(2) Package Type: TSSOP-8



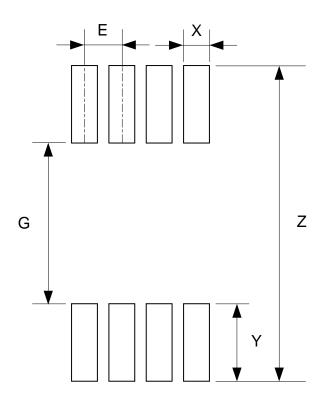
Dimensions	Z	G	X	Y	E	E1
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	7.720/0.304	4.160/0.164	0.420/0.017	1.780/0.070	0.650/0.026	1.950/0.077



AS393/393A

Suggested Pad Layout (Cont.)

(3) Package Type: MSOP-8



Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	5.500/0.217	2.800/0.110	0.450/0.018	1.350/0.053	0.650/0.026



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