



Ultra-High Voltage Protection USB2 1:2 Mux/DeMux

Features

- → Differential Bi-Directional 2:1 Mux/DeMux
- → Wide Input Voltage Range: 0-5.5V
- → Wide bandwidth: 1GHz
 - ♦ Ultra-low Con: 7pF
 - ♦ Ultra-low Ron: 5Ω (typ)
- → Low Propagation Delay, 0.25ns typ
- → Low Off-Isolation, -30dB@240MHz
- → Low Crosstalk: -35dB@240MHz,
- → Low Power Consumption: 35µA typical
- → Wide Supply Voltage 2.7-5.5V
- → Support 1.8V Logic on Control Pins
- → Protection Feature
 - ♦ Off-protection for current leakage in power-down mode
 - ♦ All I/O pins are high voltage tolerance
 - C0+/C0- tolerance to 24V
 - Lx+/- tolerance to 6V
 - V_{DD} tolerance to 9V
 - ♦ Over-voltage protection when Vbus short to C0-/C0+ when device is power-on and enabled
- → ESD Protection on (C0+/-)
 - ♦ IEC61000-4-2
- → Wide Temperature Range: -40°C to 85°C
- → Packaging (Pb-free & Green):
 - ♦ 10-contact, UQFN (ZUA10), 1.5x2mm, 0.5mm(H), 0.6mm pitch
 - ♦ 10-contact, UQFN (ZM10), 1.4x1.8mm, 0.55mm(H), 0.4mm pitch

Description

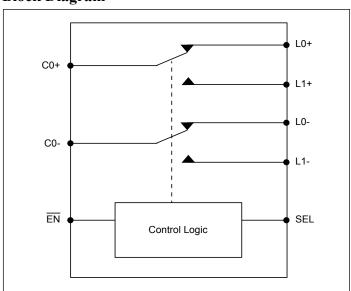
The PI3USB4000B is a 2-to-1 differential channel multiplexer/demultiplexer switch. C0+/C0- pins can tolerate voltages up to 24V. Over-voltage protection (OVP) is implemented at 4.75V to immediately switch off the channels when over-voltage condition is detected. PI3USB4000B can pass USB2.0 signal with bandwidth 1GHz to maintain signal integrity and eye diagram open.

Applications

→ Smart Phone, type-c application, Tablets, NB, PC

Block Diagram

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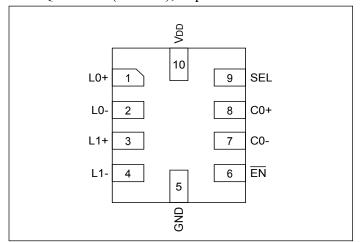




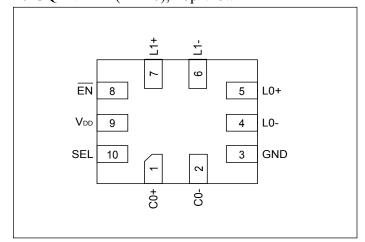


Pin Configuration

10-UQFN Pin# (ZUA10), Top View



10-UQFN Pin# (ZM10), Top View



Pin Description

10-UQFN Pin# (ZUA10)	10-UQFN Pin# (ZM10)	Pin Name	Signal Type	Description		
8,	1,	C0+,	I/O	Sizual I/O Common Port		
7	2	C0-	I/O	Signal I/O, Common Port		
3,	7,	Ll+,	I/O	Cimal I/O Chanal I		
4	8	L1-	I/O	Signal I/O, Channel 1		
1,	5,	L0+,	I/O	Simulation Champala		
2	4	L0-	I/O	Signal I/O, Channel 0		
9	10	SEL	I Operation mode Select (when SEL=0: C0→L0, when SEI C0→L1)			
6	8	EN	I $\overline{EN} = 1$, Power down is enabled. Please see Truth Table.			
10	9	VDD	Pwr Positive Supply Voltage			
5	3	GND	Pwr	Power ground		

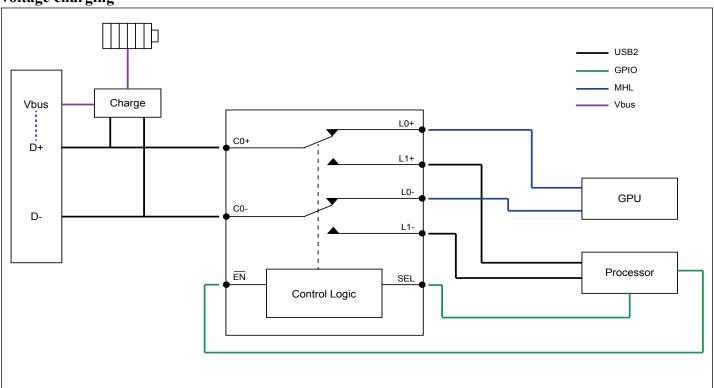
Truth Table

Function	SEL	EN
C0+/- to L0+/-	L	L
C0+/- to L1+/-	Н	L
All Switches Hi-z	X	Н

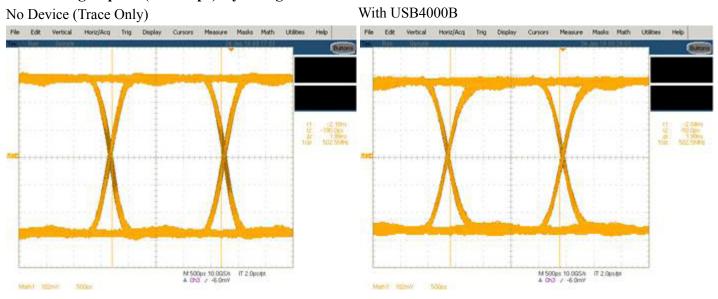




PI3USB4000B application in MHL Switching and provide overvoltage protection for D+/- when high voltage charging



USB2.0 High Speed (480Mbps) Eye Diagram







Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

65°C to +150°C
tial0.3V to +9V
0.3V to +6V
0.3V to +24V
0.3V to +6V
2KV (HBM) and 1KV (CDM)
±50mA

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions

Symbol	Description	Test Conditions	Min.	Тур.	Max.	Units
V_{DD}	Power Supply		2.7	3.3	6.0	V
V _{IO}	Analog Voltage Range		0		5.5	V
$V_{\rm I}$	Voltage Range for Control Pins		0		5.5	V
I_{DD}	Current Consumption in Normal Operation	V_{DD} =3.3 $\frac{V}{EN}$ = 0 $\frac{V}{EN}$ = Cov SEL= GND or $\frac{V}{EN}$ = Low		35	45	μА
I _{DD_OVP}	Current Consumption in OVP	V _{DD} =3.3V, V _{C0+} /V _{C0-} =5.5V, SEL=GND or V _{DD} , EN=Low		35		μА
I_{DDQ}	Chip Disabled Current Consumption	V_{DD} =3.3 $\frac{V}{EN}$ =0 $\frac{V}{EN}$ = High		1	2	μА
T_{A}	Operating Temperature Range		-40		85	°C

DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}\text{C} \text{ to } 85^{\circ}\text{C}, \text{ Typical values are at V}_{DD} = 3.3\text{V}, T_A = 25^{\circ}\text{C}, \text{ (unless otherwise noted))}$

Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units	
Control Pins - EN	Control Pins - EN/SEL						
V _{IH} - cntrl signals	Input HIGH Voltage for SEL and EN	$V_{\rm DD} = 2.7-5.5 V$	1.2			V	
V _{IL} - cntrl signals	Input LOW Voltage for SEL and EN	V _{DD} = 2.7-5.5V			0.6	V	
I_{IH}	Input HIGH Current for SEL and EN	$V_{\rm I} = 0-5.5 \rm V$	-1		1	μA	
I_{IL}	Input LOW Current for SEL and EN	$V_{\rm I} = 0-5.5 \rm V$	-1		1	μA	
High Speed IO – L0/L1/C0							
V _{OVP}	OVP trigger voltage		4.6	4.75	5.0	V	
Ron	ON resistance	$V_{I/O} = 0V, 0.4V, I_{on} = -8 \text{ mA}$		5	8	Ω	
Δ Ron	On resistance between + and - channel	$V_{I/O} = 0V, 0.4V, I_{on} = -8 \text{ mA}$		0.5	1	Ω	
Ron_Flat	ON resistance flatness	$V_{I/O} = 0V, 0.4V, I_{on} = -8 \text{ mA}$		0.2	0.5	Ω	
I _{off}	Power-off leakage	$V_{DD} = 0V, V_{I/O} = 0 - 3.6V$	-1		1	μA	
I _{OC}	Channel off leakage current	$\overline{\mathrm{EN}} = \mathrm{V_{DD}} = 3.3 \mathrm{V}, \mathrm{V_{I/O}} = 0 - 3.6 \mathrm{V}$	-1		1	μA	
I _{ON}	Channel on leakage current	$\overline{\mathrm{EN}}$ =0V, V _{DD} =3.3V, V _{I/O} =0-3.6V	-1		1	μA	
I _{OVP}	Leakage current on C0+/C0- in OVP mode	EN=0V, V _{DD} =3.3V, V _{C0+} or V _{C0-} =20V		3	15	μΑ	
Z _{ON_GND}	On-State impedance to GND	EN=low, SEL=H or L V _{I/O} =0-18V	5	7		ΜΩ	





Dynamic Electrical Characteristics

 $(T_A = -40^{\circ}C \text{ to } 85^{\circ}C, \text{ Typical values are at V}_{DD} = 3.3V, T_A = 25^{\circ}C, \text{ (unless otherwise noted))}$

Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units	
Control Pins -	Control Pins - EN/SEL						
C _I	Input capacitance	F=1MHz		5		pF	
High Speed IO	- L0/L1/C0		·				
Con	ON Capacitance	f=1MHz		7		pF	
Coff	OFF Capacitance	f=1MHz		9		pF	
DDIL	Insertion Loss	f=240MHz		-0.5		dB	
DDRL	Differential Return Loss	f=240MHz		-15		dB	
DDOI	Differential OFF Isolation	f=240MHz		-30		dB	
		f=100kHz		-80		dB	
DDXT	Differential Crosstalk	f=240MHz		-35		dB	
BW	-3dB Bandwidth			1		GHz	

Switching Characteristics⁽¹⁾

($T_A = -40$ °C to 85 °C, Typical values are at $V_{DD} = 3.3$ V, $T_A = 25$ °C, (unless otherwise noted))

Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units
t _{OVP}	OVP Response Time ⁽¹⁾	$R_{LX}=600\Omega$, time from the voltage on $C0\pm=4{\sim}6V$ to the voltage on $L_X\pm=4.75$		0.5	1	μs
t _{PZH} , t _{PZL}	Line Enable Time			20		μs
t _{PHZ} , t _{PLZ}	Line Disable Time	See Test Circuit for Electrial Characteristics	50		ns	
t_{Pd}	Propagation Delay		250		ps	
t _{b-b}	Bit-to-bit Skew Within the Same Differential Pair ⁽¹⁾			8	20	ps
Ton	Device Enable Time			100		μs
T _{off}	Device Disable Time			50		ns

Note:

1. Guaranteed by design.





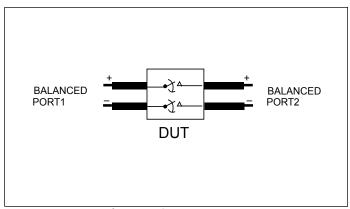


Fig 1. Differential Insertion Loss Setup

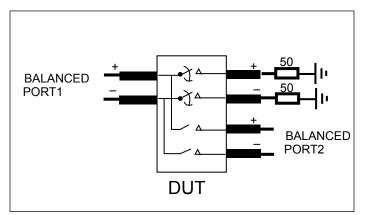


Fig 3. Crosstalk Setup

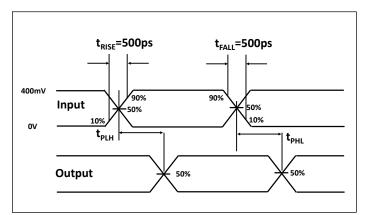


Fig 5. Skew Test

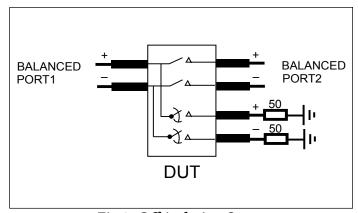


Fig 2. Off-isolation Setup

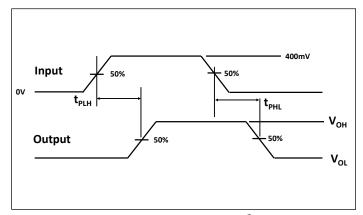
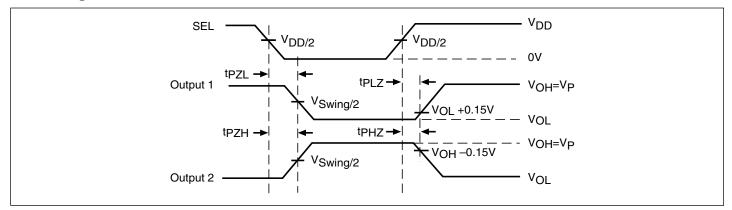


Fig 4. Propagation Delay



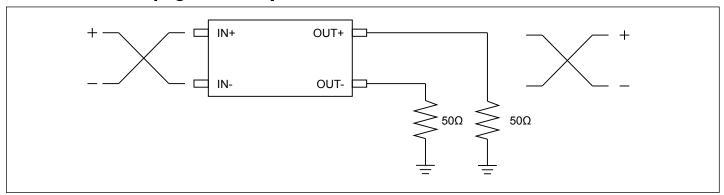


Switching Waveforms



Voltage Waveforms Enable and Disable Times

Test Circuit for Propagation Delay



Part Marking

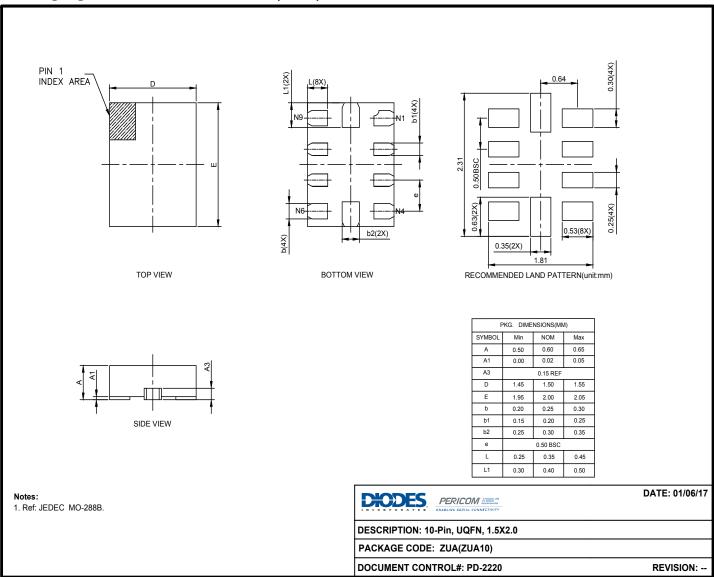
ZUA Package and ZM Package

Top mark not available at this time. To obtain advance information regarding the top mark, please contact your local sales representative.





Packaging Mechanical: 10-UQFN (ZUA)

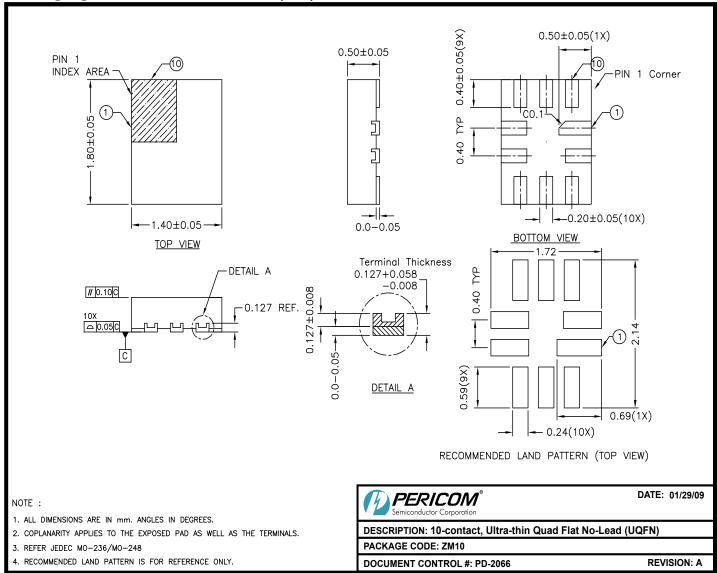


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Packaging Mechanical: 10-UQFN (ZM)



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For latest package info.

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Ordering Information

Ordering Code	Package Code	Package Description
PI3USB4000BZUAEX	ZUA	10-Pin, 1.5x2.0 (UQFN)
PI3USB4000BZMEX	ZM	10-contact, Ultra-thin Quad Flat No-Lead (UQFN)

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. Thermal characteristics can be found on the company web site at www.diodes.com/design/support/packaging/
- 3. E = Pb-free and Green
- 4. X suffix = Tape/Reel





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