

## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$ Max	$I_D$ $T_C = +25^\circ C$
100V	8.8m $\Omega$ @ $V_{GS} = 10V$	113A
	11.5m $\Omega$ @ $V_{GS} = 6V$	98A

## Description

This new generation N-channel enhancement mode MOSFET is designed to minimize  $R_{DS(ON)}$ , yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

## Applications

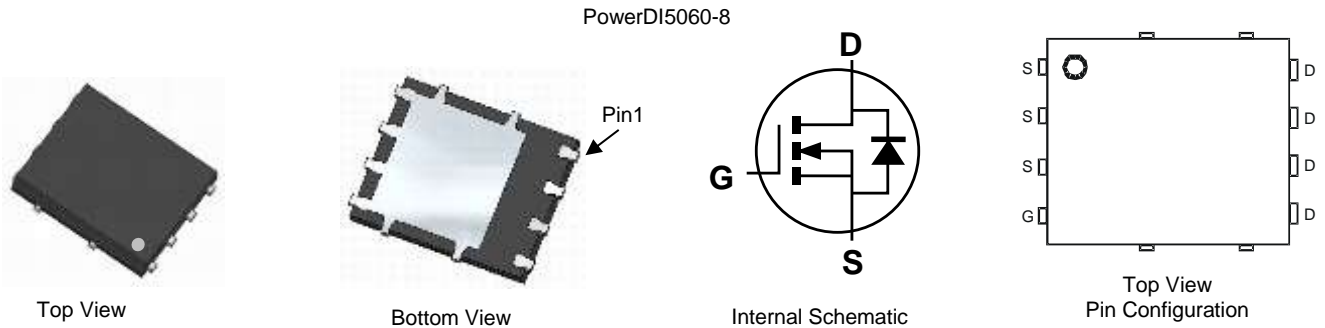
- Motor Control
- DC-DC Converters
- Power Management

## Features

- 100% Unclamped Inductive Switching – Ensures More Reliable and Robust End Application
- Low  $R_{DS(ON)}$  – Minimizes On-State Losses
- Fast Switching Speed
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: PowerDI<sup>®</sup> 5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminal Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.097 grams (Approximate)

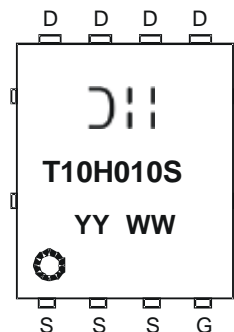


## Ordering Information (Note 4)

Part Number	Case	Packaging
DMT10H010SPS-13	PowerDI5060-8	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



= Manufacturer's Marking  
 T10H010S = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Last Two Digits of Year (ex: 17 = 2017)  
 WW = Week Code (01 to 53)

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	100	V	
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V	
Continuous Drain Current, $V_{GS} = 10\text{V}$ (Note 5)	$I_D$	$T_A = +25^\circ\text{C}$	10.7	A
		$T_A = +70^\circ\text{C}$	8.6	A
Continuous Drain Current, $V_{GS} = 10\text{V}$ (Note 6)	$I_D$	$T_C = +25^\circ\text{C}$	113	A
		$T_C = +70^\circ\text{C}$	90	A
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)	$I_{DM}$	250	A	
Maximum Continuous Body Diode Forward Current	$I_S$	100	A	
Avalanche Current, $L=0.3\text{mH}$	$I_{AS}$	33.7	A	
Avalanche Energy, $L=0.3\text{mH}$	$E_{AS}$	170	mJ	
Avalanche Current (Note 8), $L=3\text{mH}$	$I_{AS}$	14.3	A	
Avalanche Energy (Note 8), $L=3\text{mH}$	$E_{AS}$	307	mJ	

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_D$	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	99	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	$P_D$	139	W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	0.9	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 7)						
Drain-Source Breakdown Voltage	$BV_{DSS}$	100	—	—	V	$V_{GS} = 0\text{V}, I_D = 1\text{mA}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GSS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS</b> (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	2	—	4	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	6.6	8.8	m $\Omega$	$V_{GS} = 10\text{V}, I_D = 13\text{A}$
		—	8.5	11.5		$V_{GS} = 6\text{V}, I_D = 13\text{A}$
Diode Forward Voltage	$V_{SD}$	—	0.8	1.3	V	$V_{GS} = 0\text{V}, I_S = 13\text{A}$
<b>DYNAMIC CHARACTERISTICS</b> (Note 8)						
Input Capacitance	$C_{ISS}$	—	4,468	—	pF	$V_{DS} = 50\text{V}, V_{GS} = 0\text{V}$ $f = 1\text{MHz}$
Output Capacitance	$C_{OSS}$	—	746	—		
Reverse Transfer Capacitance	$C_{RSS}$	—	32	—		
Gate Resistance	$R_G$	—	0.91	—	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge	$Q_G$	—	56.4	—	nC	$V_{DD} = 50\text{V}, I_D = 13\text{A},$ $V_{GS} = 10\text{V}$
Gate-Source Charge	$Q_{GS}$	—	15.4	—		
Gate-Drain Charge	$Q_{GD}$	—	14	—		
Turn-On Delay Time	$t_{D(ON)}$	—	18.6	—	ns	$V_{DD} = 50\text{V}, V_{GS} = 10\text{V},$ $I_D = 13\text{A}, R_g = 6\Omega$
Turn-On Rise Time	$t_R$	—	22.5	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	44.8	—		
Turn-Off Fall Time	$t_F$	—	29.5	—		
Reverse Recovery Time	$t_{RR}$	—	54.5	—	ns	$I_F = 13\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Reverse Recovery Charge	$Q_{RR}$	—	106.4	—	nC	

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

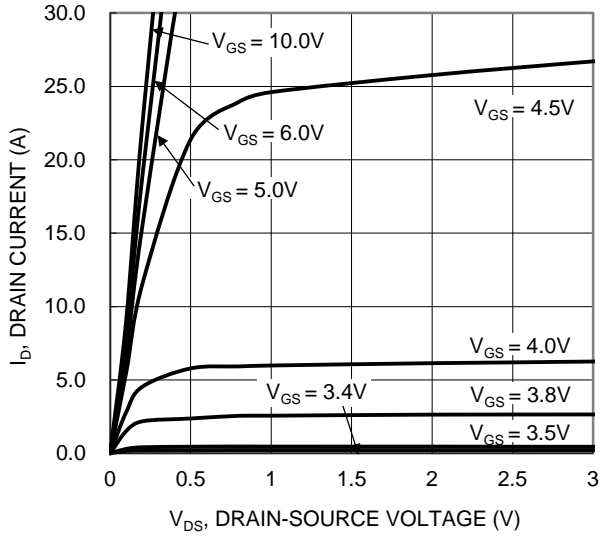


Figure 1. Typical Output Characteristic

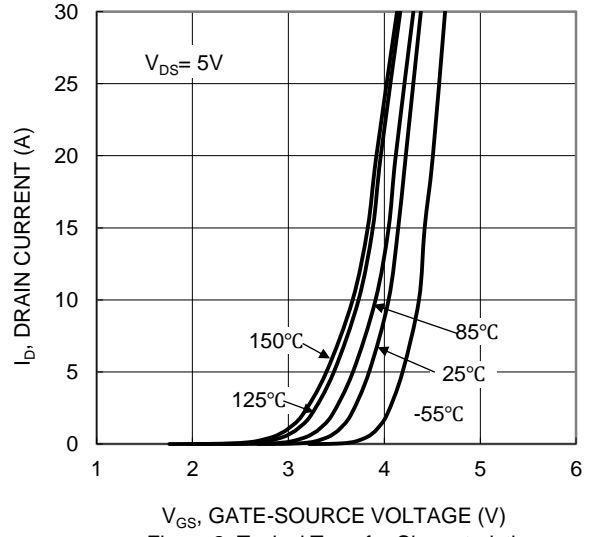


Figure 2. Typical Transfer Characteristic

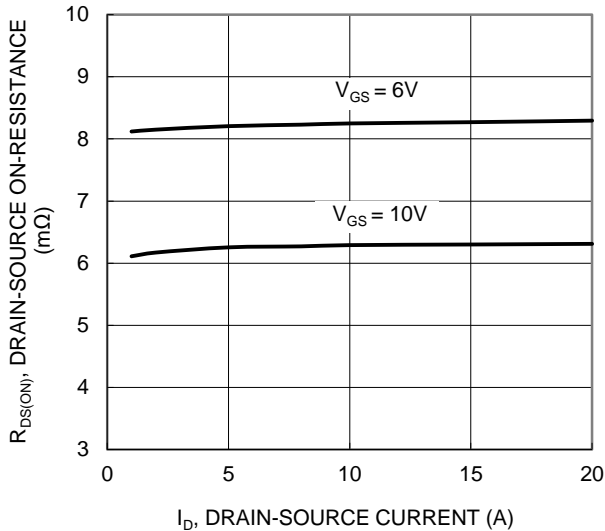


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

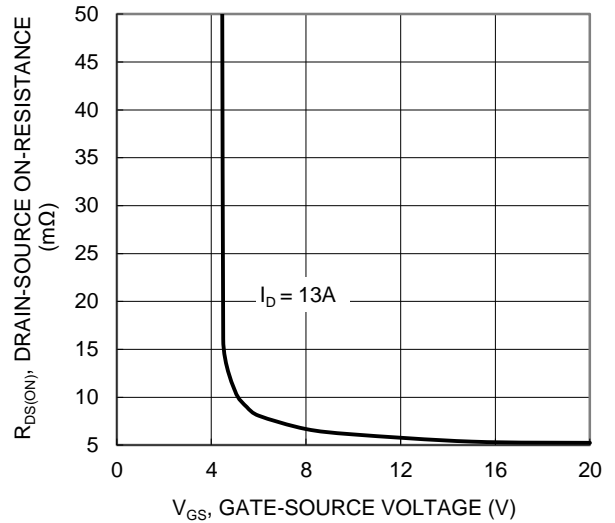


Figure 4. Typical Transfer Characteristic

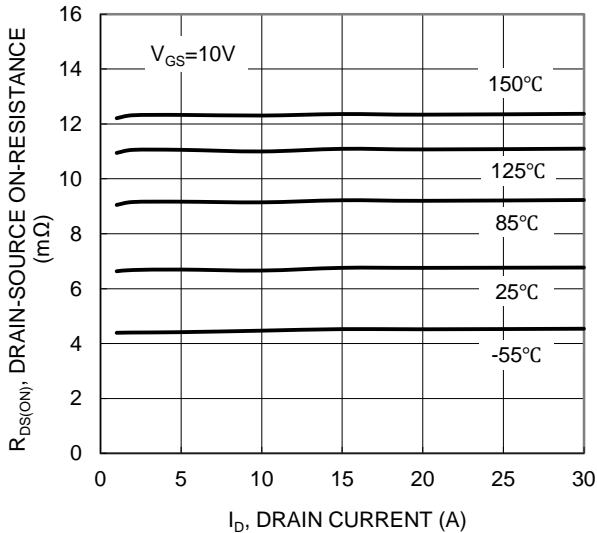


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

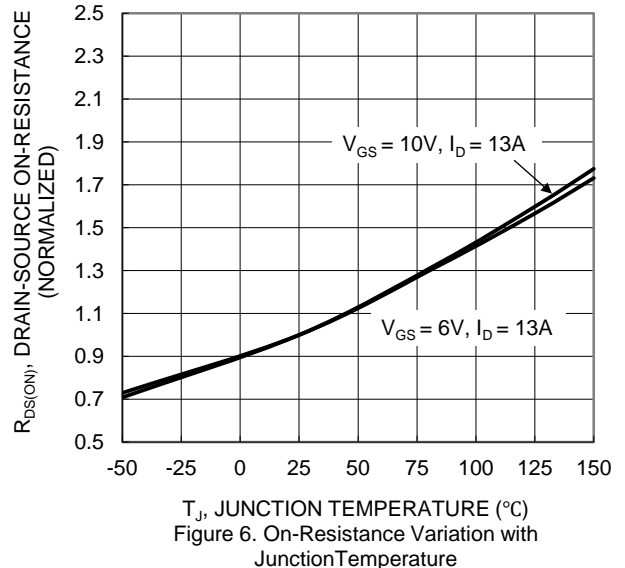


Figure 6. On-Resistance Variation with Junction Temperature

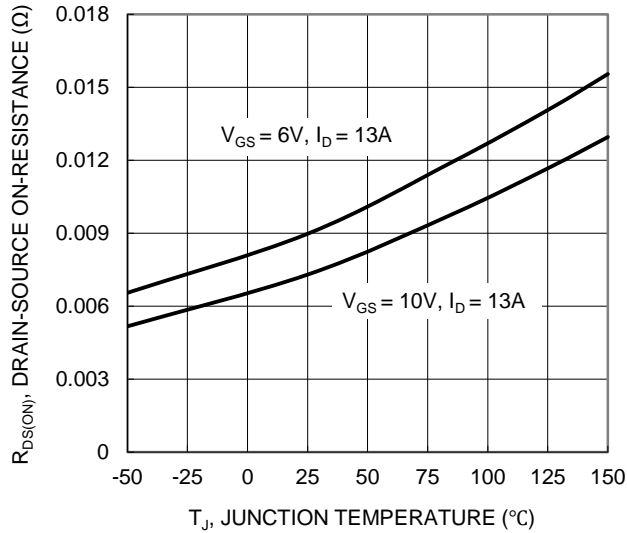


Figure 7. On-Resistance Variation with Junction Temperature

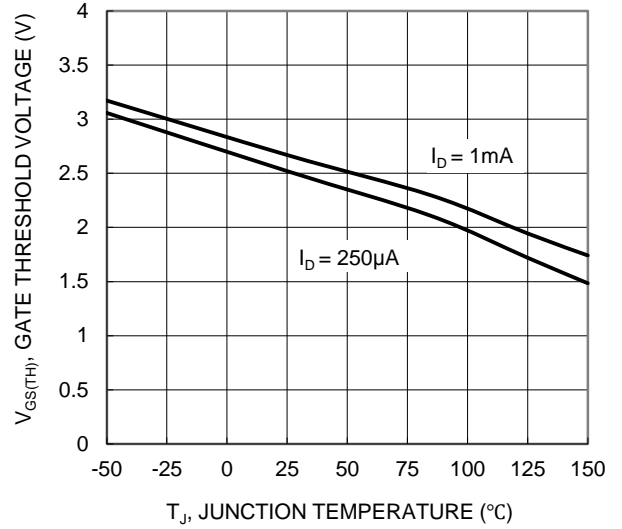


Figure 8. Gate Threshold Variation vs. Junction Temperature

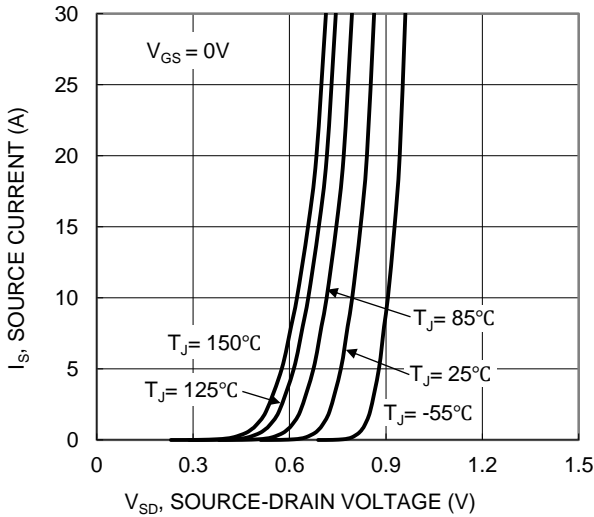


Figure 9. Diode Forward Voltage vs. Current

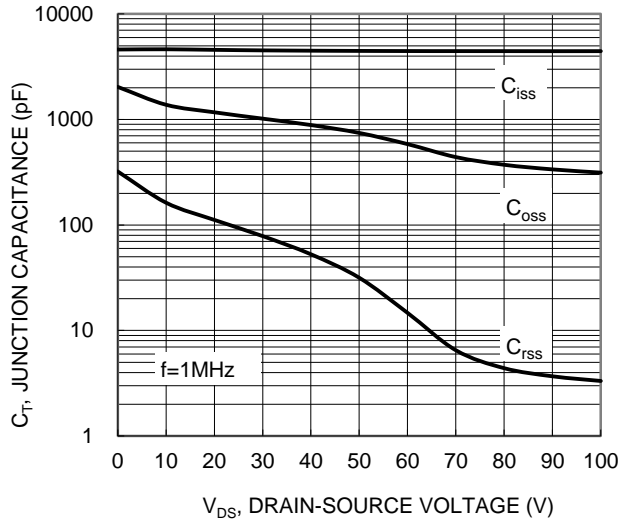


Figure 10. Typical Junction Capacitance

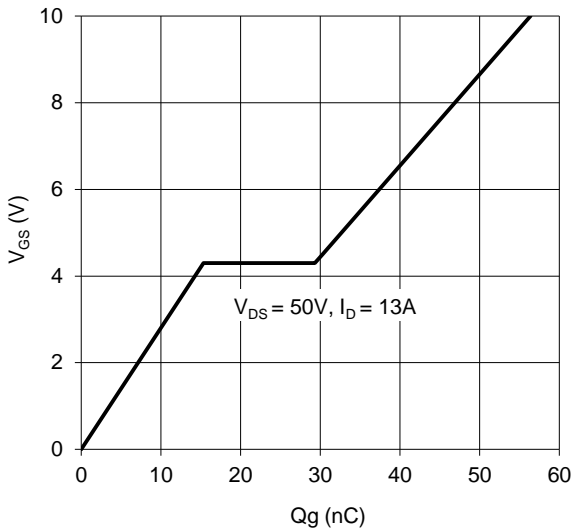


Figure 11. Gate Charge

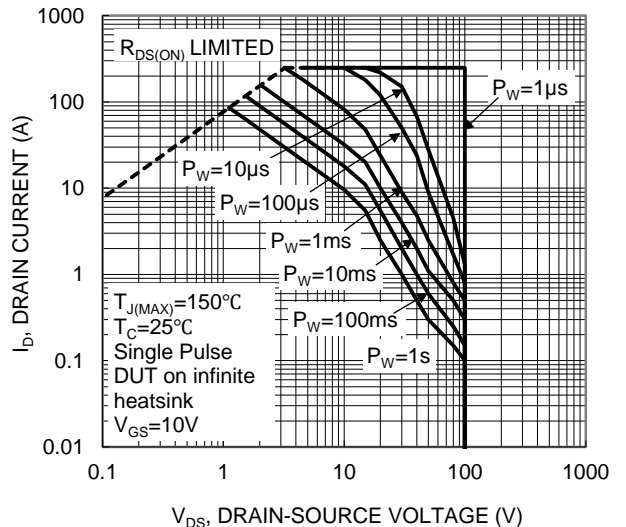


Figure 12. SOA, Safe Operation Area

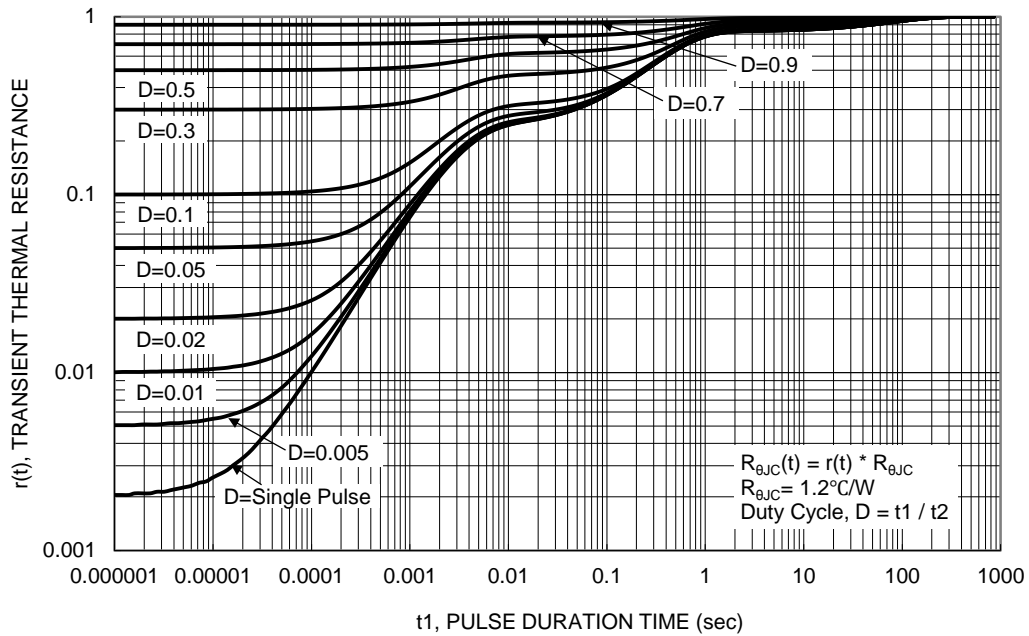
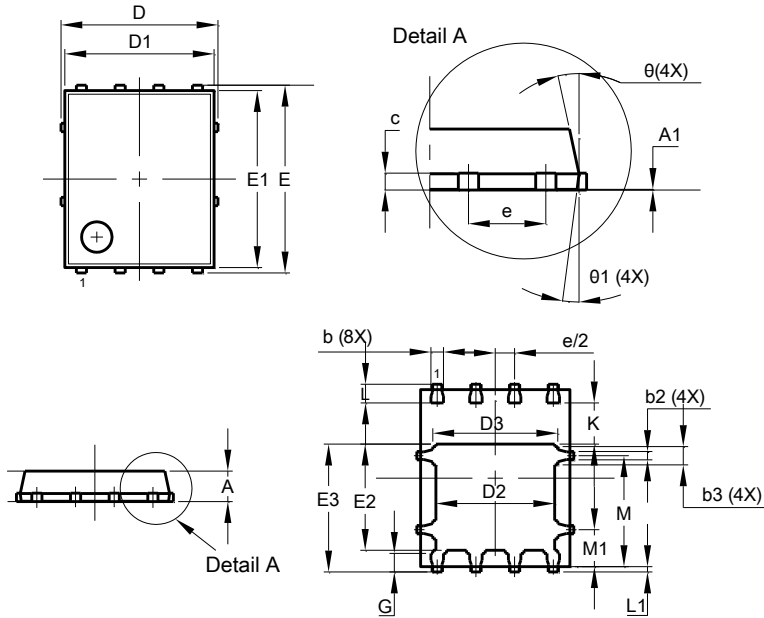


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5060-8**

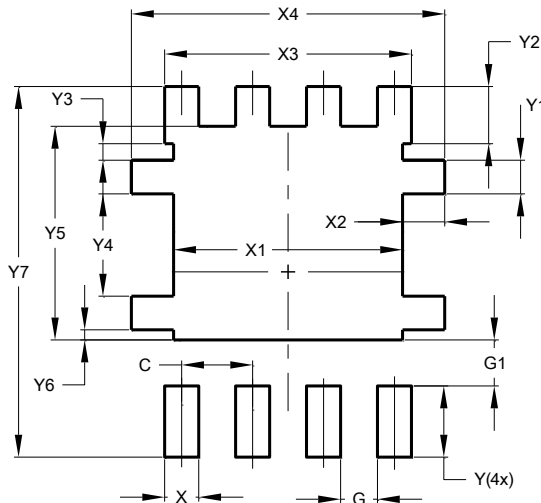


PowerDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	-
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	-	-
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
$\theta$	10°	12°	11°
$\theta1$	6°	8°	7°
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5060-8**



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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