

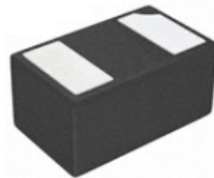
## Product Summary

<b>V<sub>BR</sub> (Min)</b>	<b>I<sub>PP</sub> (Max)</b>	<b>C<sub>T</sub> (Typ)</b>
8.2V	100A	700pF

## Description and Applications

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in:

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral



Bottom View

## Features and Benefits

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- One Channel of ESD Protection
- Low Channel Input Capacitance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: U-DFN1610-2 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.003 grams (Approximate)



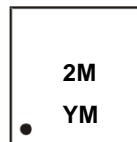
Device Schematic

## Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D8V0H1U2LP1610-7	Standard	2M	7	8	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, see <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



2M = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: F = 2018)  
 M = Month (ex: 9 = September)  
 Bar Denotes Pin1 or Cathode side

### Date Code Key

Year	2017	2018	2019	2020	2021	2022	2023
Code	E	F	G	H	I	J	K

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Condition
Peak Pulse Current	I <sub>PP</sub>	100	A	8/20μs (Note 7)
Peak Pulse Power Dissipation	P <sub>PP</sub>	1350	W	8/20μs (Note 7)
ESD Protection—Contact Discharge	V <sub>ESD_CONTACT</sub>	±30	kV	Standard IEC61000-4-2
ESD Protection—Air Discharge	V <sub>ESD_AIR</sub>	±30	kV	Standard IEC61000-4-2

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient, T <sub>A</sub> = +25°C	R <sub>θJA</sub>	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Standoff Voltage	V <sub>RWM</sub>	—	—	8.0	V	—
Channel Leakage Current (Note 6)	I <sub>R</sub>	—	—	1	μA	V <sub>R</sub> = 8.0V
Reverse Breakdown Voltage	V <sub>BR</sub>	8.2	—	9.0	V	I <sub>R</sub> = 1mA
Clamping Voltage, Positive Transients (Note 7)	V <sub>C</sub>	—	—	9.5	V	I <sub>PP</sub> = 10A, t <sub>P</sub> = 8/20μs
		—	—	11	V	I <sub>PP</sub> = 50A, t <sub>P</sub> = 8/20μs
		—	—	13.5	V	I <sub>PP</sub> = 100A, t <sub>P</sub> = 8/20μs
Channel Input Capacitance (Note 8)	C <sub>T</sub>	—	700	—	pF	V <sub>R</sub> = 0V, f = 1MHz, Any I/O to GND

- Notes:
5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
  6. Short duration pulse test used to minimize self-heating effect.
  7. Clamping voltage value is based on an 8 × 20μs peak pulse current (I<sub>PP</sub>) waveform.
  8. Measured from any I/O to GND.

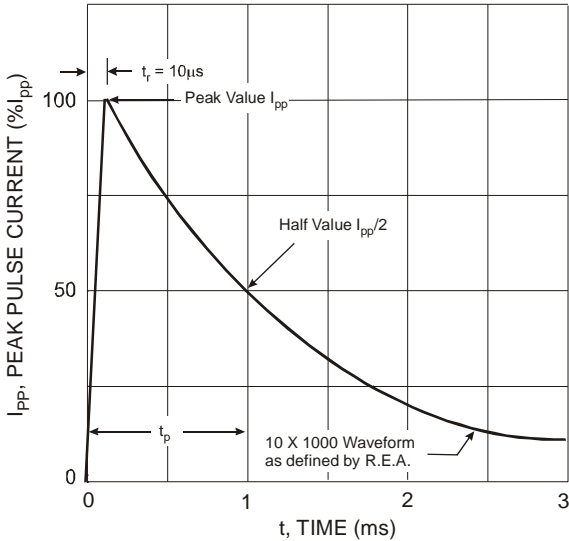


Figure 1 Pulse Waveform

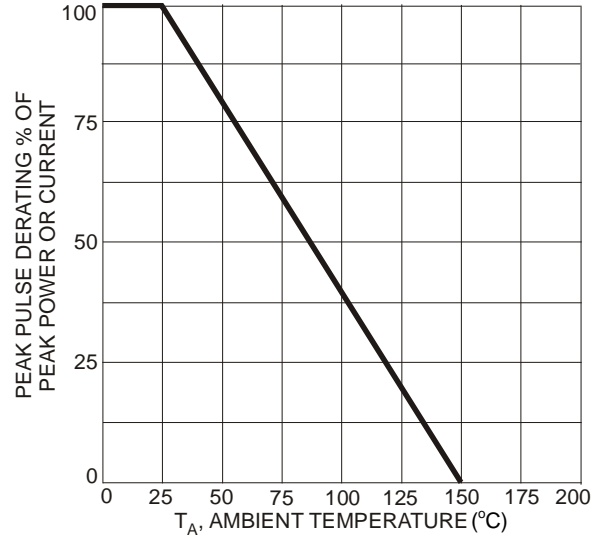


Figure 2 Pulse Derating Curve

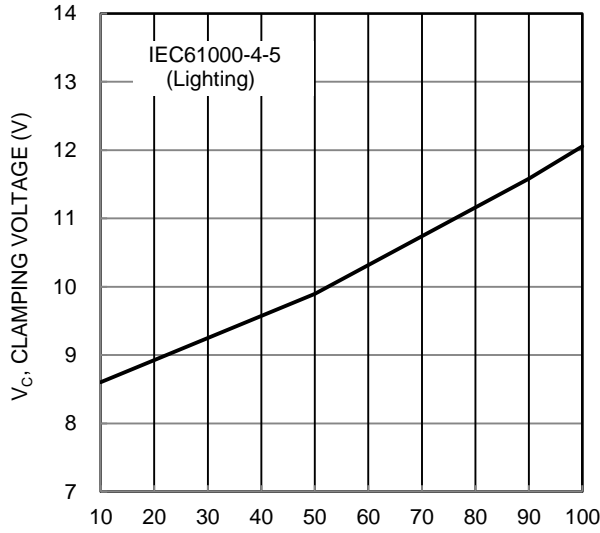


Figure 3 Clamping Voltage Characteristic

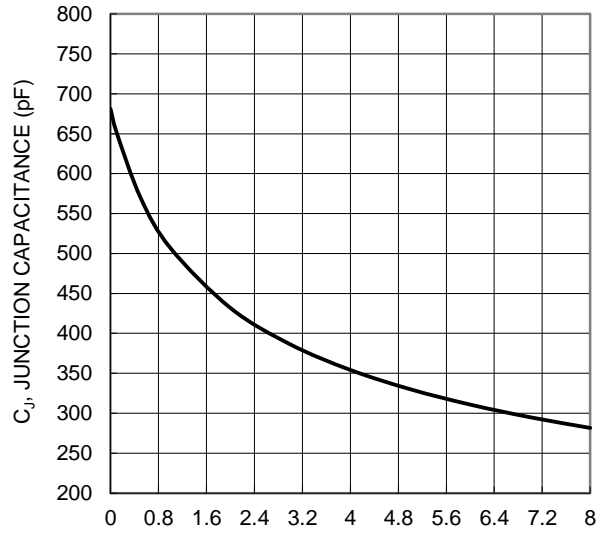
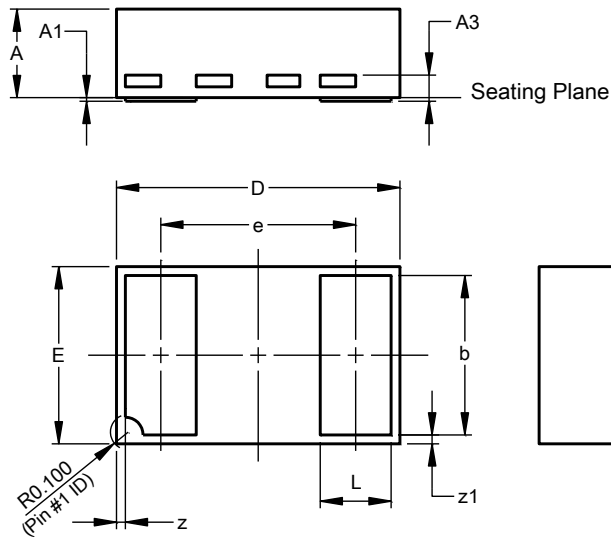


Figure 4. Typical Junction Capacitance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN1610-2 (Type B)**

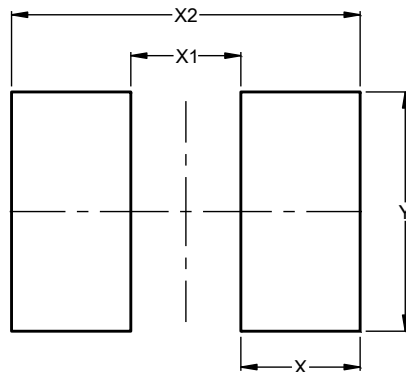


U-DFN1610-2 (Type B)			
Dim	Min	Max	Typ
A	0.45	0.55	0.50
A1	0.00	0.05	0.015
A3	-	-	0.127
b	0.85	0.95	0.90
D	1.55	1.65	1.60
E	0.95	1.05	1.00
e	-	-	1.10
L	0.35	0.45	0.40
z	0.050 REF		
z1	0.050 REF		
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN1610-2 (Type B)**



Dimensions	Value (in mm)
X	0.650
X1	0.600
X2	1.900
Y	1.300

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