



40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max (Note 9) T _C = +25°C
	3.0mΩ @ V _{GS} = 10V	100A
40V	5.0mΩ @ V _{GS} = 5V	93A

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching, Test in Production Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Ensures On-State Losses are Minimized
- Excellent Q_{GD} x R_{DS(ON)} Product (FOM)
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMTH43M8LFGQ)

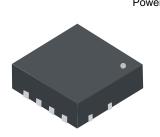
Description and Applications

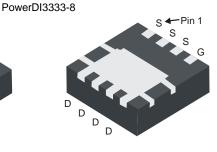
This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

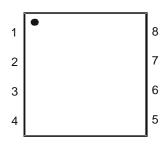
- DC-DC Converters
- Power Management

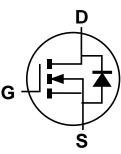
Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (Approximate)









Top View

Bottom View

Top View

Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH43M8LFG-7	PowerDI3333-8	2000/Tape & Reel
DMTH43M8LFG-13	PowerDI3333-8	3000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



HK8 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 = 2019) WW = Week Code (01 to 53)



Maximum Ratings (@ $T_C = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	40	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Notes 6 & 9) V _{GS} = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	l _D	100 85	А
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	I _D	24.0 16.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	400	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	Is	3.05	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle	I _{SM}	400	Α	
Avalanche Current, L = 1mH	I _{AS}	18.2	Α	
Avalanche Energy, L = 1mH	Eas	165	mJ	

Thermal Characteristics

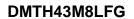
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25$ °C	P_{D}	2.62	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	57.8	°C/W	
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		P_{D}	65.2	W
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	2.3	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics (@TJ = +25°C, unless otherwise specified.)

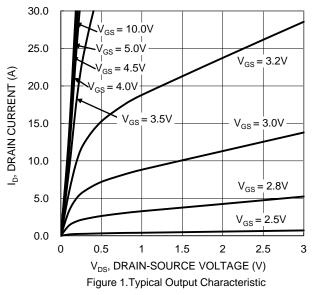
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage		_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						•	
Gate Threshold Voltage	V _{GS(TH)}	1	1.5	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	D	l	2.3	3.0		$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Nesistance	R _{DS(ON)}	_	3.4	5.0	mΩ	$V_{GS} = 5V, I_D = 15A$	
Chatia Duain Course On Bosistanes (T. 1475°C) (Note 0)	0	_	_	6.0	11122	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance (T _J = +175°C) (Note 8)	R _{DS(ON)}	_	_	9.0		$V_{GS} = 5V, I_D = 15A$	
Diode Forward Voltage	V _{SD}	_	0.8	1.0	V	V _G S = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)			•	•			
Input Capacitance	Ciss	_	2798	_		$V_{DS} = 20V$, $V_{GS} = 0V$, $f = 1MHz$	
Output Capacitance	Coss	_	904	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	88	_			
Gate Resistance	R_{G}	_	2.44	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_G	_	40.1	_			
Gate-Source Charge	Q _{GS}	_	5.2	_	nC	$V_{DS} = 20V, I_D = 20A, V_{GS} = 10V$	
Gate-Drain Charge	Q_{GD}	_	8.8	_			
Turn-On Delay Time	t _{D(ON)}	_	5.16	_		$V_{DD} = 20V, V_{GS} = 10V,$ $R_G = 1.6\Omega, I_D = 20A$	
Turn-On Rise Time	t _R	_	10.7	_			
Turn-Off Delay Time	t _{D(OFF)}	_	24.6	_	ns		
Turn-Off Fall Time	t _F	_	12.4	_			
Body Diode Reverse Recovery Time	t _{RR}	_	32.6	_	ns	1 454 174 40047	
Body Diode Reverse Recovery Charge	Q _{RR}	_	26.6	_	nC	$I_F = 15A$, di/dt = 100A/ μ s	

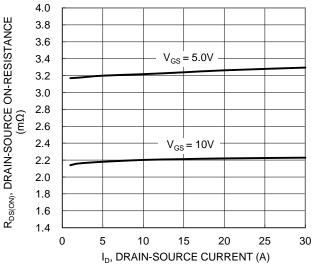
Notes:

- Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.
 Package limit.









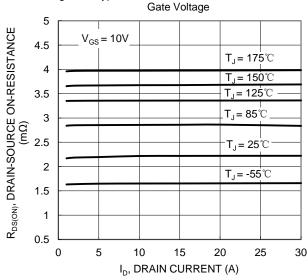


Figure 3. Typical On-Resistance vs. Drain Current and

Figure 5. Typical On-Resistance vs. Drain Current and Temperature

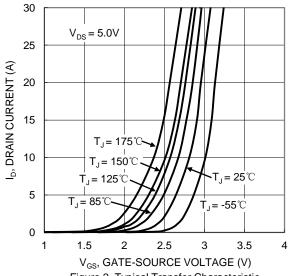


Figure 2. Typical Transfer Characteristic

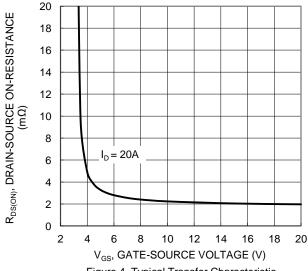


Figure 4. Typical Transfer Characteristic

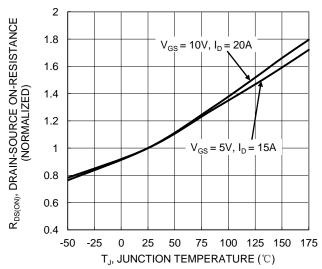


Figure 6. On-Resistance Variation with Temperature





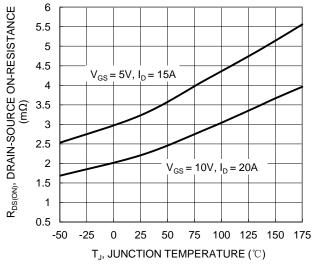
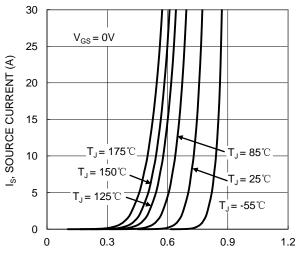


Figure 7. On-Resistance Variation with Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

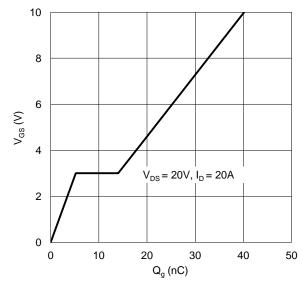


Figure 11. Gate Charge

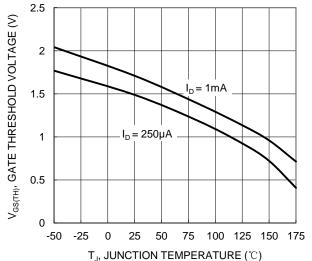
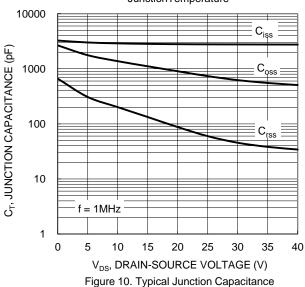


Figure 8. Gate Threshold Variation vs. JunctionTemperature



1000 $\begin{array}{c} R_{\text{DS(ON)}} \\ \text{Limited} \end{array}$ 100 ID, DRAIN CURRENT (A) 10 $P_W = 10 \text{ms}$ $P_{W} = 100 \text{ms}$ T_{J(MAX)} = 175°C T_C = 25°C Single Pulse 0.1 DUT on Infinite Heatsink $V_{GS} = 10 V$ 0.01 0.1 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area



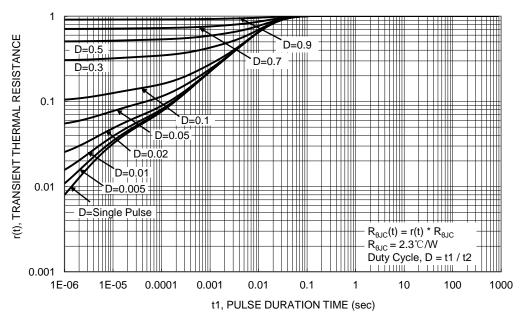


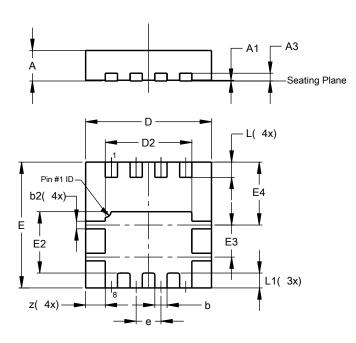
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8

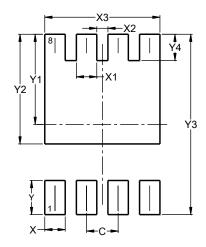


PowerDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3	_	_	0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
D	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
Е	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
е	_	_	0.65		
L	0.35	0.45	0.40		
L1	_	_	0.39		
Z	_	_	0.515		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



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