

600V High and Low Side Driver

PRODUCT SUMMARY

V_{OFFSET} 600 V max.
I_O+/- (typ.) 450mA / 950mA
V_{OUT} 10 V - 20 V
t_{on/off} (typ.) 150ns / 220ns

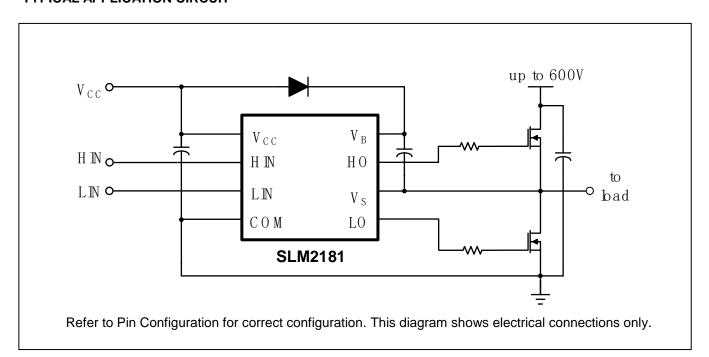
GENERAL DESCRIPTION

The SLM2181 is a high voltage, high speed power MOSFET and IGBT drivers with independent highlow-side referenced output Proprietary HVIC and latch immune **CMOS** ruggedized technologies enable monolithic construction. The logic input is compatible with standard CMOS or LSTTL output, down to 3.3 V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross conduction. Propagation delays are matched to simplify use in high frequency applications. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high-side configuration which operates up to 600 V.

FEATURES

- Floating channel designed for bootstrap operation
- Fully operational to +600 V
- Low Vcc operation
- Tolerant to negative transient voltage, dV/dt immune
- Gate drive supply range from 10 V to 20 V
- Under-Voltage lockout for both channels
- 3.3 V, and 5 V logic compatible
- CMOS Schmitt-triggered inputs with pull-down
- Matched propagation delay for both channels
- Outputs in phase with inputs
- RoHS compliant
- SOP-8 package

TYPICAL APPLICATION CIRCUIT





PIN CONFIGURATION

Package	Pin Configuration (Top View)			
	1	H IN	V _B	8
SOP-8	2	LIN	НО	7
	3	COM	V _S	6
	4	LO	V _{CC}	5

PIN DESCRIPTION

No.	Pin	Description
1	HIN	Logic input for high-side gate driver output (HO), in phase
2	LIN	Logic input for low-side gate driver output (LO), in phase
3	COM	Low-side return
4	LO	Low-side gate drive output
5	Vcc	Low-side and logic fixed supply
6	Vs	High-side floating supply return
7	НО	High-side gate drive output
8	V _B	High-side floating supply

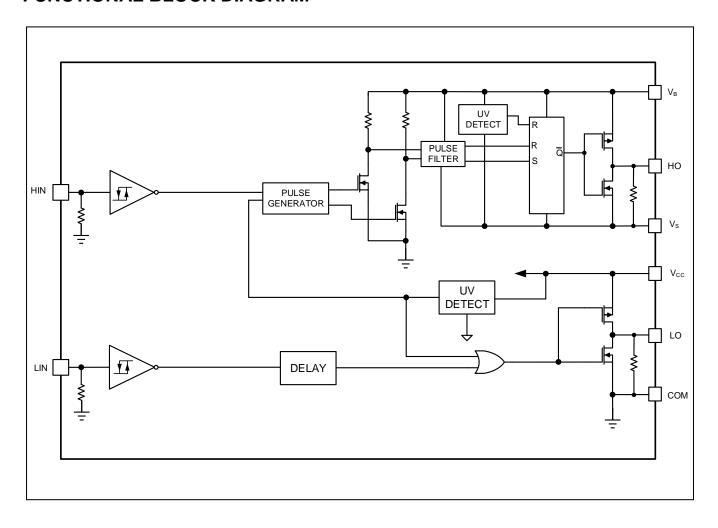
ORDERING INFORMATION

Industrial Range: -40°C to +125°C

Order Part No.	Package	QTY
SLM2181CA-DG	SOP8, Pb-Free	2500/Reel



FUNCTIONAL BLOCK DIAGRAM





ABSOLUTE MAXIMUM RATINGS

Symbol	Definition	Min.	Max.	Units
V _B	High-side floating absolute voltage	-0.3	625	
Vs	High-side floating supply offset voltage	V _B - 25	V _B + 0.3	
V _{но}	High-side floating output voltage	Vs - 0.3	V _B + 0.3	V
Vcc	Low-side and logic fixed supply voltage	-0.3	25	, v
VLO	Low-side output voltage	-0.3	Vcc + 0.3	
Vin	Logic input voltage (HIN & LIN)	-0.3	Vcc + 0.3	
dVs/dt	Allowable offset supply voltage transient		50	V/ns
P _D	Package power dissipation @ T _A ≤ +25°C		0.625	W
θ_{JA}	Thermal resistance, junction to ambient		200	°C/W
TJ	Junction temperature		150	
Ts	Storage temperature	-55	150	°C
T∟	Lead temperature (soldering, 10 seconds)		300	

Note: Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

RECOMMENDED OPERATION CONDITIONS

Symbol	Definition	Min.	Max.	Units
V _B	High-side floating absolute voltage	Vs + 10	Vs + 20	
Vs	High-side floating supply offset voltage		600	
V _{но}	High-side floating output voltage	Vs	V _B	V
Vcc	Low-side and logic fixed supply voltage	10	20	v
VLO	Low-side output voltage	0	Vcc	
VIN	Logic input voltage (HIN & LIN)	COM	Vcc	
TA	Ambient temperature	- 40	125	°C

Note: The input/output logic timing diagram is shown in Figure 1. For proper operation the device should be used within the recommended conditions. The V_S offset rating is tested with all supplies biased at a 15 V differential.



DYNAMIC ELECTRICAL CHARACTERISTICS

 V_{BIAS} (V_{CC} , V_{BS}) = 15 V, C_L = 1000 pF and T_A = 25°C unless otherwise specified.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
ton	Turn-on propagation delay	Vs = 0 V		150	300	
t _{off}	Turn-off propagation delay	Vs = 0V		220	400	
tr	Turn-on rise time			45	80	ns
t _f	Turn-off fall time			17	40	
MT	Delay matching, HS & LS turn-on/off				35	

STATIC ELECTRICAL CHARACTERISTICS

 V_{BIAS} (V_{CC} , V_{BS}) = 15 V and T_A = 25°C unless otherwise specified. The V_{IN} , V_{TH} , and I_{IN} parameters are referenced to COM and are applicable to all logic input leads: HIN and LIN. The V_O and I_O parameters are referenced to COM and are applicable to the respective output leads: HO or LO.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
VIH	Logic "1" input voltage	Vcc = 10 V to 20V	2.5			
VIL	Logic "0" input voltage	1 00 10 10 20 1			0.8	V
Vон	High level output voltage, V _{BIAS} - V _O	I _O = 2 mA		0.05	0.2	v
Vol	Low level output voltage, Vo	10 – 2 111/4		0.02	0.15	
I _{LK}	Offset supply leakage current	V _B = V _S = 600 V			50	
I _{QBS}	Quiescent V _{BS} supply current	V _{IN} = 0 V		60	78	
lacc	Quiescent Vcc supply current	VIN — U V		220	300	μΑ
I _{IN+}	Logic "1" input bias current	HIN=LIN = 5V		8	15	
I _{IN} -	Logic "0" input bias current	HIN=LIN= 0V			5	
V _{CCUV+}	Vcc supply undervoltage positive going threshold		7.2	8.0	8.9	V
Vccuv-	V _{CC} supply undervoltage negative going threshold		6.4	7.4	8.0	V
V _{BSUV+}	V _{BS} supply undervoltage positive going threshold		6.4	7.2	8.0	V
V _{BSUV} -	V _{BS} supply undervoltage negative going threshold		5.8	6.6	7.4	·
I _{O+}	Output high short circuit pulsed current	$V_0 = 0V$, $V_{IN} = Logic$ "1", $PW \le 10 \mu s$	320	450		mA
l _O -	Output low short circuit pulsed current	V_0 = 15V, V_{IN} = Logic "0", PW \leq 10 μs	680	950		1117.



SWITCHING AND TIMING RELATIONSHIPS

The relationships between the input and output signals of the SLM2181 are illustrated Figure 1 and Figure 2. These figures show the definitions of several timing parameters (i.e., t_{on} , t_{off} , t_r , and t_f) associated with this device.

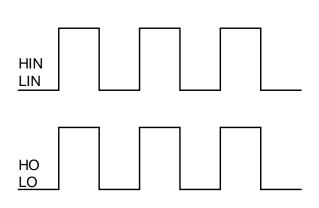


Figure 1. Input/Output Timing Diagram

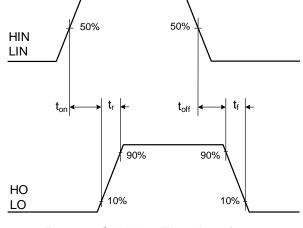


Figure 2. Switching Time Waveform

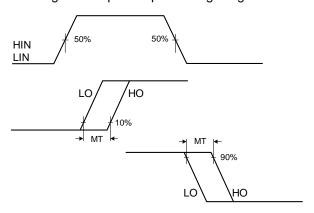
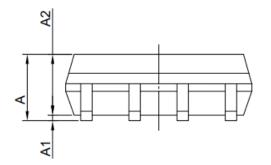
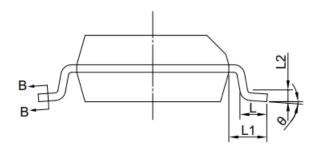


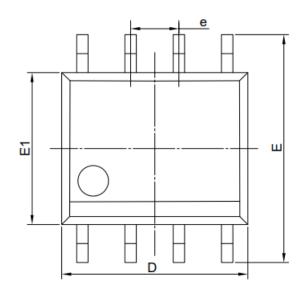
Figure 3. Delay Matching Waveform

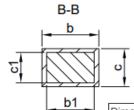


PACKAGE CASE OUTLINES









Dimension	MIN	NOM	MAX	
Α	-	-	1.75	
A1	0.1	ı	0.25	
A2	1.25	ı	-	
L	0.4	0.835	1.27	
L1	-	1.04	-	
L2	-	0.25	-	
θ	0	-	8	
b	0.31	-	0.51	
b1	0.28	ı	0.48	
С	0.1	1	0.25	
c1	0.1	-	0.25	
D	-	4.9	-	
E	-	6	-	
E1	-	3.9	-	
е	1.27 BSC			
Unit : mm				

Figure 4. SOP8 Outline Dimensions



REVISION HISTORY

Note: page numbers for previous revisions may differ from page numbers in current version

Page or Item	Subjects (major changes since previous revision)				
Rev 0.1 datasheet, 2020-1-10	Rev 0.1 datasheet, 2020-1-10				
Whole document	Draft datasheet released				
Rev 1.0 datasheet, 2021-11-1	0				
Whole datasheet	Update the Logo and format				
Page 2	Remove the SLM2181CA-TG from the ordering information				
Page 3	Update the function block diagram				
Page 5	Update the ton, toff, tr and tf in the dynamic electrical characteristics				
	Update the V _{OH} , I _{QBS} , I _{IN+} , V _{CCUV-} , I _{O+} and I _{O-} in the static electrical characteristics				
Rev 1.1 Datasheet, 2022-12-29					
Page 7	SOP8 Outline Dimensions Update				