

## FEATURES

- Average Forward Current: I<sub>F(AV)</sub>=1A
- Polarity: Color band denotes cathode

## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)			
RS1A-RS1M	SMA	RS1 <b>*</b>	2000			





★:From A-M

# MAXIMUM RATINGS (Ta=25 unless otherwise noted)

		Unit		RS1							
Item	Symbol		Test Conditions	Α	В	D	G	J	Κ	М	
Repetitive Peak Reverse Voltage	$V_{RRM}$	V		50	100	200	400	600	800	1000	
Maximum RMS Voltage	V <sub>RMS</sub>	V		35	70	140	280	420	560	700	
Average Forward Current	I <sub>F(AV)</sub>	A	60Hz Half-sine wave , Resistance load , TL=75 $^\circ$	1.0							
Surge(Non-repetitive)Forward Current	I <sub>FSM</sub>	A	60Hz Half-sine wave, 1 cycle,Ta=25℃	30							
Operation Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	Ĉ		-55 ~ +150							

# ELECTRICAL CHARACTERISTICS (Ta=25 unless otherwise specified)

				RS1								
ltem	Symbol	Unit	Test Condition			В	D	G	J	κ	Μ	
Peak Forward Voltage	$V_{F}$	V	I <sub>F</sub> =1.0A			1.3						
Maximum reverse recovery time	trr	ns	I <sub>F</sub> =0.5A,I <sub>R</sub> =1.0A,I <sub>m</sub> =0.25A			150				500		
Peak Reverse Current	I <sub>RRM1</sub>	μA	$V_{RM} = V_{RRM}$	T <sub>a</sub> =25℃				5				
	I <sub>RRM2</sub>			Ta=125℃	50							
Thermal	$R_{\theta_{J}-A}$	°C/W	Between juncti	on and ambient	55							
Resistance(Typical)	$R_{ extsf{ hetaJ-L}}$	0, 11	Between juncti	on and terminal	25							

#### Notes:

Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pad areas



# **Typical Characteristics**

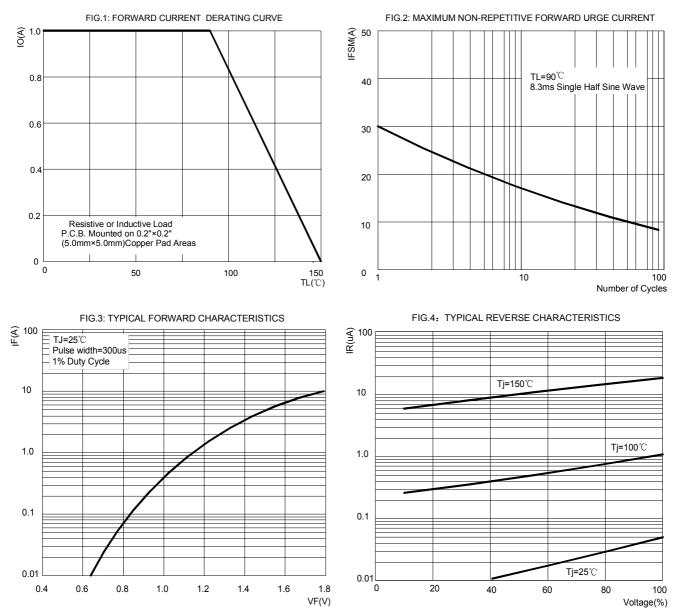
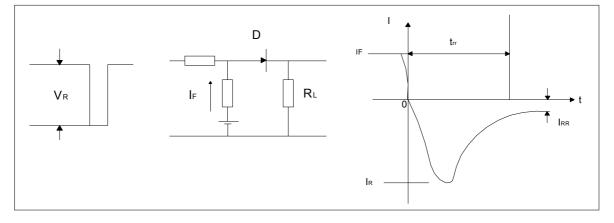
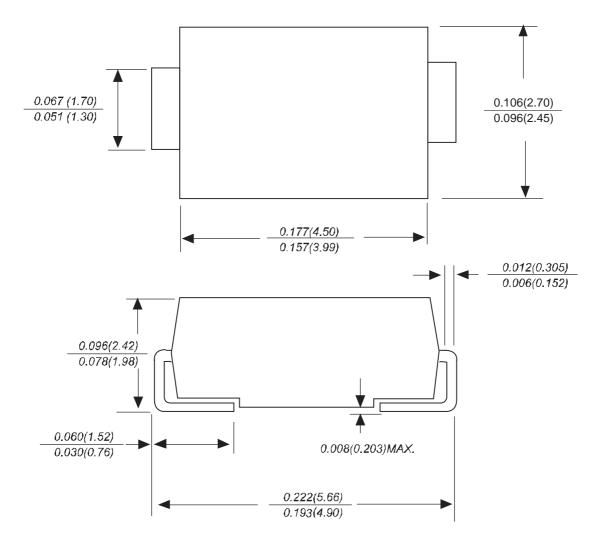


FIG.5: Diagram of circuit and Testing wave form of reverse recovery time





### **SMA Package Outline Dimensions**



Dimensions in inches and (millimeters)



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give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

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