



## U74AHC14

CMOS IC

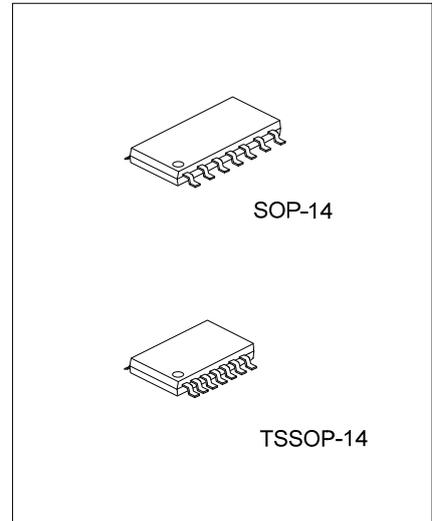
### HEX SCHMITT-TRIGGER INVERTERS

#### DESCRIPTION

The **U74AHC14** is hex Schmitt-trigger inverters and each inverter provides the function  $Y = \overline{A}$ .

#### FEATURES

- \* Operation Voltage Range: 2V~5.5V
- \* Max  $t_{pd}$  of 8.6ns at 5 V( $C_L=15pF$ )
- \* High Noise Immunity
- \* Low Power Dissipation

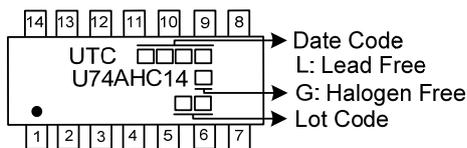


#### ORDERING INFORMATION

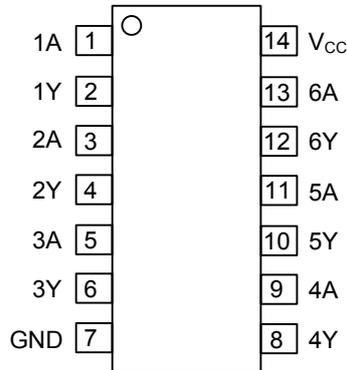
Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74AHC14L-S14-R	U74AHC14G-S14-R	SOP-14	Tape Reel
U74AHC14L-P14-R	U74AHC14G-P14-R	TSSOP-14	Tape Reel

<p>U74AHC14G-S14-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) S14: SOP-14, P14: TSSOP-14</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



■ PIN CONFIGURATION

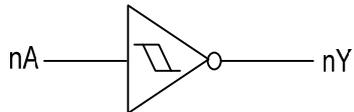


■ FUNCTION TABLE (each gate)

INPUT A	OUTPUT Y
L	H
H	L

Note: H: HIGH voltage level; L: LOW voltage level.

■ LOGIC SYMBOL



■ ABSOLUTE MAXIMUM RATING (unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{CC}$	-0.5 ~ +7	V
Input Voltage	$V_{IN}$	-0.5 ~ +7	V
Output Voltage	$V_{OUT}$	-0.5 ~ $V_{CC} + 0.5$	V
Input Clamp Current	$I_{IK}$	-20	mA
Output Clamp Current	$I_{OK}$	±20	mA
Output Sink Current	$I_{OUT}$	±25	mA
$V_{CC}$ or GND Current	$I_{CC}$	±50	mA
Storage Temperature	$T_{STG}$	-65 ~ + 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Thermal Resistance Junction-Ambient	SOP-14	86	°C/W
	TSSOP-14	113	°C/W

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	$V_{CC}$		2.0		5.5	V
Input Voltage	$V_{IN}$		0		5.5	V
Output Voltage	$V_{OUT}$		0		$V_{CC}$	V
High-Level Input Current	$I_{OH}$	$V_{CC}=2V$			-50	μA
		$V_{CC}=3.3V\pm 0.3V$			-4	mA
		$V_{CC}=5V\pm 0.5V$			-8	mA
Low-Level Input Current	$I_{OL}$	$V_{CC}=2V$			50	μA
		$V_{CC}=3.3V\pm 0.3V$			4	mA
		$V_{CC}=5V\pm 0.5V$			8	mA
Operating Temperature	$T_A$		-40		+85	°C

### ■ STATIC CHARACTERISTICS (T<sub>A</sub>=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Positive-going Input Threshold Voltage	V <sub>T+</sub>	V <sub>CC</sub> =3.0V		1.2		2.2	V
		V <sub>CC</sub> =4.5V		1.75		3.15	V
		V <sub>CC</sub> =5.5V		2.15		3.85	V
Negative-going Input Threshold Voltage	V <sub>T-</sub>	V <sub>CC</sub> =3.0V		0.9		1.9	V
		V <sub>CC</sub> =4.5V		1.35		2.75	V
		V <sub>CC</sub> =5.5V		1.65		3.35	V
Hysteresis(V <sub>T+</sub> -V <sub>T-</sub> )	ΔV <sub>T</sub>	V <sub>CC</sub> =3.0V		0.3		1.2	V
		V <sub>CC</sub> =4.5V		0.4		1.4	V
		V <sub>CC</sub> =5.5V		0.5		1.6	V
High-Level Output Voltage	V <sub>OH</sub>	I <sub>OH</sub> =-50μA	V <sub>CC</sub> =2.0V	1.9			V
			V <sub>CC</sub> =3.0V	2.9			V
			V <sub>CC</sub> =4.5V	4.4			V
		I <sub>OH</sub> =-4 mA	V <sub>CC</sub> =3.0V	2.58			V
		I <sub>OH</sub> =-8mA	V <sub>CC</sub> =4.5V	3.94			V
Low-Level Output Voltage	V <sub>OL</sub>	I <sub>OL</sub> =50μA	V <sub>CC</sub> =2.0V			0.1	V
			V <sub>CC</sub> =3.0V			0.1	V
			V <sub>CC</sub> =4.5V			0.1	V
		I <sub>OL</sub> =4 mA	V <sub>CC</sub> =3.0V			0.36	V
		I <sub>OL</sub> =8mA	V <sub>CC</sub> =4.5V			0.36	V
Input Leakage Current	I <sub>I(LEAK)</sub>	V <sub>CC</sub> =0V to 5.5V, V <sub>IN</sub> =5.5V or GND				±0.1	μA
Quiescent Supply Current	I <sub>Q</sub>	V <sub>CC</sub> =5.5V, V <sub>IN</sub> =V <sub>CC</sub> or GND, I <sub>OUT</sub> =0				2	μA
Input Capacitance	C <sub>I</sub>	V <sub>IN</sub> =V <sub>CC</sub> or GND			2	10	pF

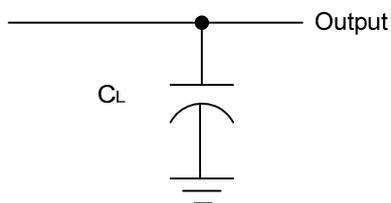
### ■ SWITCHING CHARACTERISTICS (T<sub>A</sub>=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation Delay, From Input(A) To Output(Y)	t <sub>PLH</sub> /t <sub>PHL</sub>	V <sub>CC</sub> =3.3±0.3 V	C <sub>L</sub> =15 pF		8.3	12.8	ns
			C <sub>L</sub> =50 pF		10.8	16.3	ns
	t <sub>PLH</sub> /t <sub>PHL</sub>	V <sub>CC</sub> =5.0±0.5 V	C <sub>L</sub> =15 pF		5.5	8.6	ns
			C <sub>L</sub> =50 pF		7	10.6	ns

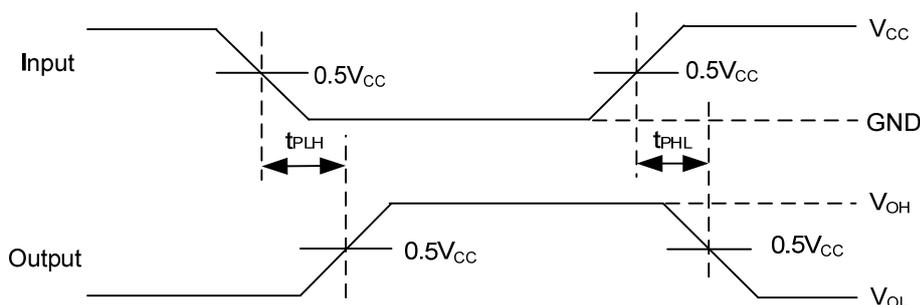
### ■ OPERATING CHARACTERISTICS (T<sub>A</sub>=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C <sub>PD</sub>	No Load, f=1MHz		9		pF

■ TEST CIRCUIT AND WAVEFORMS



Test circuit for measuring propagation delay



Waveforms showing the Input(A) to Output(Y) propagation delays

Note:  $C_L$  includes probe and jig capacitance.

All input pulses are supplied by generators having the following characteristics: PRR  $\leq 1\text{MHz}$ ,  $Z_o = 50\Omega$ ,  $t_r \leq 3\text{ns}$ ,  $t_f \leq 3\text{ns}$ .

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