

Features

I Wide Supply Voltage Range

I Single Supply: 2.0V to 36V

I Dual Supplies: ±1.0V to ±18V

I Low Supply Current at VCC=5V: 0.4mA

I Low Input Bias Current: 25nA (Typ)

Low Input Offset Current: 5nA (Typ)

Low Input Offset Voltage: ±1mV (Typ)

I Input Common Mode Voltage Range Includes

Ground

I Differential Input Voltage Range Equals to the

Power Supply Voltage

I Low Output Saturation Voltage: 200mV at 4mA

I Open Collector Output

Applications

I Battery Charger

I Cordless Telephone

I Switching Power Supply

DC-DC Module

I PC Motherboard

I Communication Equipment

Pin Configuration

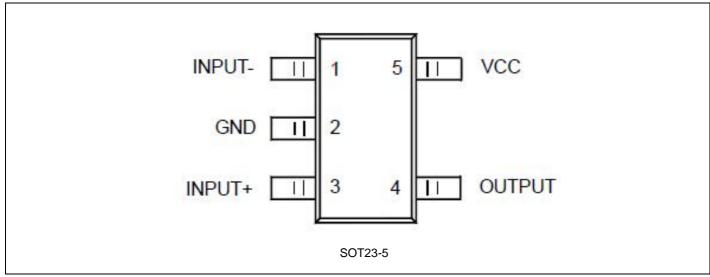


Figure 1. Pin Assignment Diagram



Functional Block Diagram

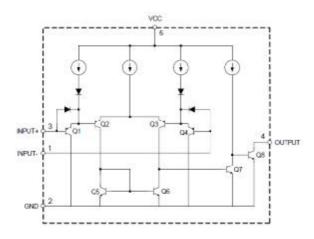


Figure 2. Functional Block Diagram of TS391IL

Absolute Maximum Ratings

Condition	Symbol	Max
Power Supply Voltage	Vcc	± 20 V or 40V
Differential input voltage	V _{I(DIFF)}	40V
Input Voltage	Vı	-0.3V~40V
Operating Junction Temperature	TJ	150°C
Storage Temperature Range	Tstg	-65°C ~+150°C

Package/Ordering Information

MODEL	CHANNEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION
TS391	Single	TS391IL	SOT23-5	Tape and Reel,3000	391



Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Supply Voltage	V _{CC}	2	36	V
Operating Temperature Range	TA	-40	85	°C

Electrical Characteristics

VCC=5V, GND=0V, TA=25oC, unless otherwise specified. Bold typeface applies over TA=-40 to 85oC (Note 3)

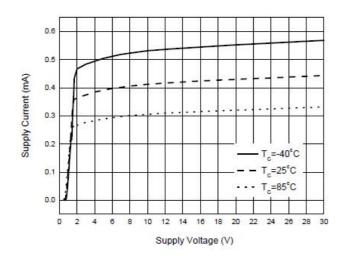
Parameter	Symbol	Conditions		Min	Тур	Max	Unit
I Off V-1	Vos	V _{OUT} =1.4V, V _{CC} =5 to 30V			1	5	37
Input Offset Voltage	VOS					7.0	mV
Innut Bire Comment	T	I_B I_{IN}^+ or I_{IN}^- with output in linear range, $V_{CM}^{=0V}$			25	250	пA
Input Bias Current	1B					400	
Input Offset Current	I _{IO}	I _{IN} +-I _{IN} -, V _{CM} =0V			5	50	пA
input Offset Current	1O			Y		200	
Input Common Mode Voltage Range (Note 4)		V _{CC} =30V		0		V _{CC} -1.5	V
Supply Current		$R_{L}=\infty$	V _{CC} =5V		0.4	1.0	mA
	I_{CC}					2.0	
	-CC		V _{CC} =30V		0.5	1.7	
						3.0	
Voltage Gain	G_{V}	V_{CC} =15V, R_L ≥15k Ω , V_{OUT} =1 to 11V		50	200		V/mV
Large Signal Response Time		V _{IN} =TTL Logic Swing, R _L =5.1kΩ			200		ns
Response Time	5	$R_L=5.1k\Omega$			1.3		μs
Output Sink Current	I _{SINK}	V _{IN} -=1V, V _{IN} +=0V, V _{OUT} =1.5V		6.0	16		mA
Output Leakage Current	•	V _{IN} -=0V, V _{IN} +=1V, V _{OUT} =5V			0.1		nA
	I _{LEAK}	V _{IN} -=0V, V _{IN} +	=1V, V _{OUT} =30V]	1	μА	
* ************************************	17	V _{IN} -=1V, V _{IN} +=0V, I _{SINK} ≤4mA		Y	200	400	mV
Saturation Voltage	V_{SAT}					500	

Note 3: These specifications are limited to -40oC≤TA≤85oC. Limits over temperature are guaranteed by design, but not tested in production.

Note 4: The input common mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at 25°C). The upper end of the common mode voltage range is VCC-1.5V (at 25°C), but either or both inputs can go to 18V without damages, independent of the magnitude of the VCC.



Typical Performance characteristics



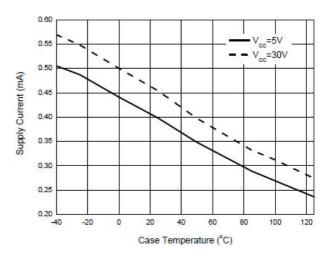
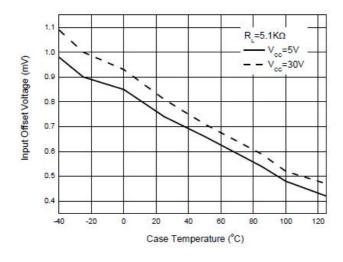


Figure 4. Supply Current vs. Supply Voltage





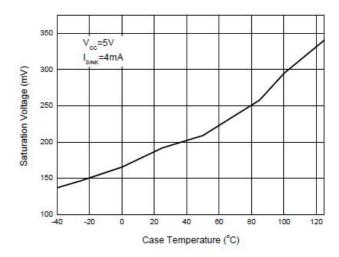
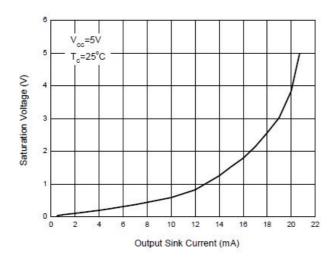


Figure 6. Input Offset Voltage vs. Case Temperature

Figure 7. Saturation Voltage vs. Case Temperature





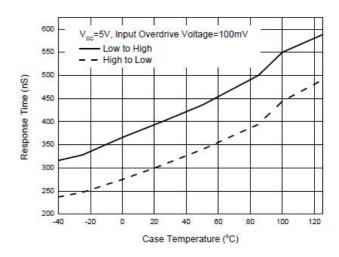
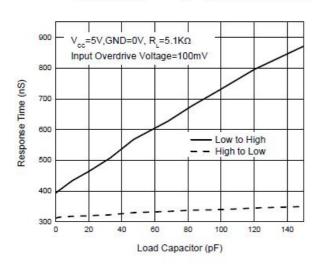


Figure 8. Saturation Voltage vs. Output Sink Current





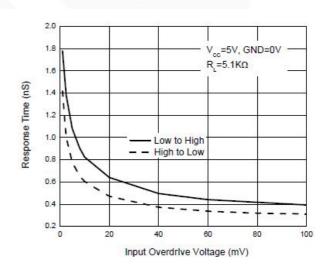
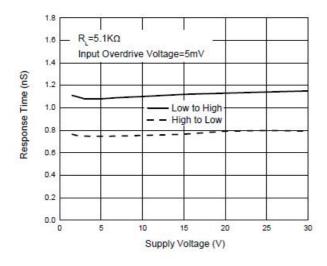


Figure 10. Response Time vs. Load Capacitor

Figure 11. Response Time vs. Input Overdrive Voltage





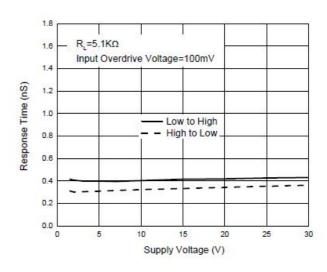
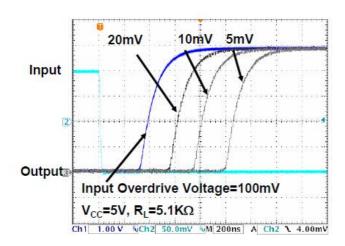


Figure 12. Response Time vs. Supply Voltage

Figure 13. Response Time vs. Supply Voltage



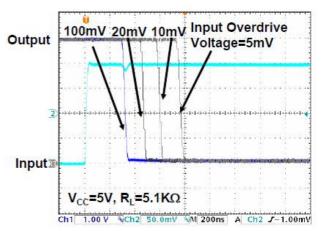
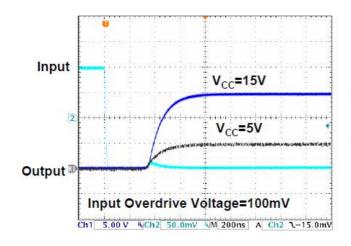


Figure 14. Response Time for Positive Transition

Figure 15. Response Time for Negative Transition





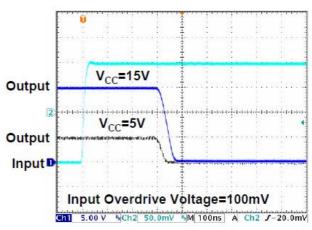
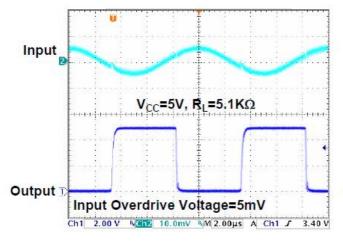


Figure 16. Response Time for Positive Transition

Figure 17. Response Time for Negative Transition



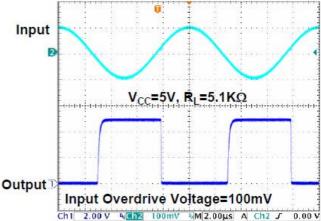


Figure 18. 100kHz Response

Figure 19. 100kHz Response



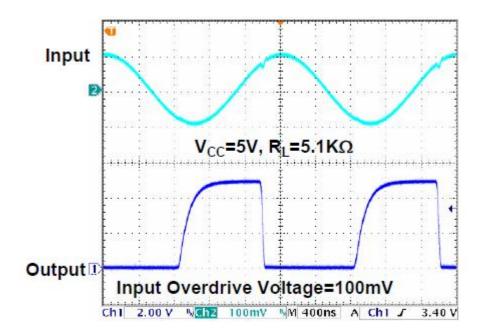
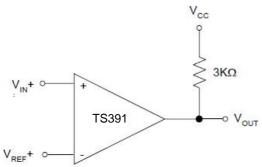


Figure 20. 500kHz Response



Typical Applications



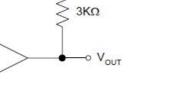


Figure 21. Basic Comparator

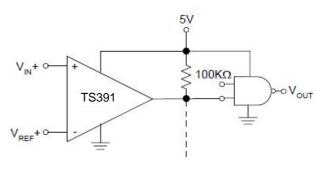
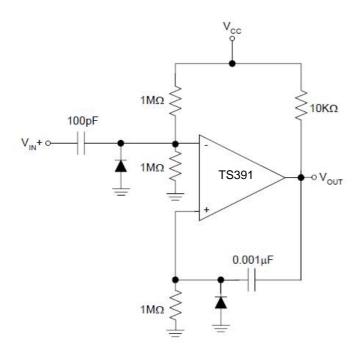
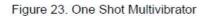


Figure 22. Driving CMOS





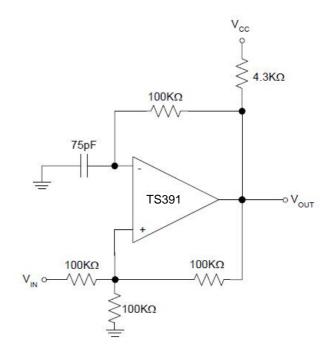
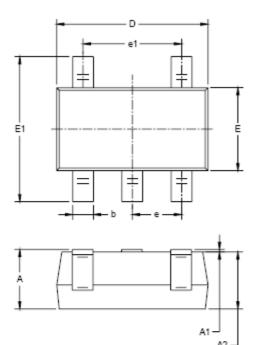


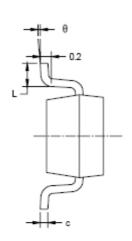
Figure 24. Squarewave Oscillator



Package Information

SOT23-5





Symbol	Dimensions In Millimeters		Dimensions In Inches		
	MIN	MAX	MIN	MAX	
A	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
e	0.950	BSC	0.037 BSC		
e1	1.900 BSC		0.075 BSC		
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	