# Silicon Epitaxial Planar Switching Diode

## Features

- Small plastic package suitable for surface mounted design
- High reliability with high surge current handling capability

## Applications

High speed switching

| PINNING   |             |  |  |  |  |
|---|-------------|--|--|--|--|
| PIN   | DESCRIPTION |  |  |  |  |
| 1   | Cathode     |  |  |  |  |
| 2   | Anode       |  |  |  |  |
|   |             |  |  |  |  |
|   |             |  |  |  |  |
| Top View<br>Simplified outline SOD-323 (Bend Lead) and symbol |             |  |  |  |  |

## Absolute Maximum Ratings (T<sub>a</sub> = 25°C)

| Parameter                         | Symbol           | Value         | Unit |
|-----------------------------------|------------------|---------------|------|
| Peak Reverse Voltage              | V <sub>RM</sub>  | 90            | V    |
| Reverse Voltage                   | VR               | 80            | V    |
| Average Rectified Forward Current | IF(AV)           | 100           | mA   |
| Peak Forward Current              | Ifm              | 225           | mA   |
| Surge Forward Current (1 s)       | IFSM             | 500           | mA   |
| Power Dissipation                 | PD               | 200           | mW   |
| Junction Temperature              | Tj               | 150           | °C   |
| Storage Temperature Range         | T <sub>stg</sub> | - 55 to + 150 | S    |

### **Thermal Characteristics**

| Parameter   | Symbol          | Max. | Unit |  |
|---|-----------------|------|------|--|
| Thermal Resistance from Junction to Ambient <sup>1)</sup> | $R_{\theta JA}$ | 625  | °C/W |  |

<sup>1)</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



## Electrical Characteristics (T<sub>a</sub> = 25°C)

| Parameter   | Symbol             | Min. | Max. | Unit |
|---|--------------------|------|------|------|
| Reverse Breakdown Voltage<br>at I <sub>R</sub> = 100 μA   | V <sub>(BR)R</sub> | 80   | -    | V    |
| Forward Voltage<br>at I⊧ = 100 mA   | VF                 | -    | 1.2  | V    |
| Reverse Current<br>at $V_R = 80 V$  | IR                 | -    | 100  | nA   |
| Capacitance between Terminals at $V_R$ = 0.5 V, f = 1 MHz   | Ст                 | -    | 3    | pF   |
| Reverse Recovery Time at I <sub>F</sub> = 10 mA, I <sub>rr</sub> = 0.1 X I <sub>R</sub> , V <sub>R</sub> = 6 V, R <sub>L</sub> = 100 $\Omega$ | t <sub>rr</sub>    | -    | 4    | ns   |



## **Electrical Characteristics Curves**

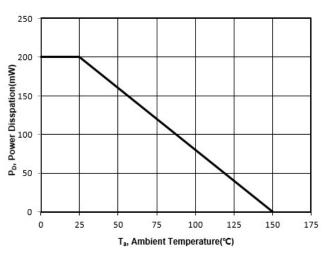


Fig 1. Power Derating Curve

Fig 2. Total Capacitance vs. Reverse Voltage

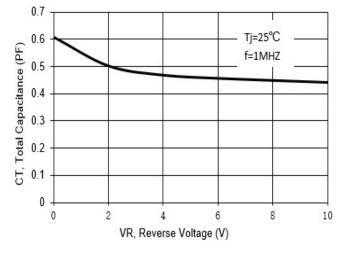


Fig 3. Reverse Current vs. Reverse Voltage

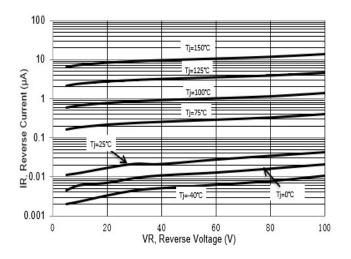
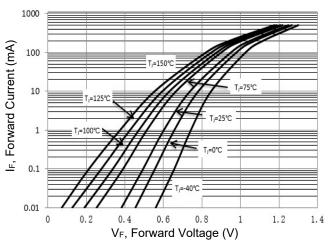


Fig 4. Forward Characteristics

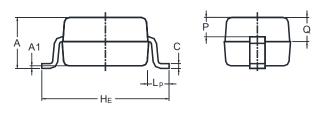


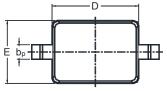


## PACKAGE OUTLINE

### Plastic surface mounted package; 2 leads

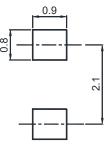
SOD-323BL





| UNIT | А   | A <sub>1</sub> | bp   | С    | D   | E    | Η <sub>E</sub> | Lp  | Q   | Р   |
|------|-----|----------------|------|------|-----|------|----------------|-----|-----|-----|
| mm   | 1.1 | 0.1            | 0.4  | 0.18 | 1.8 | 1.35 | 2.8            | 0.5 | 0.5 | 0.4 |
|      | 0.8 | 0              | 0.25 | 0.09 | 1.6 | 1.15 | 2.3            | 0.1 | 0.3 | 0.3 |

### **Recommended Soldering Footprint**



### Packing information

| Package   | Tape Width<br>(mm) | Pito    | Reel Size     |     |      |                           |
|-----------|--------------------|---------|---------------|-----|------|---------------------------|
|           |                    | mm      | inch          | mm  | inch | Per Reel Packing Quantity |
| SOD-323BL | 8                  | 4 ± 0.1 | 0.157 ± 0.004 | 178 | 7    | 3,000                     |

### **Marking information**

" W2 " = Part No. " III " = Cathode line Font type: Arial



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