

**TMN10003MI**
**N-Channel Enhancement Mosfet**
**General Description**

- Low  $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

**Applications**

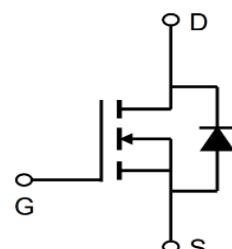
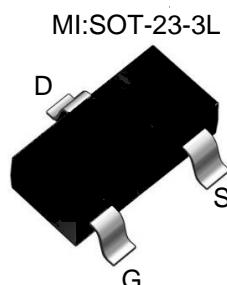
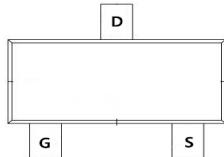
- Load switch
- PWM

**General Features**

$V_{DS} = 100V$   $I_D = 3.0A$

$R_{DS(ON)} = 200m\Omega$ (typ.)@  $V_{GS}=10V$

100% UIS Tested  
100%  $R_g$  Tested



Marking: 3N10 OR 1002

**Absolute Maximum Ratings (TC=25°C unless otherwise specified)**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$ @ $T_A=25^\circ C$	Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup>	3	A
$I_D$ @ $T_A=70^\circ C$	Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup>	1.2	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	5	A
$P_D$ @ $T_A=25^\circ C$	Total Power Dissipation <sup>3</sup>	1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup>	---	125	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	80	°C/W

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)**

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250μA	100	-	-	V
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	-	-	±100	nA
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250μA	1.1	1.5	2.5	V
Drain-Source on-state Resistance <sup>3</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2A	-	200	230	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1.5A	-	220	280	
<b>Dynamic Characteristics<sup>4</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 50V, f = 1MHz	-	440	-	pF
Output Capacitance	C <sub>oss</sub>		-	14	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	10	-	
<b>Switching Characteristics<sup>4</sup></b>						
Total gate charge	Q <sub>g</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> = 2A	-	5.3	-	nC
Gate-source charge	Q <sub>gs</sub>		-	1.4	-	
Gate-drain charge	Q <sub>gd</sub>		-	1.8	-	
Turn-on Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 50V, R <sub>G</sub> = 1Ω, I <sub>D</sub> = 2A	-	14	-	ns
Rise time	t <sub>f</sub>		-	54	-	
Turn-off Time	t <sub>d(off)</sub>		-	18	-	
Fall time	t <sub>f</sub>		-	11	-	
<b>Source-Drain Diode characteristics</b>						
Body Diode Voltage <sup>3</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1A, V <sub>GS</sub> = 0V	-	-	1.2	V
Continuous Source Current	I <sub>S</sub>		-	-	3.0	A

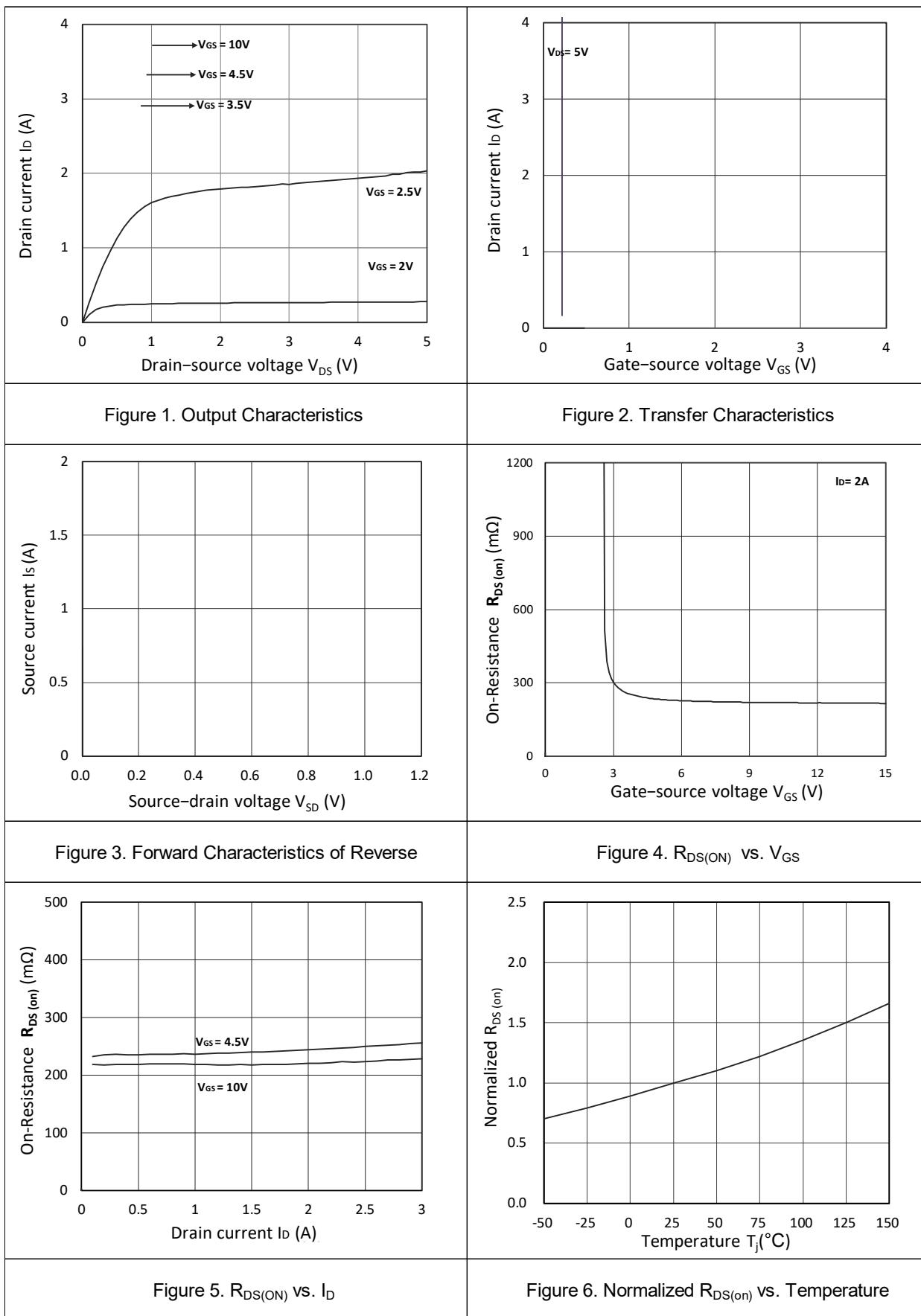
**Notes:**

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
2. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

## **TMN10003MI**

## **N-Channel Enhancement Mosfet**

### **Typical Characteristics**



## **TMN10003MI**

## **N-Channel Enhancement Mosfet**

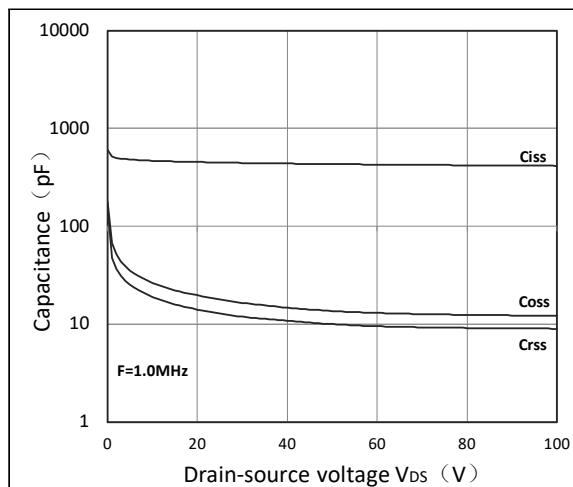


Figure 7. Capacitance Characteristics

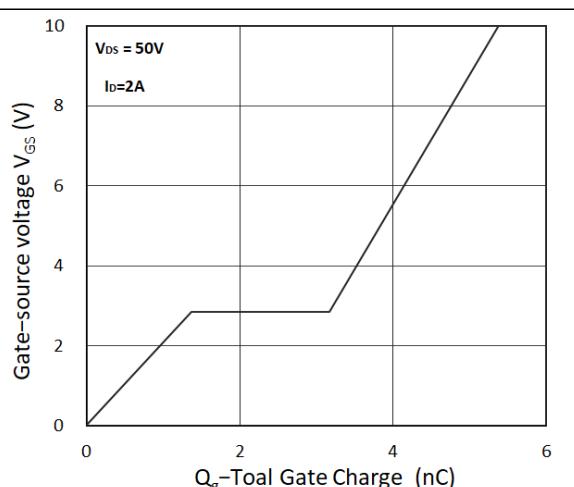
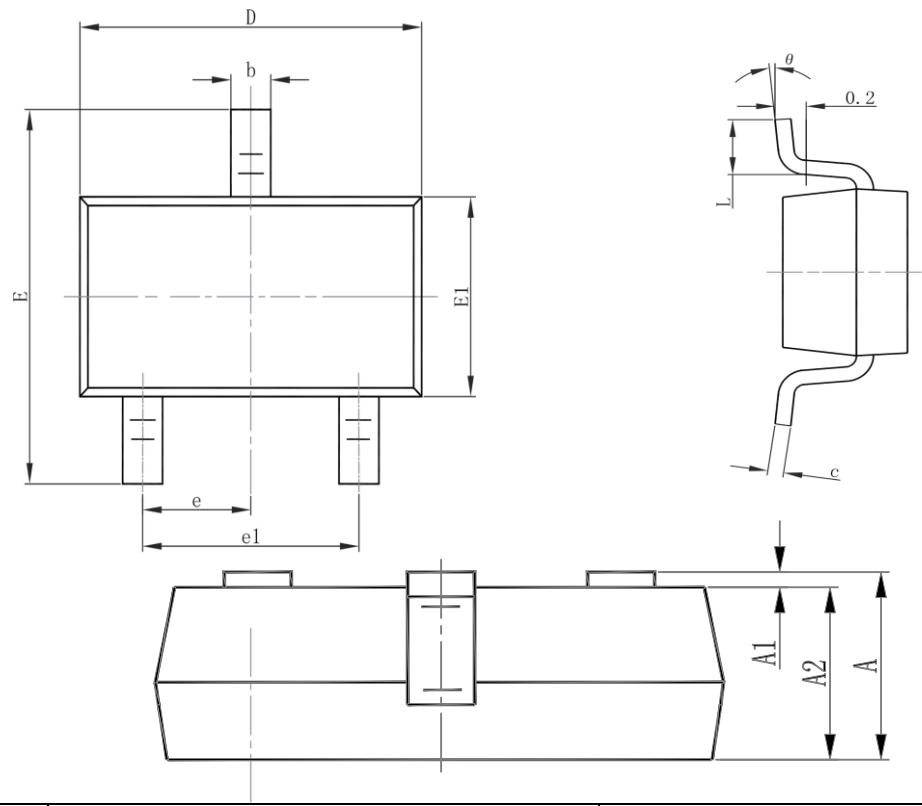


Figure 8. Gate Charge Characteristics

## Package Information:SOT-23-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°