

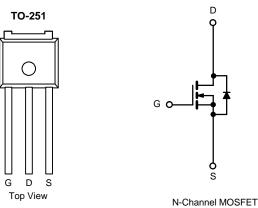
N-Channel 30-V (D-S) MOSFET

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PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ)		
30	0.0035 at V _{GS} = 10 V	100	95nC		
30	0.0045 at V _{GS} = 4.5 V	97	30110		



FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
 Compliant to RoHS Directive 2011/65/EU



- OR-ing
- Server
- DC/DC

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20	V	
	T _C = 25 °C		100 ^{a, e}		
Continuous Drain Current (T = $175 ^{\circ}$ C)	T _C = 70 °C		95 ^e		
Continuous Drain Current ($T_J = 175 \ ^{\circ}C$)	T _A = 25 °C	I _D	35 ^{b, c}	A	
	T _A = 70 °C		26 ^{b, c}		
Pulsed Drain Current		I _{DM}	197		
Avalanche Current Pulse	he Current Pulse		39		
Single Pulse Avalanche Energy	L = 0.1 IIIH	E _{AS}	94.8	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C	l.	90 ^{a, e}	٨	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	3.13 ^{b, c}	— A	
	T _C = 25 °C		250 ^a		
Mauianua Davia Diasia atian	T _C = 70 °C	P _D	175	10/	
Maximum Power Dissipation	T _A = 25 °C	۲D	3.75 ^{b, c}	W	
	T _A = 70 °C		2.63 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Тур.	Max.	Unit	
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 \text{ sec}$	R _{thJA}	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.5	0.6	0,00	

Notes:

a. Based on $T_C = 25 \text{ °C}$. b. Surface mounted on 1" x 1" FR4 board.

a. t = 10 sec.
d. Maximum under steady state conditions is 90 °C/W.
e. Calculated based on maximum junction temperature. Package limitation current is 90 A.



VBZFB80N03

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SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_{D} = 250 \mu A$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250 \mu A$		- 7.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	1.0		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zene Oete Maltere Drein Ourrest	1	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			А	
		V _{GS} = 10 V, I _D = 38.8 A		0.0035			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 37 A		0.0045		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 38.8 A		160		S	
Dynamic ^b				•		•	
Input Capacitance	C _{iss}			3000			
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		710		pF	
Reverse Transfer Capacitance	C _{rss}			170			
Total Opto Oberra	Qg	V_{DS} = 15 V, V_{GS} = 10 V, I_{D} = 38.8 A		171	257	1	
Total Gate Charge				81.5	123	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_{D} = 28.8 A		34			
Gate-Drain Charge	Q _{gd}			29			
Gate Resistance	Rg	f = 1 MHz		1.4	2.1	Ω	
Turn-On Delay Time	t _{d(on)}			18	27		
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		11	17	-	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 24 A, V_{GEN} = 10 V, R_g = 1 Ω		70	105		
Fall Time	t _f			10	15		
Turn-On Delay Time	t _{d(on)}			55	83	ns	
Rise Time	t _r	V_{DD} = 15 V, R _L = 0.67 Ω		180	270	-	
Turn-Off Delay Time	t _{d(off)}	$\rm I_D\cong$ 22.5 A, $\rm V_{GEN}$ = 4.5 V, $\rm R_g$ = 1 Ω		55	83		
Fall Time	t _f			12	18		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			120	•	
Pulse Diode Forward Current ^a	I _{SM}				120	A	
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			52	78	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			70.2	105	nC	
Reverse Recovery Fall Time	t _a	I _F = 20 A, di/dt = 100 A/μs, T _J = 25 °C		27		ns	
Reverse Recovery Rise Time	t _b			25			

Notes:

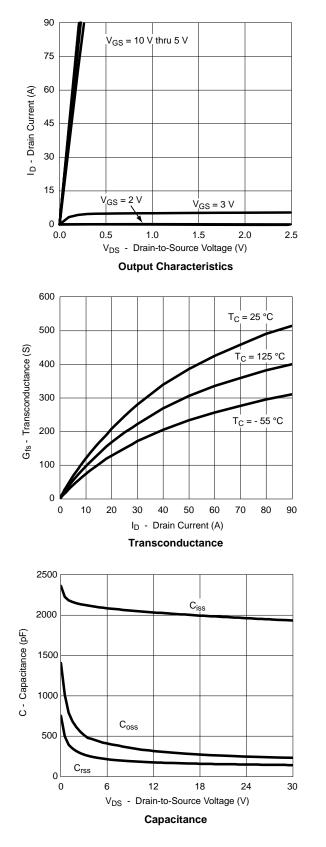
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

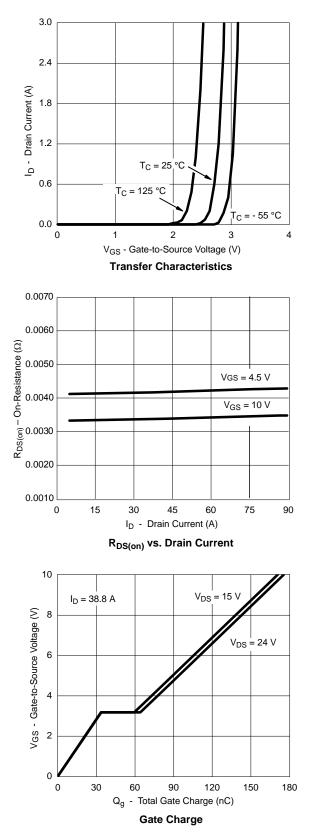
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





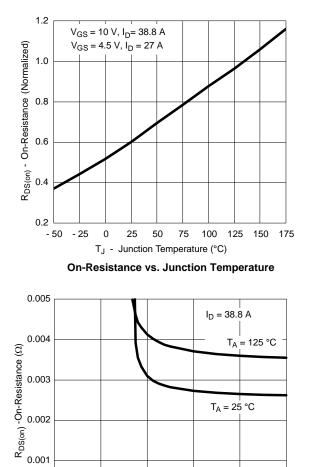


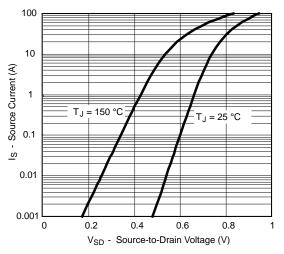
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

T_A = 25 °C

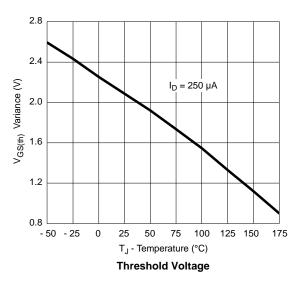
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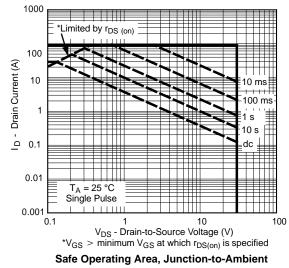
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Forward Diode Voltage vs. Temperature





0.001

0.000

0

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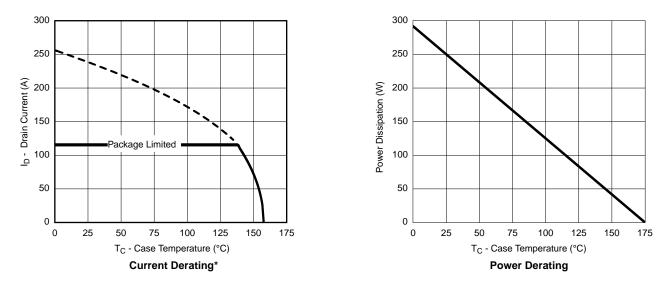
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 V_{GS} - Gate-to-Source Voltage (V)

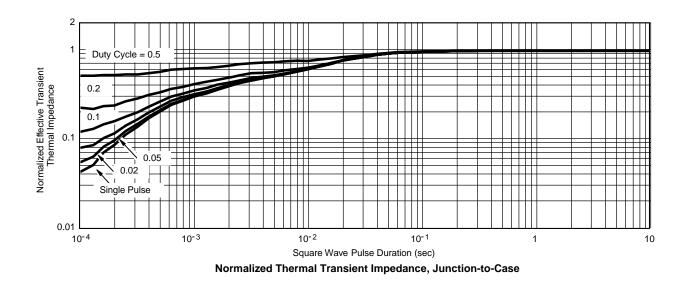
 $R_{DS(on)}$ vs. V_{GS} vs. Temperature





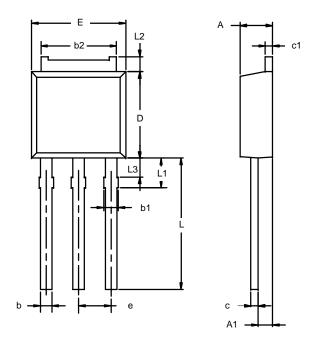
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

*The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

	MILLIN	IETERS	INC	HES
Dim	Min	Мах	Min	Max
Α	2.21	2.38	0.087	0.094
A1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
С	0.46	0.58	0.018	0.023
c1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
Е	6.48	6.73	0.255	0.265
е	2.28 BSC		0.090 BSC	
L	8.89	9.53	0.350	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060
ECN: S-0 DWG: 53	3946—Rev. I 346	E, 09-Jul-01		

服务热线:400-655-8788



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