



## Description

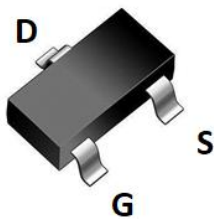
### JMT N-channel Enhancement Mode Power MOSFET

#### Features

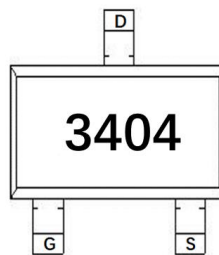
- 30V,5.8A  
 $R_{DS(ON)} < 25m\Omega @ V_{GS} = 10V$   
 $R_{DS(ON)} < 40m\Omega @ V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

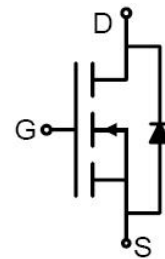
- Load Switch
- PWM Application
- Power management



SOT-23 top view



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
3404	JMTL3404A	TAPING	SOT-23	7inch	3000	120000

## Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

Symbol	Parameter	Max.	Units
V <sub>DSS</sub>	Drain-Source Voltage	30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> = 25°C	5.8
		T <sub>A</sub> = 100°C	3.8
I <sub>DM</sub>	Pulsed Drain Current <sup>note1</sup>	23.2	A
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> = 25°C	1.47
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	85	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150	°C



## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

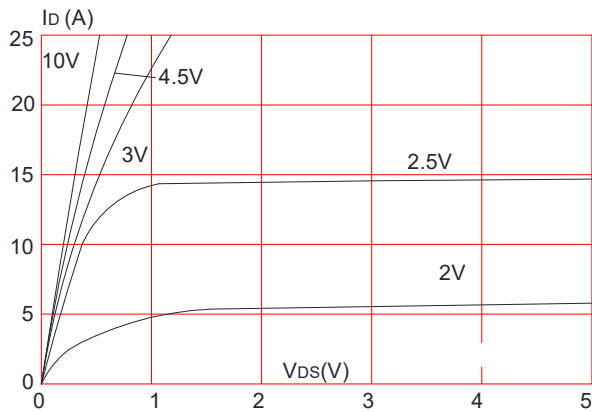
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V,	-	-	1.0	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.5	2.5	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note2</small>	V <sub>GS</sub> =10V, I <sub>D</sub> =5.5A	-	18	25	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =4.5A	-	28	40	
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1.0MHz	-	490	-	pF
C <sub>oss</sub>	Output Capacitance		-	79	-	pF
C <sub>riss</sub>	Reverse Transfer Capacitance		-	61	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V, I <sub>D</sub> =5.8A, V <sub>GS</sub> =10V	-	5.2	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	0.9	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	1.3	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =15V, I <sub>D</sub> =3A, R <sub>GEN</sub> =3Ω, V <sub>GS</sub> =10V	-	4.5	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	2.5	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	14.5	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	3.5	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	5.8	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	23.2	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =5.8A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

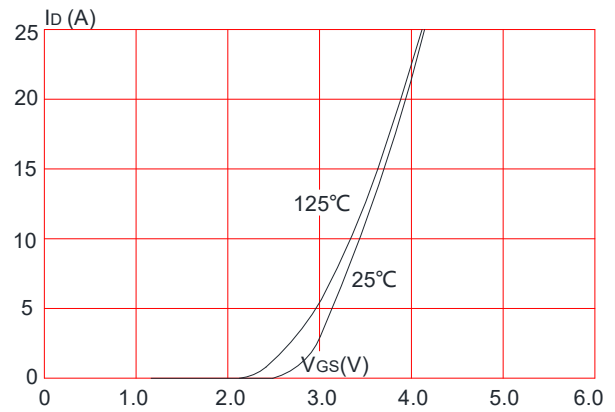
2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



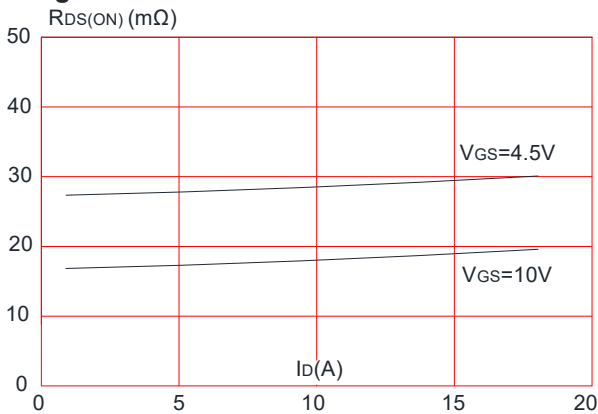
**Figure 1: Output Characteristics**



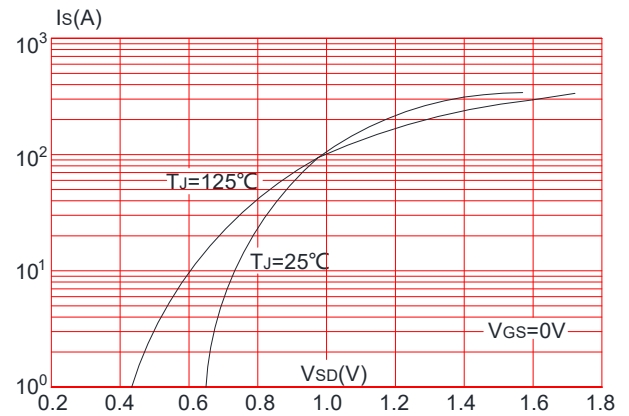
**Figure 2: Typical Transfer Characteristics**



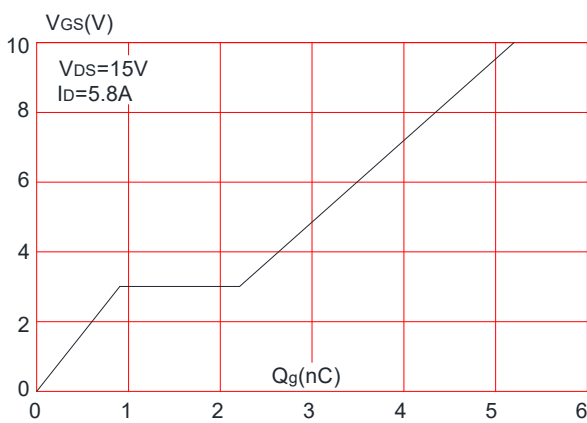
**Figure 3: On-resistance vs. Drain Current**



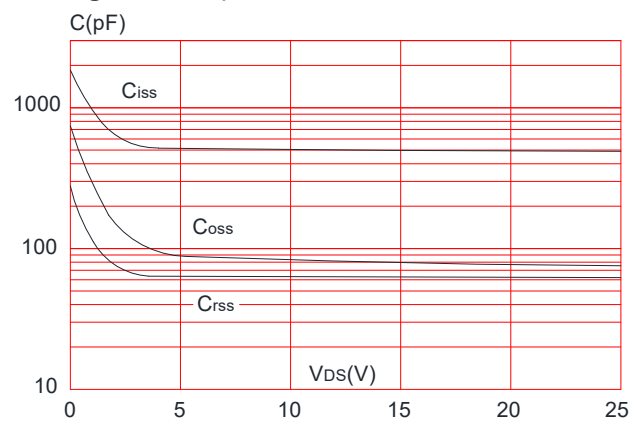
**Figure 4: Body Diode Characteristics**



**Figure 5: Gate Charge Characteristics**

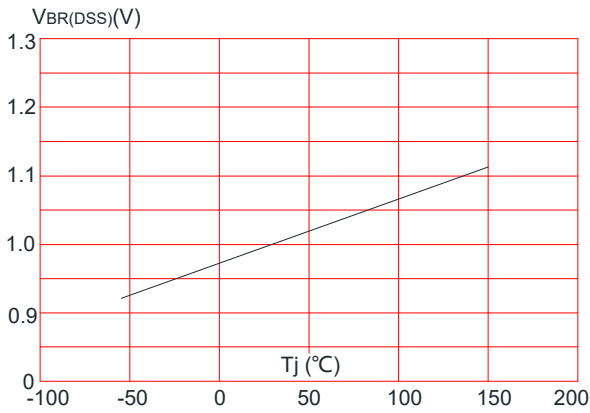


**Figure 6: Capacitance Characteristics**

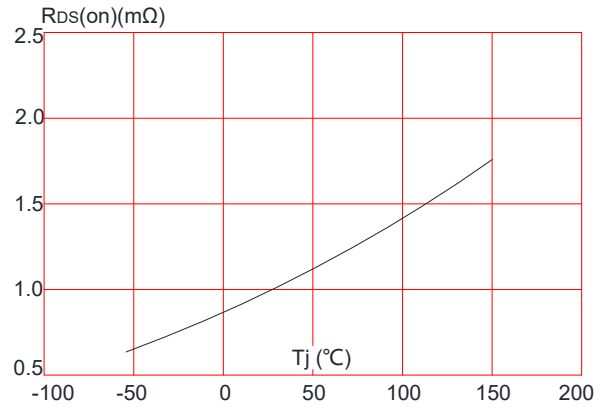




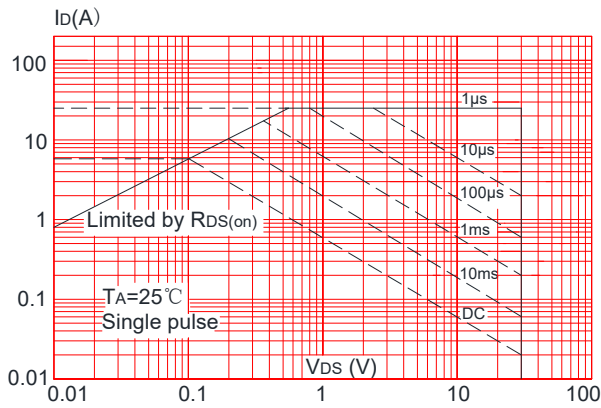
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



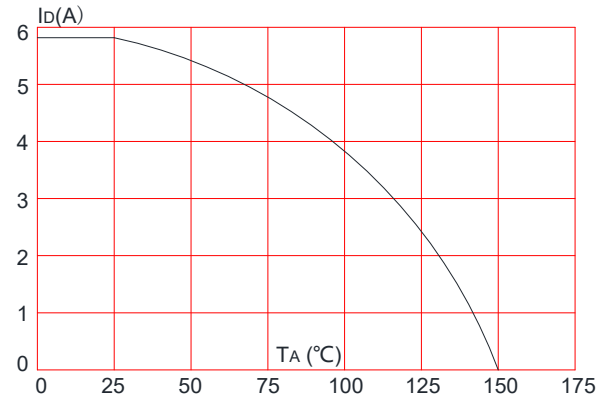
**Figure 8:** Normalized on Resistance vs. Junction Temperature



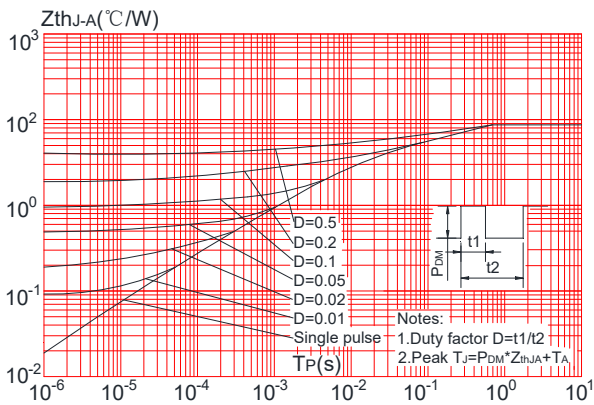
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



## Test Circuit



Figure1:Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveforms

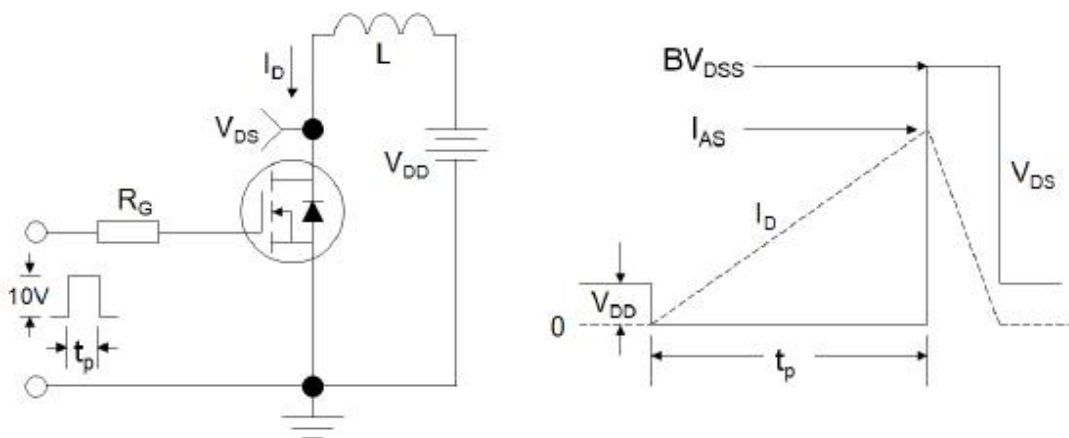
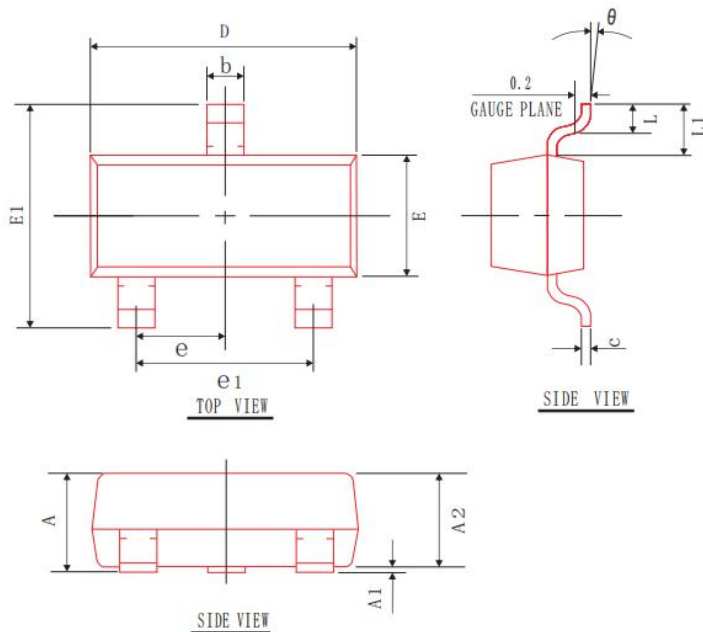


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



## Package Mechanical Data-SOT-23




COMMON DIMENSIONS  
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.90	1.05	1.20
A1	0.00	0.05	0.10
A2	0.90	1.00	1.10
b	0.30	0.40	0.50
c	0.08	0.10	0.15
D	2.80	2.90	3.00
E	1.20	1.30	1.40
E1	2.30	2.40	2.50
L	0.30	0.40	0.50
$\theta$	0°	5°	10°
L1	0.55 REF		
e	0.95 BSC		
e1	1.90 REF		

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