

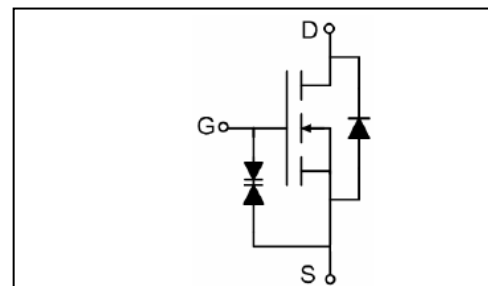
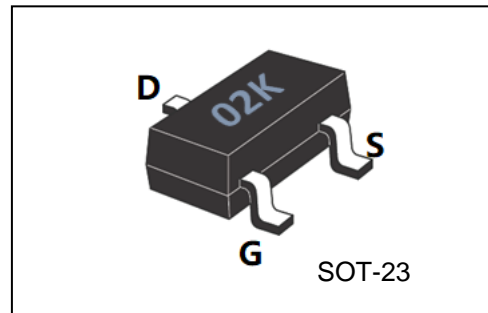
**Features:**

- Fast Switching
- Low Gate Charge and Rdson
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test

$V_{DSS}$	60	V
$I_D$	0.3	A
$P_D$	0.35	W
$R_{DS(ON)max}$	2.5	$\Omega$

**Applications:**

- PWM applications
- Load switch
- Power management



**Absolute** ( $T_c=25^\circ\text{C}$  unless otherwise specified):

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	60	V
$I_D$	Continuous Drain Current	0.3	A
	Continuous Drain Current $T_c = 100^\circ\text{C}$	0.19	A
$I_{DM}^{a1}$	Pulsed Drain Current	0.9	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$dv/dt^{a3}$	Peak Diode Recovery $dv/dt$	5.0	V/ns
$P_D$	Power Dissipation	0.35	W
VESD(G-S)	Gate source ESD (HBM-C= 100pF, R=1.5k $\Omega$ )	2000	V
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ\text{C}$
$T_L$	Maximum Temperature for Soldering	300	$^\circ\text{C}$

**Electrical Characteristics** (Tc= 25°C unless otherwise specified):

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	60	--	--	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Bvdss Temperature Coefficient	I <sub>D</sub> =-250uA, Reference 25°C	--	0.1	--	V/°C
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> =60, V <sub>GS</sub> =0V, T <sub>a</sub> =25°C	--	--	1	μA
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>a</sub> =125°C	--	--	250	
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> = +20V	--	--	10	μA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> = -20V	--	--	-10	μA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A	--	--	2.5	Ω
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.3A	--	--	3.5	Ω
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.7	2.5	V
Pulse width tp ≤ 380μs, δ ≤ 2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =0.2A	0.1	5.0	--	S
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V f = 1.0MHz	--	20	--	pF
C <sub>oss</sub>	Output Capacitance		--	12	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	4.4	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	I <sub>D</sub> = 0.2A, V <sub>DD</sub> = 15V V <sub>GS</sub> = 10V, R <sub>G</sub> = 3.0Ω	--	10	--	ns
t <sub>r</sub>	Rise Time		--	45	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	15	--	
t <sub>f</sub>	Fall Time		--	10	--	
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> = 0.3A, V <sub>DD</sub> = 15V V <sub>GS</sub> = 10V	--	1.7	--	nC
Q <sub>gs</sub>	Gate to Source Charge		--	0.9	--	
Q <sub>gd</sub>	Gate to Drain ( "Miller" ) Charge		--	1.3	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$I_S$	Continuous Source Current (Body Diode)		--	--	0.3	A
$I_{SM}$	Maximum Pulsed Current (Body Diode)		--	--	0.9	A
$V_{SD}$	Diode Forward Voltage	$I_S=0.3A, V_{GS}=0V$	--	--	1.5	V
$t_{rr}$	Reverse Recovery Time	$I_S=0.3A, T_j = 25^\circ C$	--	40	--	ns
$Q_{rr}$	Reverse Recovery Charge	$di_F/dt=100A/us, V_{GS}=0V$	--	120	--	nC
Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$						

Symbol	Parameter	Typ.	Units
$R_{\theta JA}$	Junction-to-Ambient	350	$^\circ C/W$

<sup>a1</sup>: Repetitive rating; pulse width limited by maximum junction temperature

<sup>a3</sup>:  $I_{SD}=0.3A, di/dt \leq 100A/us, V_{DD} \leq BV_{DS}, Start T_j=25^\circ C$

Typical Electrical And Thermal Characteristics

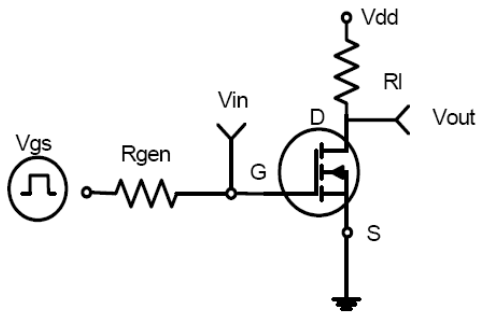


Figure 1: Switching Test Circuit

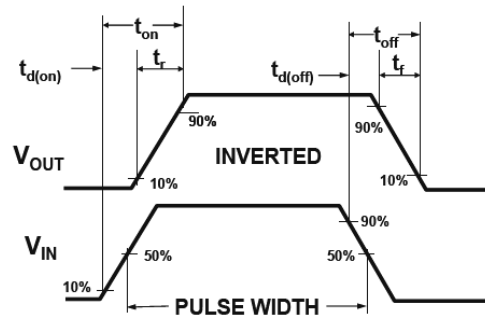


Figure 2: Switching Waveforms

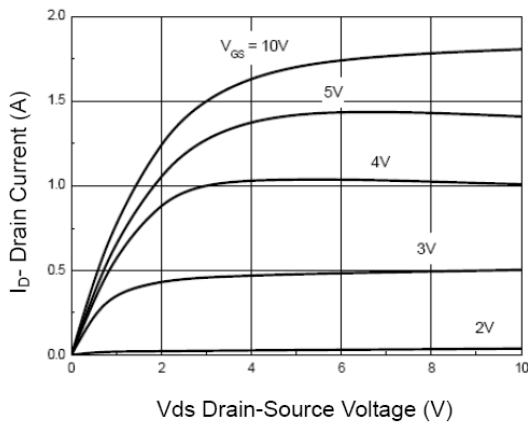


Figure 3 Output Characteristics

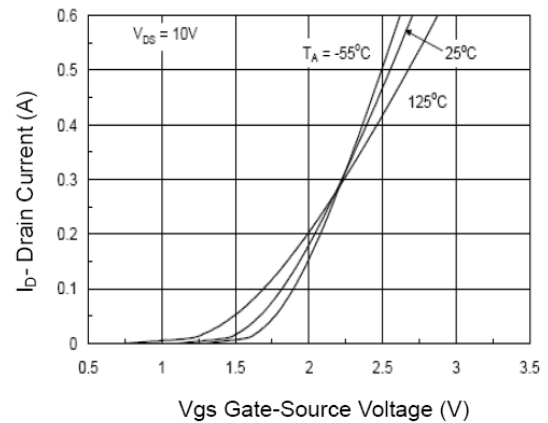


Figure 4 Transfer Characteristics

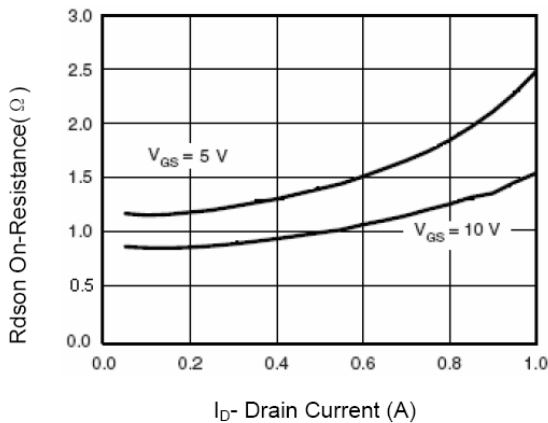


Figure 5 Drain-Source On-Resistance

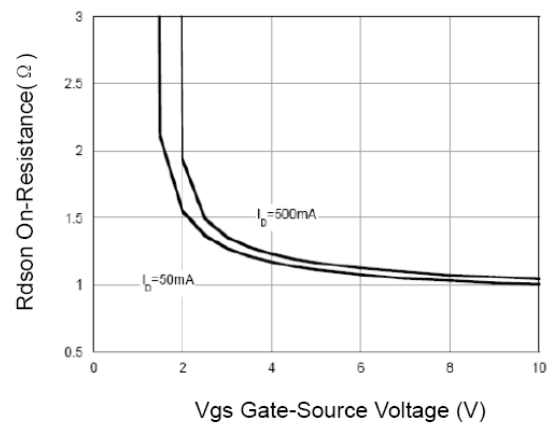


Figure 6 Rds(on) vs Vgs

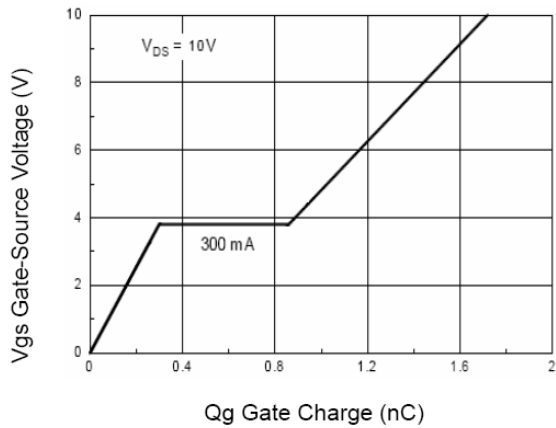


Figure 7 Gate Charge

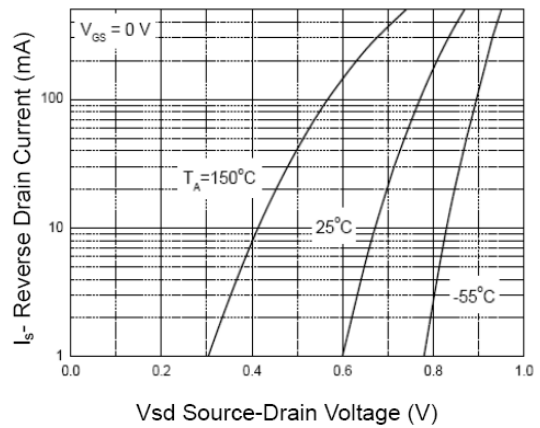


Figure 8 Source-Drain Diode Forward

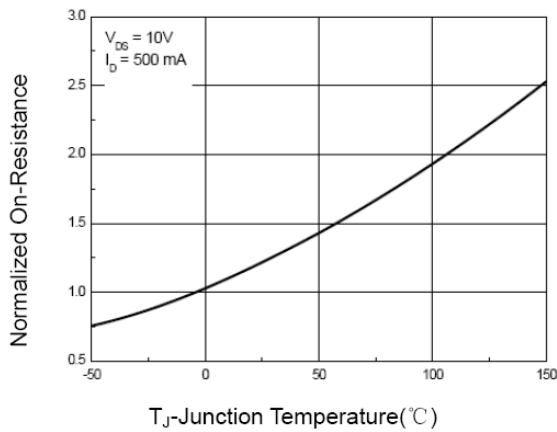


Figure 9 Drain-Source On-Resistance

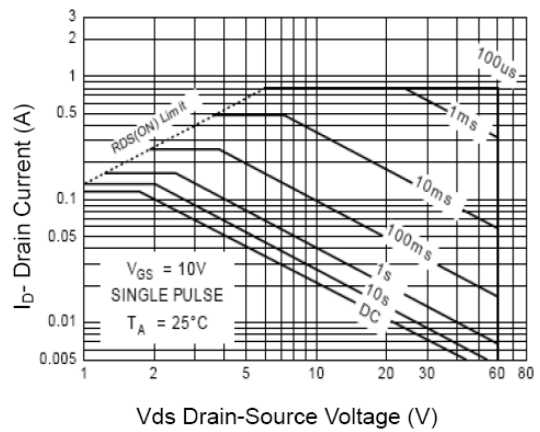


Figure 10 Safe Operation Area

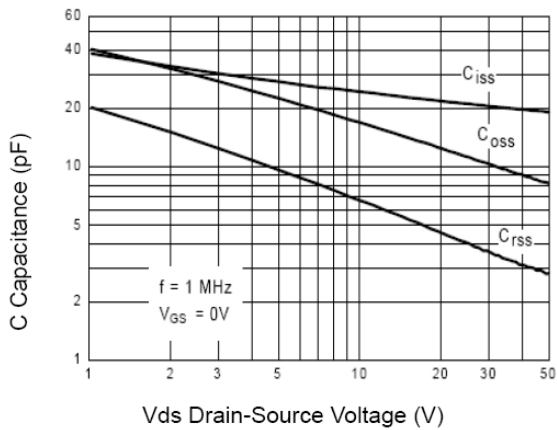


Figure 11 Capacitance vs Vds

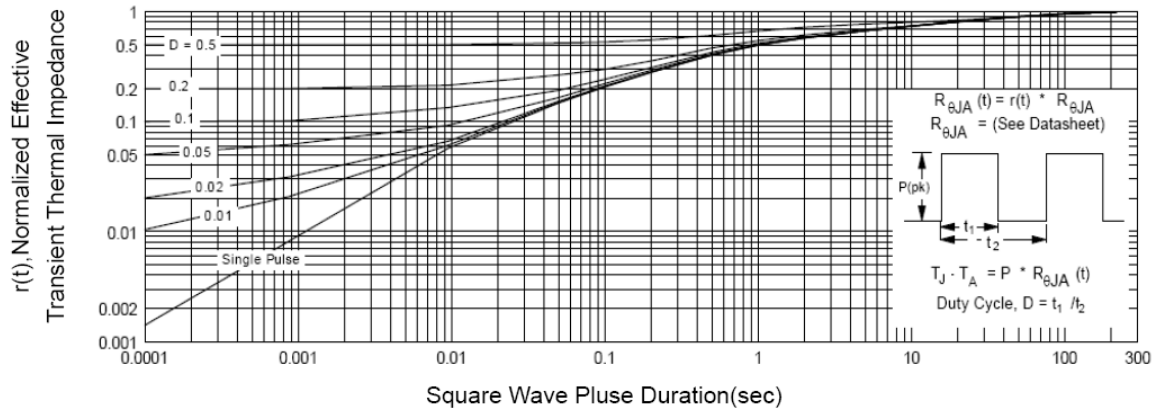


Figure 12 Normalized Maximum Transient Thermal Impedance