





1.0A SCHOTTKY BARRIER RECTIFER CHIP SCALE PACKAGE

Product Summary

| V _{RRM} (V) | I _O (A) | V _{F max} (V) | I _{R max} (μA) |
|----------------------|--------------------|------------------------|-------------------------|
| 40 | 1.0 | 0.48 | 100 |

Description and Applications

The DIODES™ SDM1U40CSP is a 40V 1A Schottky Barrier Rectifier optimized for low forward voltage drop and low-leakage current. Housed in a compact chip scale package (CSP), the SDM1U40CSP occupies only 0.84 mm² board-space with low profile. The low thermal resistance enables designers to meet design challenges of increasing efficiency whilst at the same time reducing board space. It is ideally suited for use in portable applications as a:

- Blocking diode
- Boost diode
- Switching diode
- Reverse protection diode

Features and Benefits

- Low Forward Voltage (V_F) Minimizes Conduction Losses and Improves Efficiency
- Reduced High Temperature Reverse Leakage
- Increased Reliability Against Thermal Runaway Failure in High Temperature Operation
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

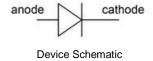
Mechanical Data

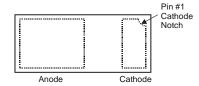
Package: X3-WLB1406-2

Moisture Sensitivity: Level 1 per J-STD-020

Polarity: Cathode Dot

Weight: 0.001 grams (Approximate)





Ordering Information (Note 4)

| Part Number | Paakaga | Packing | | |
|--------------|--------------|---------|-------------|--|
| Fait Number | Package | Qty. | Carrier | |
| SDM1U40CSP-7 | X3-WLB1406-2 | 5000 | Tape & Reel | |

Notes:

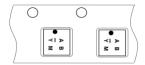
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Pin 1



X1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 1 = January) Dot denotes Cathode Pin



Date Code Key

| Year | 2014 | | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 |
|-------|------|-----|------|------|------|------|------|------|------|------|------|------|
| Code | В | | J | K | L | М | N | 0 | Р | R | S | Т |
| | | | | | | | | | | | | |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|------------------|-------|------|
| Peak Repetitive Reverse Voltage | V_{RRM} | 40 | V |
| Average Rectified Output Current | lo | 1.0 | А |
| Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%) | I _{FRM} | 5.0 | А |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I _{FSM} | 18 | А |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|-----------------------------------|-------------|------|
| Typical Thermal Resistance Junction to Ambient (Note 5) | R _{OJA} | 190 | °C/W |
| Typical Thermal Resistance Junction to Ambient (Note 6) | R _{OJA} | 105 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +150 | °C |

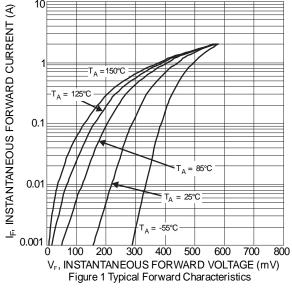
$\textbf{Electrical Characteristics} \ (@T_A = +25 ^{\circ}\text{C}, \ unless \ \underline{otherwise \ specified.})$

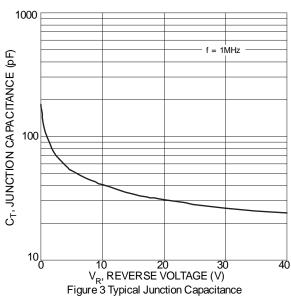
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|--------------------------|--------|-----|------|------|------|---------------------------|
| Forward Voltage Drop | VF | | 0.37 | 0.41 | W | I _F = 0.5A |
| Forward Voltage Drop | ٧F | _ | 0.44 | 0.48 | V | I _F = 1.0A |
| Reverse Current (Note 7) | - | _ | _ | 22 | μA | V _R = 10V |
| Reverse Current (Note 7) | IR | _ | _ | 100 | μΑ | $V_R = 40V$ |
| Junction Capacitance | СЈ | | 58 | - | pF | $V_R = 4V$, $f = 1.0MHz$ |

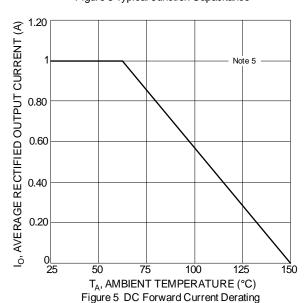
Notes:

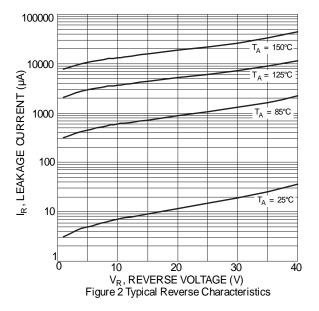
- 5. Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.
- 6. Device mounted on FR-4 PCB, 2oz. 1 square inch Copper.
 7. Short duration pulse test used to minimize self-heating effect.

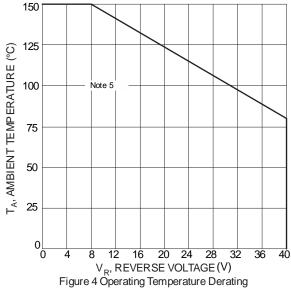










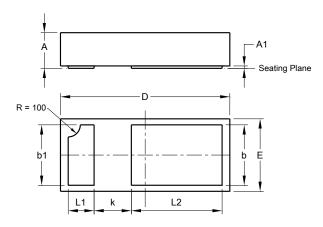




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-WLB1406-2

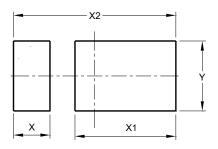


| X3-WLB1406-2 | | | | | | |
|--------------|----------------------|-------|-------|--|--|--|
| Dim | Min | Max | Тур | | | |
| Α | 0.250 | 0.300 | 0.275 | | | |
| A1 | 0.000 | 0.015 | _ | | | |
| b | 0.45 | 0.55 | _ | | | |
| b1 | 0.45 | 0.55 | _ | | | |
| D | 1.37 | 1.43 | 1.40 | | | |
| Е | 0.57 | 0.63 | 0.60 | | | |
| k | _ | _ | 0.30 | | | |
| L1 | 0.20 | 0.26 | _ | | | |
| L2 | 0.70 | 0.80 | _ | | | |
| All I | All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-WLB1406-2



| Dimensions | Value (in mm) |
|------------|------------------|
| Х | 0.304 |
| X1 | 0.840 |
| X2 | 1.352 |
| v | 0.580 |



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