## 顺翔诺 SXN

#### SMTC0420A Wire Wound Molded SMD Power Inductors







### ◆特征:

- 低直流电阻和超大电流的薄型设计
- 磁屏蔽型抗电磁干扰强适用于高密度安装
- 高可靠性,通过采用一体成型结构享有卓越 的抗震动性
- 由于复合结构,超低蜂鸣噪声
- 低损耗合金粉末压铸低阻抗,小寄生电容
- 能效高,可减少绕线的低直流电阻与磁芯的 涡流损耗
- 频率高达 3MHz
- 绝缘最大电压 30VDC
- 符合 RoHS, 无卤和 REACH
- 符合 AEC-Q200

### ◆用途:

- PDA,笔记本,台式机,服务器应用程序
- 大电流 POL 转换器
- 电池供电设备、基站
- 分布式电源系统中的 DC/DC 转换器

#### ◆环境:

工作温度: -55℃ 至+155℃
 (包括线圈自身温升)

### ◆试验设备:

- 电感值: WK3260B 或同等仪器
- 电流: WK3260B+WK3265B 或同等仪器
- 直流电阻: Chroma 16502 或同等仪器

SMTC

### ◆产品型号:

### Features:

- Low RDC and ultra-high current thin design
- Magnetic shielding type, strong anti- electromagnetic
   Interference, suitable for high- density installation
- High-reliability, High vibration resistance as result of newly developed integral construction
- Ultra Low buzz noise, due to composite construction
- Die-casting by low loss alloy powder low impedance,
   Small parasitic capacitance
- High efficiency Low DC resistance of winding and low eddy-current loss of the core
- Frequency up to 3MHz
- Absolute maximum voltage 30VDC
- RoHS, Halogen Free and REACH Compliance
- AEC-Q200 Compliant

### **Applications:**

- PDA, notebook, desktop, server applications
- High current POL converters
- Battery powered devices. Base station
- DC/DC converters in distributed power systems

#### Environmental Data:

Operating Temperature: -55℃ to +155℃ (Including coils self-temperature rise)

### **Test Equipment:**

- L: WK3260B LCR meter or equivalent
- Isat & Irms: WK3260B+WK3265B or equivalent

**(5)** 

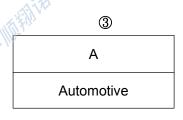
DCR:Chroma 16502 or equivalent

#### Product Identification:

1R0

<b>①</b>				
类型 Type				
	成型贴片功率电感			
SMTC	Molding SMD Power Inductor			

<b>2</b>						
外形尺寸(L	外形尺寸(L×W×H) (mm)					
External Dime	External Dimensions (L×W×H)					
(n	(mm)					
0420	4.1×4.1×2.0					



0420



4

Inductance 1.0 uH

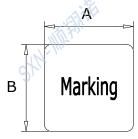
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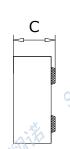
公差 Inductance Tolerance

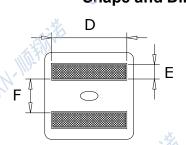
J:±5%,K: ±10%, L: ±15% M: ±20%,P: ±25%, N: ±30%

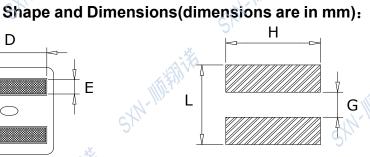
11/1/2	<b>w</b>
lu,	包装 Packing
В	散装Bulk Package
Т	编带Tape & Reel

### ◆外观尺寸:









Recommended Land Pattern

Part No.	ITEM				5,	Unit:mn	n		
Tare Type	Α	В	C	D	Е	THE PARTY OF THE P	L	Н	G
SMTC0420A	4.1±0.3	4.1±0.3	1.9±0.3	3.4±0.3	0.88±0.3	1.7±0.3	3.4ref	3.8ref	1.4ref

### ▶规格特性:

### Specifications:

• SMTC0420A Series Electrical Characteristics (Electrical specifications at 25°C)

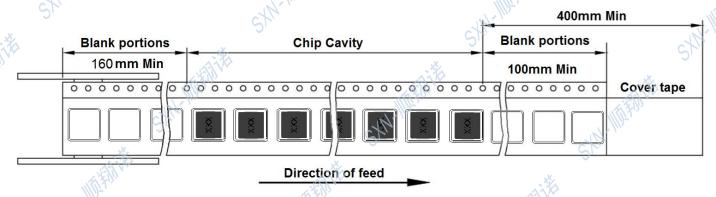
Dord No	Inductance 100KHz 1.0V		DCR (mΩ)	Saturation Cu	rrent (A)	Temperature Rise Current(A)	
Part No	L(µH) '@0A	Tol	Max	Max	Typical	20℃ Rise	40℃ Rise
SMTC0420A-R10M	0.1	±20%	2.42	33	38	13.5	18
SMTC0420A-R22M	0.22	±20% 5	4.6	18.8	19.5	13.0	16.8
SMTC0420A-R36M	0.36	±20%	6.3	15	17	11	14.5
SMTC0420A-R40M	0.4	±20%	7.73	13.5	15.5	10	14
SMTC0420A-R47M	0.47	±20%	8.58	13	14.5	9	12.5
SMTC0420A-R56M	0.56	±20%	9.3	12.6	14	8.5	12
SMTC0420A-R60M	0.6	±20%	9.52	12.3	13.7	8.05	11.7
SMTC0420A-R72M	0.72	±20%	11.6	10.6	12	7.6	10.5
SMTC0420A-1R0M	1.0	±20%	14.6	8.8	9.6	6.8	9.6
SMTC0420A-1R2M	1.2	±20%	17.9	7.8	9/	6.6	9.0
SMTC0420A-1R5M	1.5	±20%	23.5	7.4	8	5.8	7.6
SMTC0420A-1R8M	1.8	±20%	28	7	7.5	5.2	7
SMTC0420A-2R2M	2.2	±20%	38.7	6	6.5	4.6	5.6



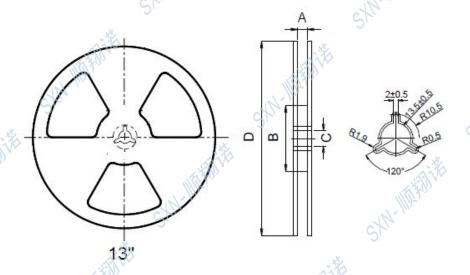
- Saturation Current: DC current at which inductance drops 30% from its value without current.
- Temperature Rise Current: the actual value of DC current when the temperature rise is ΔT 40°C (Ta=25°C).
- Rated DC Current: The less value which is Isat or Irms.
- Special remind: Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- Saturation current VS temperature rise current curve

### ◆产品包装: Packaging:

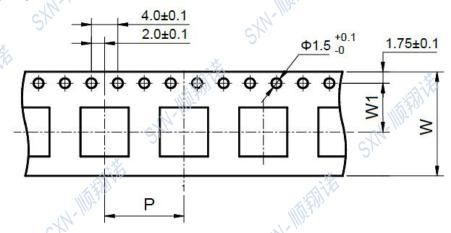
• Tape and Reel Specifications: (Dimensions are in mm)



Reel dimensions (mm)

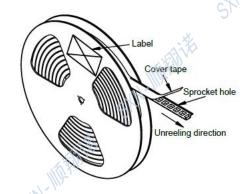


◆Tape Dimension (mm)

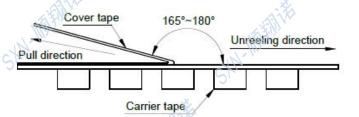




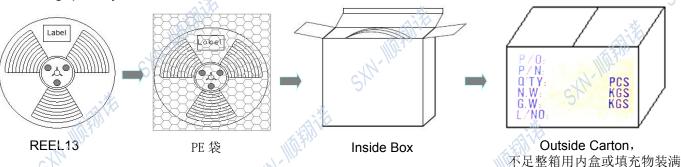
### • Cover tape peel off condition



- a) Cover tape peel force shall be 10 to 120g
- b) Noodle strip peeling angle165°to 180°



### Packing quantity



Dark No.	Таре	e Dimen	nsion	>	Reel Di	mensions	, ž	REEL	Inside	Outside
Part No.	W	Р	W1	Α	В	C	D	(PCS)	Box(PCS)	Carton(PCS)
SMTC0420A	12 ±0.3	8 ±0.1	5.5 ±0.1	12.8±0.2	97±0.5	13.0±0.2	330+2.0	3000	12,000	48,000
SXM-III	游游港			54)				N-IIII	SK.	
X.								CV.		1



## ◆可靠性测试:

### Reliability Testing:

▼ 可		Reliability lesting:
Items	Requirements	Test Methods and Remarks
Terminal Strength Reference docu ments: GB/T 2423.60-2008 端子強度(SMT)	1. Pulling test:  Define: A: sectional area of terminal  A ≤ 8mm2 force ≥ 5N time:30sec  8mm2 <a 10n="" 10sec="" 2.solder="" 20mm2="" 20mm2<a="" 20n="" 3.meet="" above="" any="" force="" loose="" paste="" requirements="" td="" terminal<="" the="" thickness:0.12mm="" time:="" without="" ≤="" ≥=""><td>Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.</td></a>	Solder the inductor to the testing jig using leadfree solder. Then apply a force in the Keep time: 10±1s Speed: 1.0mm/s.
erminal Strength Reference docu ments: GB/T 2423.60-2008 端子強度(DIP)	1.Terminal diameter(d) mm 0.35 < d < 0.50 Applied force:5N Duration: 10sec2.Terminal diameter(d) mm0.50 < d < 0.80 Applied force:10N Duration: 10sec3.Terminal diameter(d) mm0.80 < d < 1.25 Applied force:20N Duration: 10sec4.Terminal diameter(d) mmD > 1.25 Applied force:40N Duration: 10sec5.Meet the above requirements without any loose terminal.	Pull Force:the force shall be applied gradually to the terminal and thenmaintained for 10 seconds.
N <sup>I</sup>	1.No visible mechanical damage.	1.Solder the inductor to the test jig (glass epoxy board 2.shown in Using a leadfree solder. Then apply a force in the direction shown
Resistance to Flexure JIS C 5321:1997 抗弯曲性试验	SKM-IIII FAITH	3.Flexure: 2mm. 4.Pressurizing Speed: 0.5mm/sec. 5.Keep time: 30 sec.
Dropping Reference documents: GB/T 2423.7-2018 落下試驗	1.No case deformation or change inappearance.  2.No short and no open.	Drop the packaged products from 1m high in 1 angle, 3 ridges and 6surfaces, twice in each direction.
Solderability Reference documents: GB/T 2423.28-2005 可焊性试验	1.No visible mechanical damage. 2.Wetting shall exceed 75% coverage for 3.Terminals must have 95% minimum solder coverage	<ul> <li>1.Solder temperture:240±2℃</li> <li>2.Duration: 3 sec.</li> <li>3. Solder: Sn/3.0Ag/0.5Cu.</li> <li>4.Flux: 25% Resin and 75% ethanol in weight</li> </ul>



Items	Requirements	Test Methods and Remarks
	1.No visible mechanical damage.	1.Solder the inductor to the testing jig (glass epoxy
	2. Inductance change: Within ±10%.	boardshown in ) using leadfree solder.
	3.Q factor change: Within ±20%.	2. The inductor shall be subjected to a simple
	o. Quador change. Within 120%.	harmonic motion having total amplitude of 1.5mm,
C	Cu pad Solder mask	the frequency being varieduniformly between the
		approximate limits of 10 and 55 Hz.
Vibration		3. The frequency range from 10 to 55 Hz and
Reference documents:		return to 10 Hz shallbe traversed in approximately
GB/T 2423.10-2019	Glass Epoxy Board	1 minute. This motion shall be applied for a period
振動試验	SKON BOARD	of 2 hours in each 3mutually perpendicular
*	x.	directions(total of 6 hours).
		Freq 1
	11/1/278	55Hz
	ETU.	
.30	Sh.	10Hz
		0 1Min Time
	1.No visible mechanical damage.	1.Start at ( 85~125℃) for T time, rush to
CTM	2. Inductance change: Within ±10%.(Mn-Zn:	(-55~40℃) for T time as one cycle, go through100
5,	Within ≤ 30% )	cycles.
jik	3.Q factor change: Within ±20%.	2.Transforming interval: Max. 20 sec.
Thermal Shock		3.Tested cycle: 100 cycles.
Reference documents:	17 - Mhr.	4. The chip shall be stabilized at normal condition
GB/T 2423.22-2012	Str.	for 1~2 hours
Method Na	51	125°C/85°C 30 min. 30 min.
冷热冲击试验	\$\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	Ambient
		Temperature 30 min. 3
Str	W. III	20sec. (max.)
	Sr	STA
	1.No visible mechanical damage.	1.Temperature:M(-55~-40±2℃)
7.7	2. Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours
	Within ≦30%)	3.The chip shall be stabilized at normal condition for
Low temperature Storage	3.Q factor change: Within ±20%.	1~2 hoursbefore measuring.
Reference documents: GB/T 2423.1-2008		Room
Method Ab	The state of the s	Temp OSH Test
低温储存试验	W Take	97H 98H Time
队価141大风沙	EXM. II	M°C Low temperature
。 《卷	ے,	Temp
Ziegiji V		**

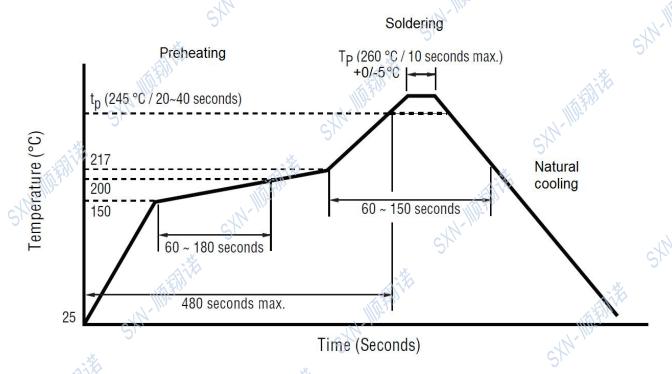


Items	Requirements	Test Methods and Remarks
	1.No visible mechanical damage.	1.Temperature:N(125~85±2℃).
High temperature	2. Inductance change: Within ±10%.(Mn-Zn:	2.Duration: 96±2 hours
Storage	Within ≤ 30% )	3. The chip shall be stabilized at normal condition
Reference documents:	3 Q factor change: Within ±20%.	for 1~2 hoursbefore measuring
GB/T 2423.2-2008	cth. "	Temp High temperature
Method Bb	2,	N.C Sha
高温储存试验		Room Temp
		0 96H 97H 98H Time
SK	1.No visible mechanical damage.	1.Temperature: 60±2°C
	2)	
Dames Heat	2. Inductance change: Within ±10%.(Mn-Zn:	
Damp Heat	Within ≤30%)	3.Duration: 96±2 hours.
(Steady States)	3.Q factor change: Within ±20%.	4. The chip shall be stabilized at normal condition
Reference documents:	the Marine	for 1~2 hoursbefore measuring.
GB/T 2423.3-2016	ST	60°C Temp & Humidity 93%RH High temperature
恒定湿热试验	<u> </u>	Room High humidity
		Conditions
exh. III	7 1/1/12	0 96H 97H 98H Time
Heat endurance of	1.No significant defects in appearance.	1.Refer to the above reflow curve and go through
Reflow soldering	2. △ L/L ≦ 10% (Mn-Zn: △ L/L ≦ 30% )	the reflow for twice.
Reference documents:	3. △ Q/Q ≦ 30% (SMD series only)	2.The peak temperature : 260+0/-5℃
GJB 360B-2009	4. △ DCR/DCR ≦ 10%	
回流焊耐热性试验		Dr.,
	No case deformation or change in	To dip parts into IPA solvent for 5±0.5Min,then
Resistance to solvent	appearance or obliteration of marking	drying them at room temp for 5Min,at last ,to
test	in the second second	brushing making 10 times.
Reference documents:	11/23/15	
IEC 68-2-45:1993	c th' in	1 1/1/12.
耐溶剂性试验	2)	St
Overload test	1.During the test no smoke, no peculiar,	- X
Reference documents:	smell, no fire	SANTE STATE
JIS C5311-6.13	The characteristic is normal after test	Apply twice as rated current for 5 minutes.
过负荷试验	ST CHI	
, x	5,	SKI
voltage resistance test	1.During the test no breakdown	<u> </u>
Reference documents:	2.The characteristic is normal after test	A Facility and the same of the
MIL-STD-202G Method	W. III	1. For parts with two coils
301	Sr	DC1000V, Current: 1mA, Time: 1Min.     Refer to catalogue of specific products
绝缘耐压测试		o. Note: to catalogue of specific products
11/2717		1



◆推荐回流焊温度曲线

### Recommended reflow soldering curve:



The recommended reflow conditions as above graph, is set according to our soldering equipment. DUE to various manufactures may have different reflow soldering equipment, products, process conditions, set methods. And so on, when setting the reflow conditions, Please adjust and confirm according to users' environment/equipment.



#### 使用注意事项

### REMINDERS FOR USING THESE PRODUCTS



保存时间为12 个月以内,保存条件(温度5~40°C以下、湿度35 ~ 66%RH 以下),需充分注意。
 若超过保存时间,端子电极的可焊性将可能老化。

The storage period is within 12 months. Be sure to follow the storage conditions (temperature: 5~40°C, humidity: 35 to 65% RH or less). If the storage period elapses, the soldering of the terminal electrodes may deteriorate.

• 请勿在气体腐蚀环境(盐、酸、碱等)下使用和保存。

Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).

• 手上的油脂会导致可焊性降低,应避免用手直接接触端子。

Don't touch electrodes directly with bare hands as oil secretions may inhibit soldering Always ensure optimum conditions for soldering.

请小心轻拿轻放,避免由于产品的跌落或取出不当而导致的损坏。

Please always handle products carefully to prevent any damage caused bydropping down or inappropriate removing.

• 端子过度弯曲会导致断线,请不要过度弯曲端子。

Don't bend the terminals with excessive stress in case of any wire fracture.

• 不要清洗产品, 如需要清洗时请联系我司。

Don't rinse coils by yourself and please contact SXN if necessary.

• 请勿将本产品靠近磁铁或带有磁力的物体

Don't expose the products to magnets or magnetic fields

- 在实施焊接前,请务必进行预热。预热温度与焊接温度及芯片温度的温度差要在150°C 以内。
  Before soldering, be sure to preheat components. The preheating temperature should be set so that the temperature difference between the solder temperature and chip temperature does not exceed 150°C.
- 安装后的焊接修正应在规格书规定的条件范围内。若加热过度可能导致短路、性能降低、寿命减少。
   Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
- 装置会因通电而自我发热(温度上升),因此在热设计方面需留有充分余地。
   Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
- 非磁屏蔽型在基板设计时需注意配置线圈,受到电磁干扰可能会导致误动作。
  Carefully lay out the coil for the circuit board design of the non-magnetic shield type. A malfunction may occur due to magnetic interference.