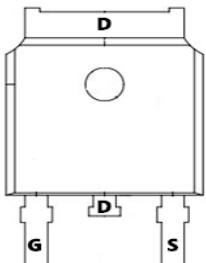
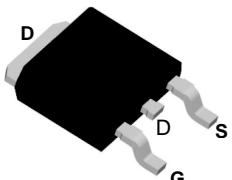
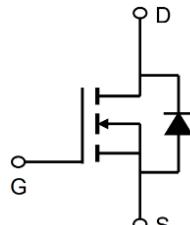


**TMN10020D**
**N-Channel Enhancement Mosfet**

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>• Low <math>R_{DS(ON)}</math></li> <li>• RoHS and Halogen-Free Compliant</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>• Load switch</li> <li>• PWM</li> </ul>	<p><b>General Features</b></p> <p><math>V_{DS} = 100V</math> <math>I_D = 20A</math></p> <p><math>R_{DS(ON)} = 65m\Omega</math>(typ.) @ <math>V_{GS} = 10V</math></p> <p>100% UIS Tested 100% <math>R_g</math> Tested</p> 																																												
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**TMN10020D**
**N-Channel Enhancement Mosfet**
**Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100	-	-	V
Gate-body Leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current T <sub>J</sub> =25°C	I <sub>DSS</sub>	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1	μA
T <sub>J</sub> =100°C			-	-	100	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.2	-	2.5	V
Drain-Source on-Resistance <sup>4</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5A	-	65	90	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3A	-	75	105	
Forward Transconductance <sup>4</sup>	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =5A	-	12	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1MHz	-	1220	-	pF
Output Capacitance	C <sub>oss</sub>		-	53	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	42	-	
Gate Resistance	R <sub>g</sub>	f = 1MHz	-	1.3	-	Ω
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> =5A	-	20.6	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	4	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	3.7	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 50V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = 5A	-	4.7	-	ns
Rise Time	t <sub>r</sub>		-	21	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	20	-	
Fall Time	t <sub>f</sub>		-	16	-	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1A, V <sub>GS</sub> = 0V	-	-	1.2	V
Continuous Source Current	T <sub>C</sub> =25°C	I <sub>S</sub>	-	-	20	A

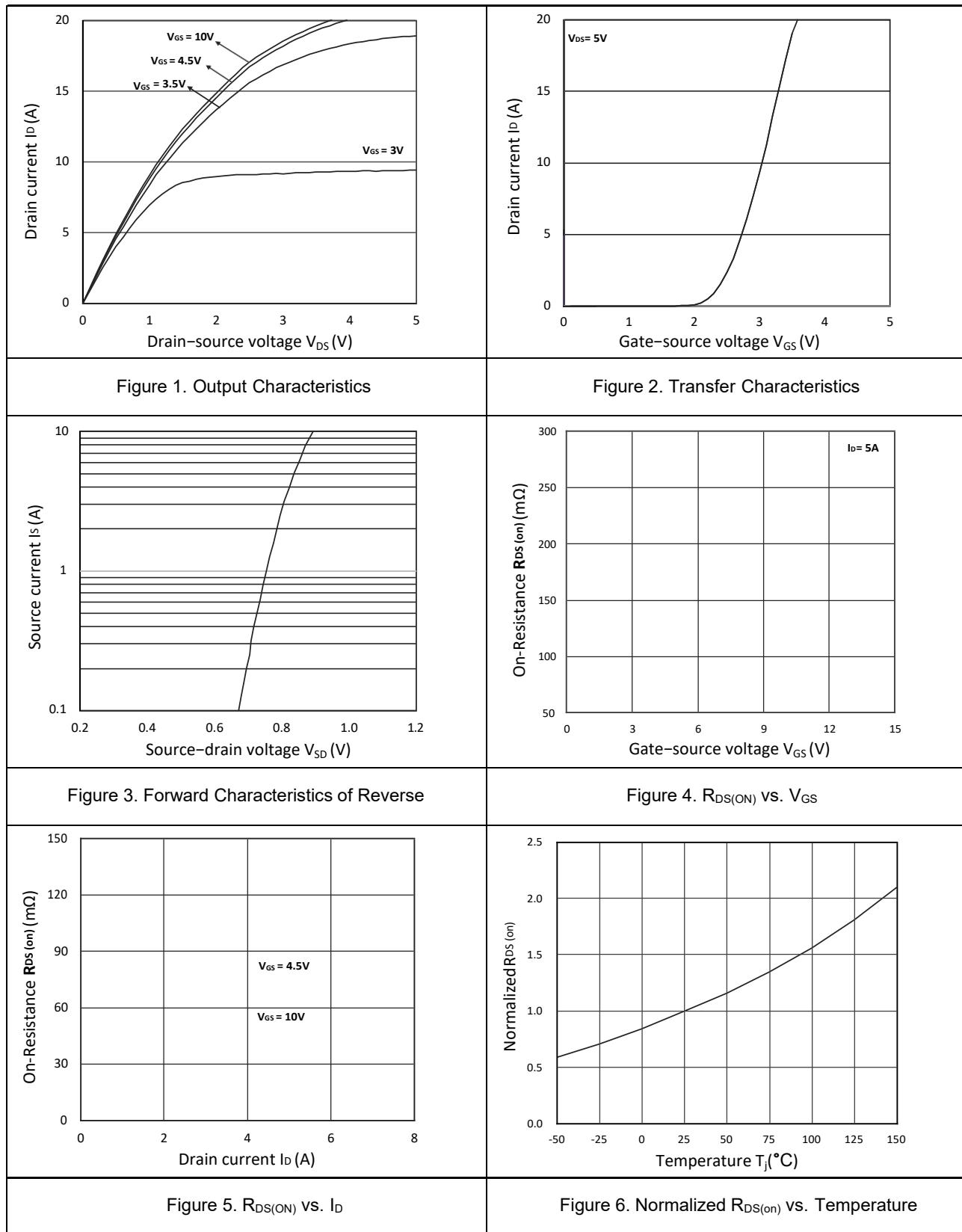
**Notes:**

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=8A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

## **TMN10020D**

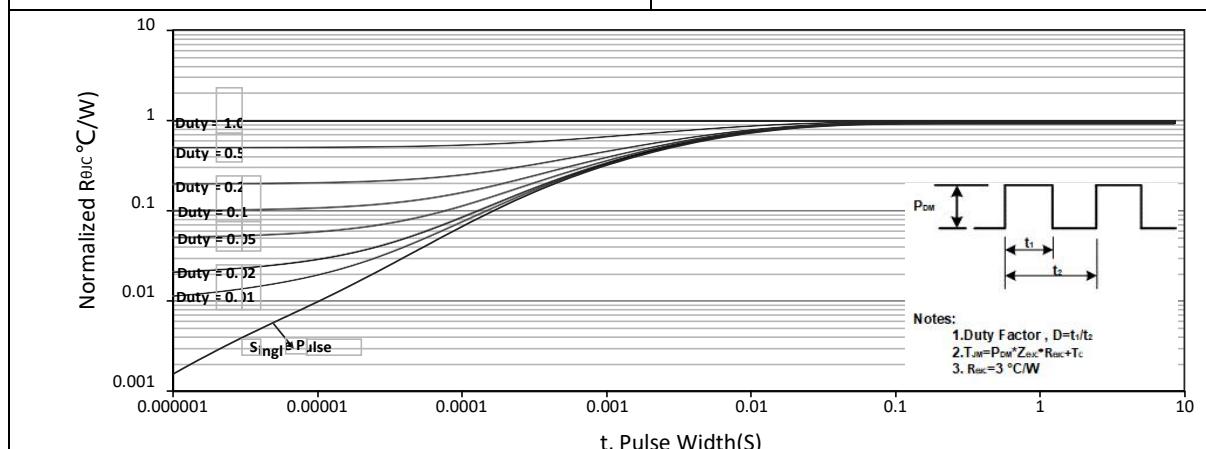
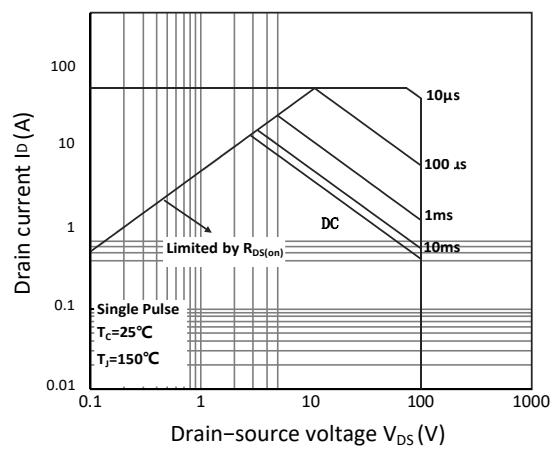
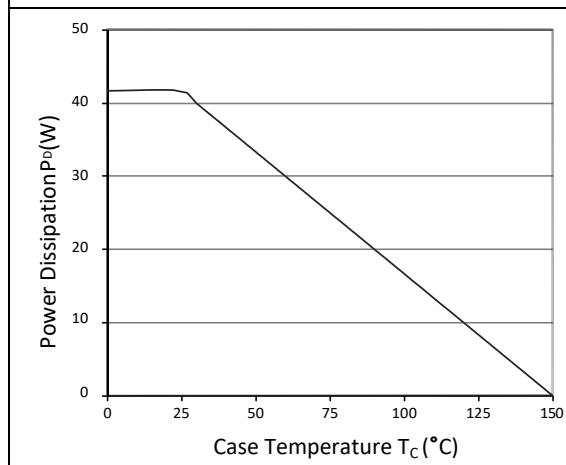
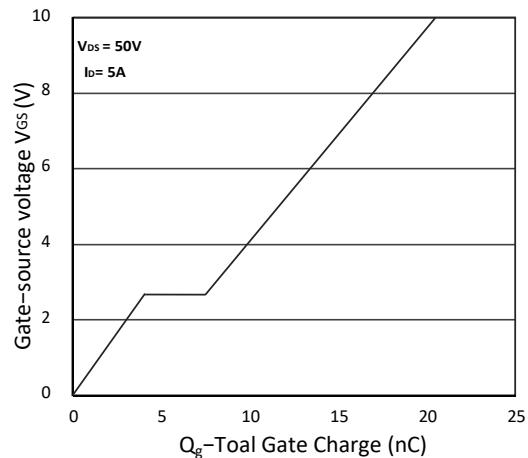
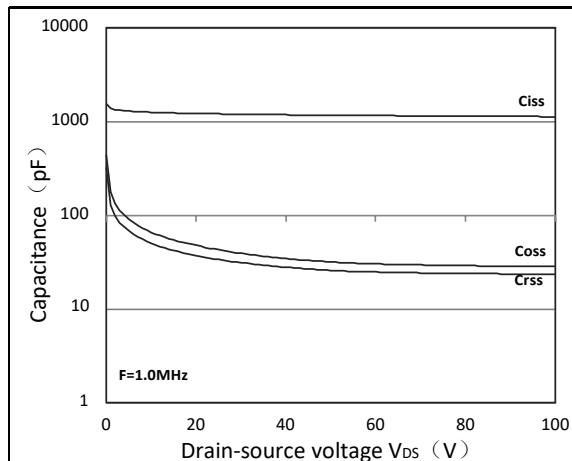
## **N-Channel Enhancement Mosfet**

### **Typical Characteristics**

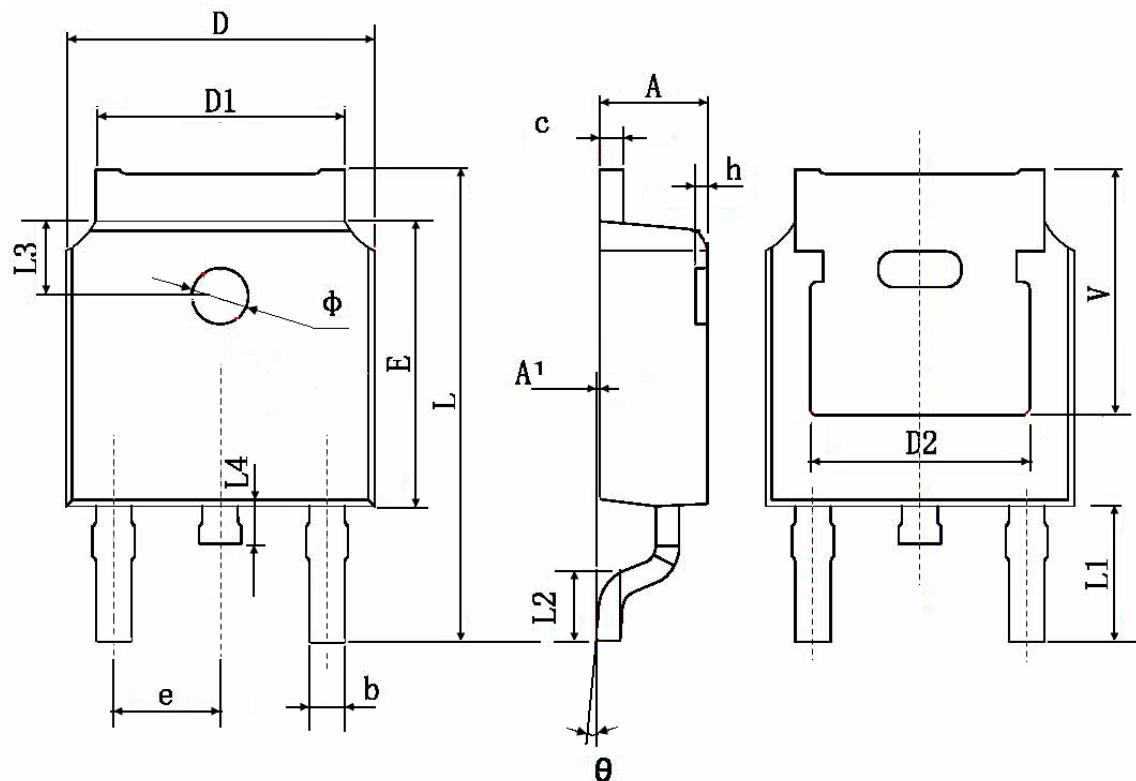


## **TMN10020D**

## **N-Channel Enhancement Mosfet**



## Package Information:TO-252-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	