



AiP74AHC/AHCT1G08

Single 2-input And Gate

Product Specification

Specification Revision History:

Version	Date	Description
2018-06-A1	2018-06	New
2021-09-A2	2021-09	Modify ambient temperature to -40°C~+105°C and add electrical characteristics of -40°C~+105°C
2021-10-A3	2021-10	Modify Ordering Information
2021-12-A4	2021-12	Modify Ordering Information
2022-03-A5	2022-03	Modify ordering information note 1



1、 General Description

AiP74AHC1G08 and AiP74AHCT1G08 are high-speed Si-gate CMOS devices. They provide a 2-input AND function.

The AHC device has CMOS input switching levels and supply voltage range 2V to 5.5V.

The AHCT device has TTL input switching levels and supply voltage range 4.5V to 5.5V.

Features:

- Symmetrical output impedance
- Low power dissipation
- Balanced propagation delays
- Specified from -40°C to +105°C
- Packaging information: SOT23-5/SOT353

Ordering Information:

Reel packing specifications:

Part number	Packaging form	Marking code	Reel quantity	Boxed reel quantity	Notes
AiP74AHC1G08GB235.TR	SOT23-5	CLXX	3000 PCS/reel	30000 PCS/box	Dimensions of plastic enclosure: 2.9mm×1.6mm Pin spacing:0.95mm
AiP74AHC1G08GC353.TR	SOT353	CLXX	3000 PCS/reel	30000 PCS/box	Dimensions of plastic enclosure: 2.1mm×1.3mm Pin spacing:0.65mm
AiP74AHCT1G08GB235.TR	SOT23-5	CMXX	3000 PCS/reel	30000 PCS/box	Dimensions of plastic enclosure: 2.9mm×1.6mm Pin spacing:0.95mm
AiP74AHCT1G08GC353.TR	SOT353	CMXX	3000 PCS/reel	30000 PCS/box	Dimensions of plastic enclosure: 2.1mm×1.3mm Pin spacing:0.65mm

Note 1: "XX" refers to variable content, meaning year and package batch serial number.

Note 2: If the physical information is inconsistent with the ordering information, please refer to the actual product.



2、Block Diagram And Pin Description

2.1、Block Diagram

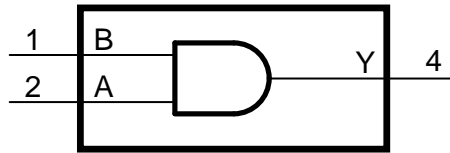


Figure 1. Logic symbol



Figure 2. IEC logic symbol

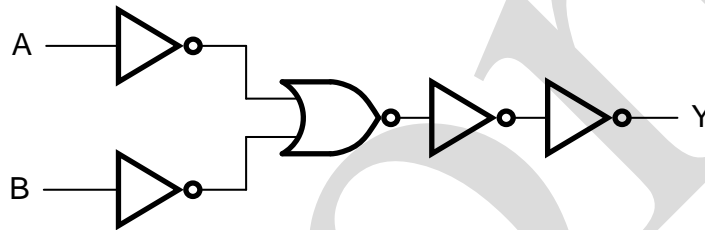
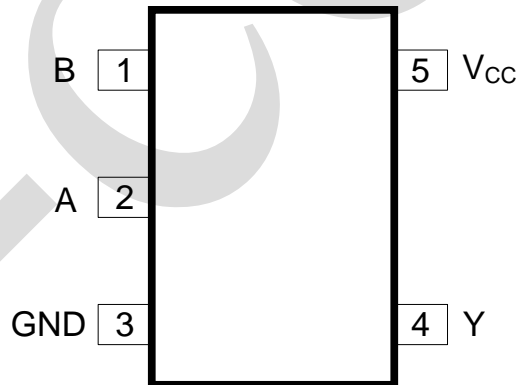


Figure 3. Logic diagram

2.2、Pin Configurations



2.3、Pin Description

Pin No.	Pin Name	Description
1	B	data input
2	A	data input
3	GND	ground (0V)
4	Y	data output
5	V _{CC}	supply voltage



2.4、Function Table

Input		Output
A	B	Y
L	L	L
L	H	L
H	L	L
H	H	H

Note: H=HIGH voltage level; L=LOW voltage level.

3、Electrical Parameter

3.1、Absolute Maximum Ratings

($T_{amb}=25^{\circ}\text{C}$, All voltage referenced to GND, unless otherwise specified)

Characteristic	Symbol	Conditions	Min.	Max.	Unit
supply voltage	V_{CC}	-	-0.5	+7.0	V
input voltage	V_I	-	-0.5	+7.0	V
input clamping current	I_{IK}	$V_I < -0.5\text{V}$	-20	-	mA
output clamping current	I_{OK}	$V_O < -0.5\text{V}$ or $V_O > V_{CC} + 0.5\text{V}$	-	± 20	mA
output current	I_O	$-0.5\text{V} < V_O < V_{CC} + 0.5\text{V}$	-	± 25	mA
supply current	I_{CC}	-	-	75	mA
ground current	I_{GND}	-	-75	-	mA
storage temperature	T_{stg}	-	-65	+150	$^{\circ}\text{C}$
total power dissipation	P_{tot}	-	-	250	mW
soldering temperature	T_L	10s	250		$^{\circ}\text{C}$

3.2、Recommended Operating Conditions

(Voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
AiP74AHC1G08						
supply voltage	V_{CC}	-	2.0	5.0	5.5	V
input voltage	V_I	-	0	-	5.5	V
output voltage	V_O	-	0	-	V_{CC}	V
ambient temperature	T_{amb}	-	-40	-	+105	$^{\circ}\text{C}$
input transition rise and fall rate	$\Delta t/\Delta V$	$V_{CC}=3.3\text{V} \pm 0.3\text{V}$	-	-	100	ns/V
		$V_{CC}=5.0\text{V} \pm 0.5\text{V}$	-	-	20	ns/V
AiP74AHCT1G08						
supply voltage	V_{CC}	-	4.5	5.0	5.5	V
input voltage	V_I	-	0	-	5.5	V
output voltage	V_O	-	0	-	V_{CC}	V
ambient temperature	T_{amb}	-	-40	-	+105	$^{\circ}\text{C}$
input transition rise and fall rate	$\Delta t/\Delta V$	$V_{CC}=5.0\text{V} \pm 0.5\text{V}$	-	-	20	ns/V



3.3、Electrical Characteristics

3.3.1、DC Characteristics 1

($T_{amb}=25^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
AiP74AHC1G08							
HIGH-level input voltage	V_{IH}	$V_{CC}=2.0\text{V}$	1.5	-	-	V	
		$V_{CC}=3.0\text{V}$	2.1	-	-	V	
		$V_{CC}=5.5\text{V}$	3.85	-	-	V	
LOW-level input voltage	V_{IL}	$V_{CC}=2.0\text{V}$	-	-	0.5	V	
		$V_{CC}=3.0\text{V}$	-	-	0.9	V	
		$V_{CC}=5.5\text{V}$	-	-	1.65	V	
HIGH-level output voltage	V_{OH}	$V_I = V_{IH} \text{ or } V_{IL}$	$I_O=-50\mu\text{A}; V_{CC}=2.0\text{V}$	1.9	2.0	-	V
			$I_O=-50\mu\text{A}; V_{CC}=3.0\text{V}$	2.9	3.0	-	V
			$I_O=-50\mu\text{A}; V_{CC}=4.5\text{V}$	4.4	4.5	-	V
			$I_O=-4\text{mA}; V_{CC}=3.0\text{V}$	2.58	-	-	V
			$I_O=-8\text{mA}; V_{CC}=4.5\text{V}$	3.94	-	-	V
LOW-level output voltage	V_{OL}	$V_I = V_{IH} \text{ or } V_{IL}$	$I_O=50\mu\text{A}; V_{CC}=2.0\text{V}$	-	0	0.1	V
			$I_O=50\mu\text{A}; V_{CC}=3.0\text{V}$	-	0	0.1	V
			$I_O=50\mu\text{A}; V_{CC}=4.5\text{V}$	-	0	0.1	V
			$I_O=4\text{mA}; V_{CC}=3.0\text{V}$	-	-	0.36	V
			$I_O=8\text{mA}; V_{CC}=4.5\text{V}$	-	-	0.36	V
input leakage current	I_I	$V_I=5.5\text{V}$ or GND; $V_{CC}=0\text{V}$ to 5.5V	-	-	0.1	μA	
supply current	I_{CC}	$V_I=V_{CC}$ or GND; $I_O=0\text{A}$; $V_{CC}=5.5\text{V}$	-	-	1.0	μA	
input capacitance	C_I	-	-	1.5	10	pF	
AiP74AHCT1G08							
HIGH-level input voltage	V_{IH}	$V_{CC}=4.5\text{V}$ to 5.5V	2.0	-	-	V	
LOW-level input voltage	V_{IL}	$V_{CC}=4.5\text{V}$ to 5.5V	-	-	0.8	V	
HIGH-level output voltage	V_{OH}	$V_I = V_{IH} \text{ or } V_{IL}$	$I_O=-50\mu\text{A}; V_{CC}=4.5\text{V}$	4.4	4.5	-	V
			$I_O=-8\text{mA}; V_{CC}=4.5\text{V}$	3.94	-	-	V
LOW-level output voltage	V_{OL}	$V_I = V_{IH} \text{ or } V_{IL}$	$I_O=50\mu\text{A}; V_{CC}=4.5\text{V}$	-	0	0.1	V
			$I_O=8\text{mA}; V_{CC}=4.5\text{V}$	-	-	0.36	V
input leakage current	I_I	$V_I=5.5\text{V}$ or GND; $V_{CC}=0\text{V}$ to 5.5V	-	-	0.1	μA	
supply current	I_{CC}	$V_I=V_{CC}$ or GND; $I_O=0\text{A}$; $V_{CC}=5.5\text{V}$	-	-	1.0	μA	
additional supply current	ΔI_{CC}	per input pin; $V_I=3.4\text{V}$; other inputs at V_{CC} or GND; $I_O=0\text{A}; V_{CC}=5.5\text{V}$	-	-	1.35	mA	
input capacitance	C_I	-	-	1.5	10	pF	



3.3.2、DC Characteristics 2

($T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
AiP74AHC1G08							
HIGH-level input voltage	V_{IH}	$V_{CC}=2.0\text{V}$	1.5	-	-	V	
		$V_{CC}=3.0\text{V}$	2.1	-	-	V	
		$V_{CC}=5.5\text{V}$	3.85	-	-	V	
LOW-level input voltage	V_{IL}	$V_{CC}=2.0\text{V}$	-	-	0.5	V	
		$V_{CC}=3.0\text{V}$	-	-	0.9	V	
		$V_{CC}=5.5\text{V}$	-	-	1.65	V	
HIGH-level output voltage	V_{OH}	$V_I = V_{IH}$ or V_{IL}	$I_O=-50\mu\text{A}; V_{CC}=2.0\text{V}$	1.9	-	-	V
			$I_O=-50\mu\text{A}; V_{CC}=3.0\text{V}$	2.9	-	-	V
			$I_O=-50\mu\text{A}; V_{CC}=4.5\text{V}$	4.4	-	-	V
			$I_O=-4\text{mA}; V_{CC}=3.0\text{V}$	2.48	-	-	V
			$I_O=-8\text{mA}; V_{CC}=4.5\text{V}$	3.8	-	-	V
LOW-level output voltage	V_{OL}	$V_I = V_{IH}$ or V_{IL}	$I_O=50\mu\text{A}; V_{CC}=2.0\text{V}$	-	-	0.1	V
			$I_O=50\mu\text{A}; V_{CC}=3.0\text{V}$	-	-	0.1	V
			$I_O=50\mu\text{A}; V_{CC}=4.5\text{V}$	-	-	0.1	V
			$I_O=4\text{mA}; V_{CC}=3.0\text{V}$	-	-	0.44	V
			$I_O=8\text{mA}; V_{CC}=4.5\text{V}$	-	-	0.44	V
input leakage current	I_I	$V_I=5.5\text{V}$ or GND; $V_{CC}=0\text{V}$ to 5.5V	-	-	1.0	μA	
supply current	I_{CC}	$V_I=V_{CC}$ or GND; $I_O=0\text{A}$; $V_{CC}=5.5\text{V}$	-	-	10	μA	
input capacitance	C_I	-	-	-	10	pF	
AiP74AHCT1G08							
HIGH-level input voltage	V_{IH}	$V_{CC}=4.5\text{V}$ to 5.5V	2.0	-	-	V	
LOW-level input voltage	V_{IL}	$V_{CC}=4.5\text{V}$ to 5.5V	-	-	0.8	V	
HIGH-level output voltage	V_{OH}	$V_I = V_{IH}$ or V_{IL}	$I_O=-50\mu\text{A}; V_{CC}=4.5\text{V}$	4.4	-	-	V
			$I_O=-8\text{mA}; V_{CC}=4.5\text{V}$	3.8	-	-	V
LOW-level output voltage	V_{OL}	$V_I = V_{IH}$ or V_{IL}	$I_O=50\mu\text{A}; V_{CC}=4.5\text{V}$	-	-	0.1	V
			$I_O=8\text{mA}; V_{CC}=4.5\text{V}$	-	-	0.44	V
input leakage current	I_I	$V_I=5.5\text{V}$ or GND; $V_{CC}=0\text{V}$ to 5.5V	-	-	1.0	μA	
supply current	I_{CC}	$V_I=V_{CC}$ or GND; $I_O=0\text{A}$; $V_{CC}=5.5\text{V}$	-	-	10	μA	
additional supply current	ΔI_{CC}	per input pin; $V_I=3.4\text{V}$; other inputs at V_{CC} or GND; $I_O=0\text{A}; V_{CC}=5.5\text{V}$	-	-	1.5	mA	
input capacitance	C_I	-	-	-	10	pF	



3.3.3、DC Characteristics 3

($T_{amb} = -40^{\circ}\text{C}$ to $+105^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
AiP74AHC1G08							
HIGH-level input voltage	V_{IH}	$V_{CC}=2.0\text{V}$	1.5	-	-	V	
		$V_{CC}=3.0\text{V}$	2.1	-	-	V	
		$V_{CC}=5.5\text{V}$	3.85	-	-	V	
LOW-level input voltage	V_{IL}	$V_{CC}=2.0\text{V}$	-	-	0.5	V	
		$V_{CC}=3.0\text{V}$	-	-	0.9	V	
		$V_{CC}=5.5\text{V}$	-	-	1.65	V	
HIGH-level output voltage	V_{OH}	$V_I = V_{IH}$ or V_{IL}	$I_O=-50\mu\text{A}; V_{CC}=2.0\text{V}$	1.9	-	-	V
			$I_O=-50\mu\text{A}; V_{CC}=3.0\text{V}$	2.9	-	-	V
			$I_O=-50\mu\text{A}; V_{CC}=4.5\text{V}$	4.4	-	-	V
			$I_O=-4\text{mA}; V_{CC}=3.0\text{V}$	2.4	-	-	V
			$I_O=-8\text{mA}; V_{CC}=4.5\text{V}$	3.7	-	-	V
LOW-level output voltage	V_{OL}	$V_I = V_{IH}$ or V_{IL}	$I_O=50\mu\text{A}; V_{CC}=2.0\text{V}$	-	-	0.1	V
			$I_O=50\mu\text{A}; V_{CC}=3.0\text{V}$	-	-	0.1	V
			$I_O=50\mu\text{A}; V_{CC}=4.5\text{V}$	-	-	0.1	V
			$I_O=4\text{mA}; V_{CC}=3.0\text{V}$	-	-	0.55	V
			$I_O=8\text{mA}; V_{CC}=4.5\text{V}$	-	-	0.55	V
input leakage current	I_I	$V_I=5.5\text{V}$ or GND; $V_{CC}=0\text{V}$ to 5.5V	-	-	2.0	μA	
supply current	I_{CC}	$V_I=V_{CC}$ or GND; $I_O=0\text{A}$; $V_{CC}=5.5\text{V}$	-	-	40	μA	
input capacitance	C_I	-	-	-	10	pF	
AiP74AHCT1G08							
HIGH-level input voltage	V_{IH}	$V_{CC}=4.5\text{V}$ to 5.5V	2.0	-	-	V	
LOW-level input voltage	V_{IL}	$V_{CC}=4.5\text{V}$ to 5.5V	-	-	0.8	V	
HIGH-level output voltage	V_{OH}	$V_I = V_{IH}$ or V_{IL}	$I_O=-50\mu\text{A}; V_{CC}=4.5\text{V}$	4.4	-	-	V
			$I_O=-8\text{mA}; V_{CC}=4.5\text{V}$	3.7	-	-	V
LOW-level output voltage	V_{OL}	$V_I = V_{IH}$ or V_{IL}	$I_O=50\mu\text{A}; V_{CC}=4.5\text{V}$	-	-	0.1	V
			$I_O=8\text{mA}; V_{CC}=4.5\text{V}$	-	-	0.55	V
input leakage current	I_I	$V_I=5.5\text{V}$ or GND; $V_{CC}=0\text{V}$ to 5.5V	-	-	2.0	μA	
supply current	I_{CC}	$V_I=V_{CC}$ or GND; $I_O=0\text{A}$; $V_{CC}=5.5\text{V}$	-	-	40	μA	
additional supply current	ΔI_{CC}	per input pin; $V_I=3.4\text{V}$; other inputs at V_{CC} or GND; $I_O=0\text{A}; V_{CC}=5.5\text{V}$	-	-	1.5	mA	
input capacitance	C_I	-	-	-	10	pF	



3.3.4、AC Characteristics 1

($T_{amb}=25^{\circ}C$, $GND=0V$, $t_r=t_f \leq 3.0ns$, unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
AiP74AHC1G08							
propagation delay	t_{pd}	A and B to Y; see Figure 5 ^[1]	$V_{CC}=3.0V$ to $3.6V$ ^[2]				
			$C_L=15pF$	-	4.6	8.8	ns
			$C_L=50pF$	-	6.5	12.3	ns
			$V_{CC}=4.5V$ to $5.5V$ ^[3]				
			$C_L=15pF$	-	3.2	5.9	ns
			$C_L=50pF$	-	4.6	7.9	ns
Power dissipation capacitance	C_{PD}	per buffer; $C_L=50pF$; $f=1MHz$; $V_I = GND$ to V_{CC} ^[4]	-	17	-	pF	
AiP74AHCT1G08							
propagation delay	t_{pd}	A and B to Y; see Figure 5 ^[1]	$V_{CC}=4.5V$ to $5.5V$ ^[3]				
			$C_L=15pF$	-	3.6	6.2	ns
			$C_L=50pF$	-	5.1	7.9	ns
Power dissipation capacitance	C_{PD}	per buffer; $C_L=50pF$; $f=1MHz$; $V_I = GND$ to V_{CC} ^[4]	-	19	-	pF	

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

[2] Typical values are measured at $V_{CC}=3.3V$.

[3] Typical values are measured at $V_{CC}=5.0V$.

[4] C_{PD} is used to determine the dynamic power dissipation (P_D in uW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i =input frequency in MHz; f_o =output frequency in MHz;

C_L =output load capacitance in pF;

V_{CC} =supply voltage in Volts.



3.3.5、AC Characteristics 2

($T_{amb}=-40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $\text{GND}=0\text{V}$, $t_r=t_f\leq 3.0\text{ns}$, unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
AiP74AHC1G08							
propagation delay	t_{pd}	A and B to Y; see Figure 5 ^[1]	$V_{CC}=3.0\text{V}$ to 3.6V ^[2]				
			$C_L=15\text{pF}$	1.0	-	10.5	ns
			$C_L=50\text{pF}$	1.0	-	14.0	ns
			$V_{CC}=4.5\text{V}$ to 5.5V ^[3]				
			$C_L=15\text{pF}$	1.0	-	7.0	ns
			$C_L=50\text{pF}$	1.0	-	9.0	ns
AiP74AHCT1G08							
propagation delay	t_{pd}	A and B to Y; see Figure 5 ^[1]	$V_{CC}=4.5\text{V}$ to 5.5V ^[3]				
			$C_L=15\text{pF}$	1.0	-	7.1	ns
			$C_L=50\text{pF}$	1.0	-	9.0	ns

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

[2] Typical values are measured at $V_{CC}=3.3\text{V}$.

[3] Typical values are measured at $V_{CC}=5\text{V}$.

3.3.6、AC Characteristics 3

($T_{amb}=-40^{\circ}\text{C}$ to $+105^{\circ}\text{C}$, $\text{GND}=0\text{V}$, $t_r=t_f\leq 3.0\text{ns}$, unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	
AiP74AHC1G08							
propagation delay	t_{pd}	A and B to Y; see Figure 5 ^[1]	$V_{CC}=3.0\text{V}$ to 3.6V ^[2]				
			$C_L=15\text{pF}$	1.0	-	12.0	ns
			$C_L=50\text{pF}$	1.0	-	16.0	ns
			$V_{CC}=4.5\text{V}$ to 5.5V ^[3]				
			$C_L=15\text{pF}$	1.0	-	8.0	ns
			$C_L=50\text{pF}$	1.0	-	10.5	ns
AiP74AHCT1G08							
propagation delay	t_{pd}	A and B to Y; see Figure 5 ^[1]	$V_{CC}=4.5\text{V}$ to 5.5V ^[3]				
			$C_L=15\text{pF}$	1.0	-	8.0	ns
			$C_L=50\text{pF}$	1.0	-	10.5	ns

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

[2] Typical values are measured at $V_{CC}=3.3\text{V}$.

[3] Typical values are measured at $V_{CC}=5\text{V}$.



4、 Testing Circuit

4.1、 AC Testing Circuit

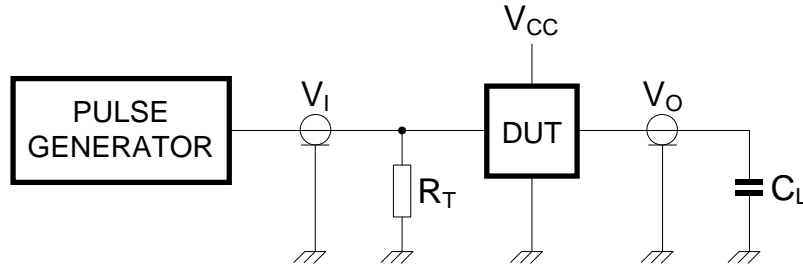


Figure 4. Test circuit for measuring switching times

Definitions for test circuit:

C_L =Load capacitance including jig and probe capacitance.

R_T =Termination resistance should be equal to output impedance Z_o of the pulse generator.

4.2、 AC Testing Waveforms

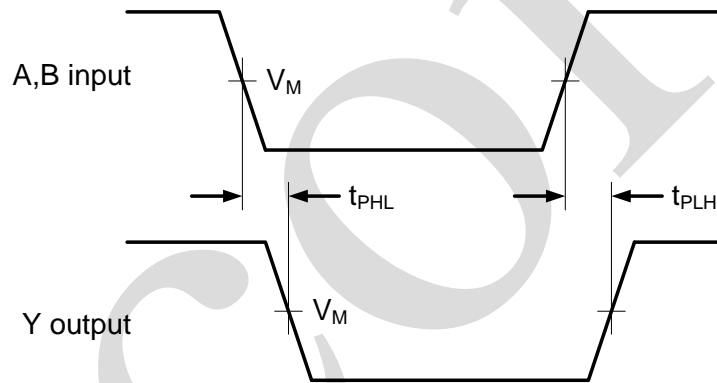


Figure 5. Input (A and B) to output (Y) propagation delays

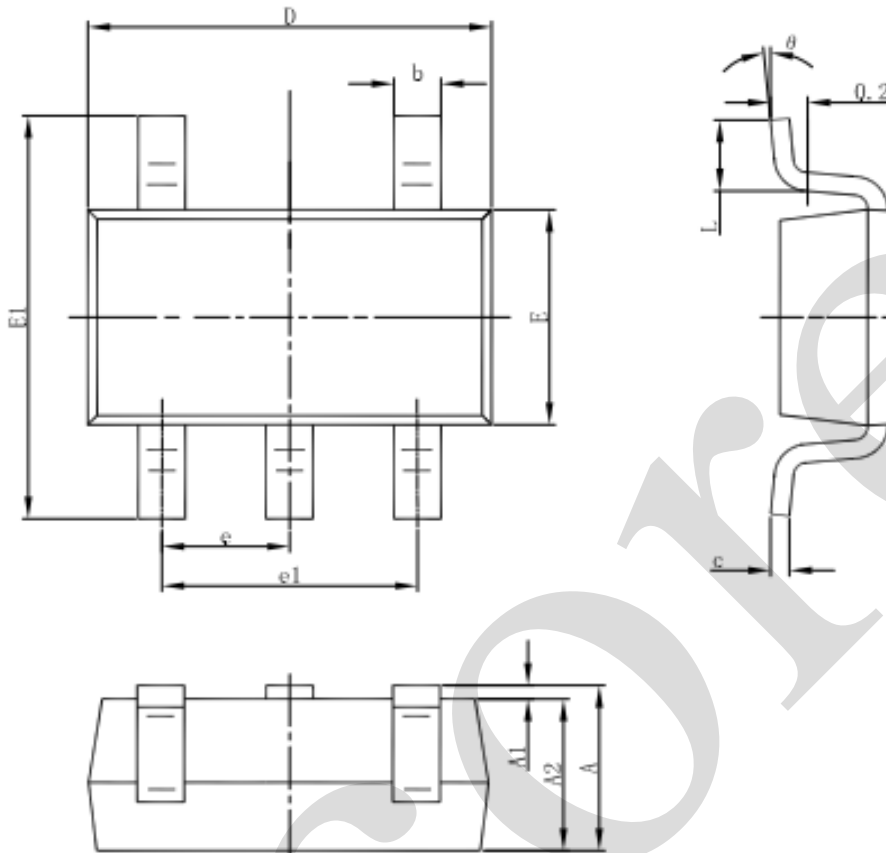
4.3、 Measurement Points

Type	Input		Output
	V_I	V_M	V_M
AiP74AHC1G08	GND to V_{CC}	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
AiP74AHCT1G08	GND to 3.0V	1.5V	$0.5 \times V_{CC}$



5、Package Information

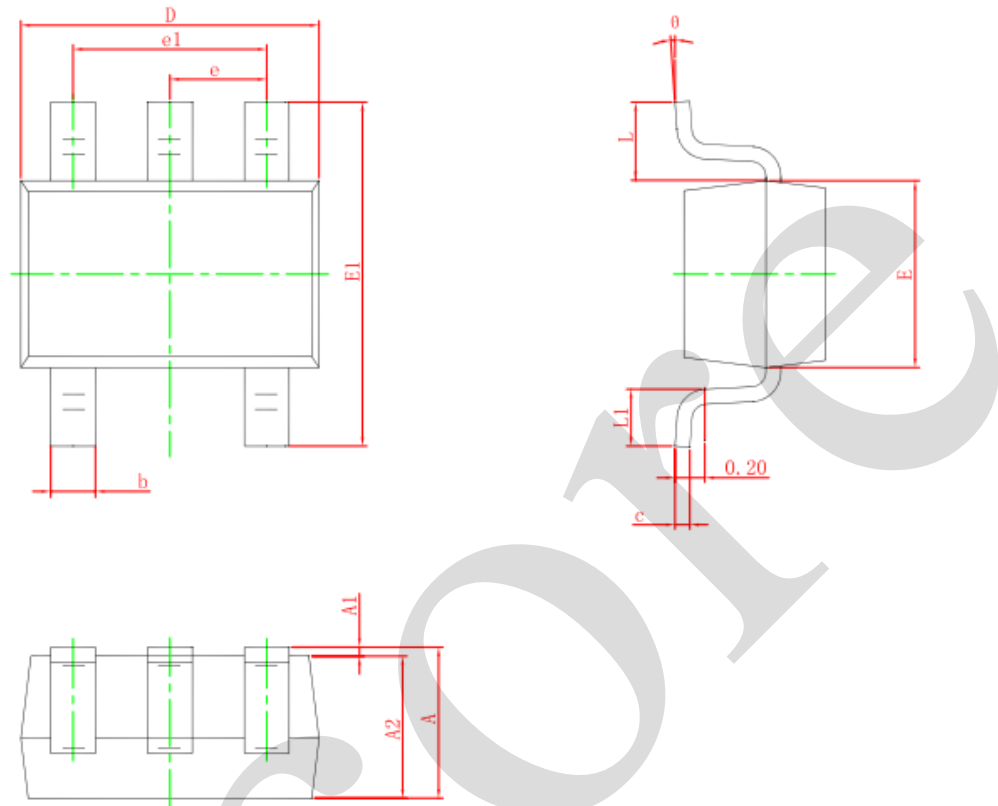
5.1、SOT23-5



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



5.2、SOT353



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.525 REF.		0.021 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°



6、 Statements And Notes

6.1、 The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

6.2、 Notion

Recommended carefully reading this information before the use of this product;

The information in this document are subject to change without notice;

This information is using to the reference only, the company is not responsible for any loss;

The company is not responsible for the any infringement of the third party patents or other rights of the responsibility.