

### General Description

The CMSC5006 combines advanced trench MOSFET technology with a low resistance package to provide extremely low RDS(ON). This device is ideal for load switch and battery protection applications.

### Features

- P-Channel MOSFET
- Low ON-resistance
- Surface Mount Package
- RoHS Compliant

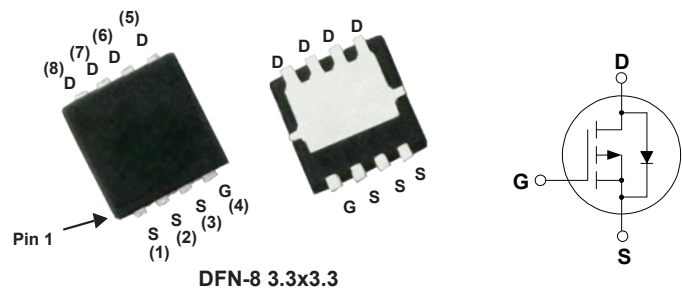
### Product Summary

BVDSS	RDSON	ID
-60V	85mΩ	-15A

### Applications

- High side in DC - DC Buck Converters
- Notebook battery power management
- Load switch in Notebook

### DFN-8 3.3x3.3 Pin Configuration



Type	Package	Marking
CMSC5006	DFN-8 3.3*3.3	CMSC5006

### Absolute Maximum Ratings (TA=25 °C Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current	-15	A
I <sub>DM</sub>	Pulsed Drain Current	-30	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	56	mJ
P <sub>D@T<sub>C</sub>=25°C</sub>	Total Power Dissipation	30	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	---	25	°C/W

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$  , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-6A$	---	---	85	m $\Omega$
		$V_{GS}=-4.5V, I_D=-4A$	---	---	95	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D = -250\mu A$	-1	---	-3	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V$	---	---	-1	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS} = \pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_g$	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	23	---	$\Omega$
$Q_g$	Total Gate Charge	$V_{DS}=-48V, I_D=-3A$ $V_{GS}=-4.5V$	---	13	---	nC
$Q_{gs}$	Gate-Source Charge		---	3	---	
$Q_{gd}$	Gate-Drain Charge		---	7	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-15V, V_{GEN}=-10V, I_D=-1A$ $R_G=3.3\Omega$	---	10	---	ns
$T_r$	Rise Time		---	21	---	
$T_{d(off)}$	Turn-Off Delay Time		---	47	---	
$T_f$	Fall Time		---	10	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-25V, V_{GS}=0V, f=1MHz$	---	1600	---	pF
$C_{oss}$	Output Capacitance		---	76	---	
$C_{rss}$	Reverse Transfer Capacitance		---	50	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_{SD}=-5A$	---	---	-1.2	V

Notes:

1. The test condition is  $V_{DS}=-25V, V_{GS}=-10V, L=0.5mH, I_D=-15A$ .

This product has been designed and qualified for the consumer market.  
 Cmos assumes no liability for customers' product design or applications.  
 Cmos reserves the right to improve product design, functions and reliability without notice.