

#### **80V N-Channel MOSFET**

### **General Description**

The 75NF75 is N-Channel MOSFET, It has specifically been designed to minimize input capacitance and gate charge. The device is therefore suitable in advanced high-efficiency switching applications.

#### **Features**

- Minimize input capacitance and gate charge
- 100% avalanche tested
- Low On-Resistance

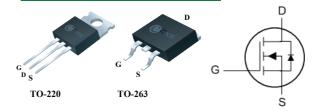
#### **Product Summary**

BVDSS	RDSON	ID
80V	9mΩ	80A

### **Applications**

- Motor Control
- DC-DC converters
- Switching applications

### TO-220/263 Pin Configuration



Туре	Package	Marking
CMP75NF75	TO-220	CMP75NF75
CMB75NF75	TO-263	CMB75NF75

## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage 80		V	
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current	80	Α	
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current	64	Α	
I <sub>DM</sub>	Pulsed Drain Current 320		Α	
EAS	Single Pulse Avalanche Energy <sup>1</sup>	338	mJ	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	200	W	
T <sub>STG</sub>	Storage Temperature Range -55 to 175		$^{\circ}$	
TJ	Operating Junction Temperature Range -55 to 175		$^{\circ}$ C	

### **Thermal Data**

Symbol	Parameter		Max.	Unit	
$R_{ heta JA}$	Thermal Resistance Junction-ambient		62	°C/W	
R <sub>eJC</sub>	Thermal Resistance Junction-case		0.88	°C/W	

# **CMP75NF75/CMB75NF75**



#### 80V N-Channel MOSFET

### Electrical Characteristics ( $T_J=25^{\circ}$ C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	80			V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =30A			9	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ =250uA	2		4	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =80 V, V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =15A		19		S
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.5		Ω
Qg	Total Gate Charge	I <sub>D</sub> =50A		92		
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =40V		25		nC
$Q_{gd}$	Gate-Drain Charge	V <sub>GS</sub> =10 V		31		
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =30V, I <sub>D</sub> =2A		19		
T <sub>r</sub>	Rise Time	R <sub>L</sub> =15Ω		13		ne
$T_{d(off)}$	Turn-Off Delay Time	R <sub>G</sub> =2.5Ω		55		ns
T <sub>f</sub>	Fall Time	V <sub>GS</sub> =10V		16		
C <sub>iss</sub>	Input Capacitance			5300		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		345		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			260		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			80	Α
I <sub>SM</sub>	Pulsed Source Current				320	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =30A , T <sub>J</sub> =25℃		0.9	1.4	V

#### Note:

1. The EAS data shows Max. rating . The test condition is  $V_{DD}=30V,V_{GS}=10V,L=1mH,I_{AS}=26A$ 

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