

CMN3416M

# N-Channel Enhancement Mode Field Effect Transistor

## General Description

The CMN3416M uses advanced trench technology to provide excellent RDS(ON) and low gate charge. This device is suitable for use as a load switch or in PWM applications. It is ESD protected.

## Features

- RDS(ON)<24mΩ @ VGS=4.5V
  - RDS(ON)<28mΩ @ VGS=2.5V
  - SOT-23-3L Package
  - ESD Protected: 2000V

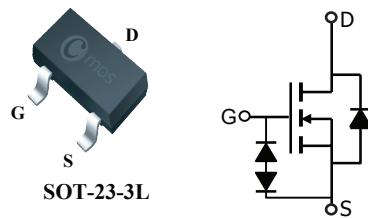
## Product Summary

BVDSS	RDS(on)	ID
20V	24mΩ	6.5A

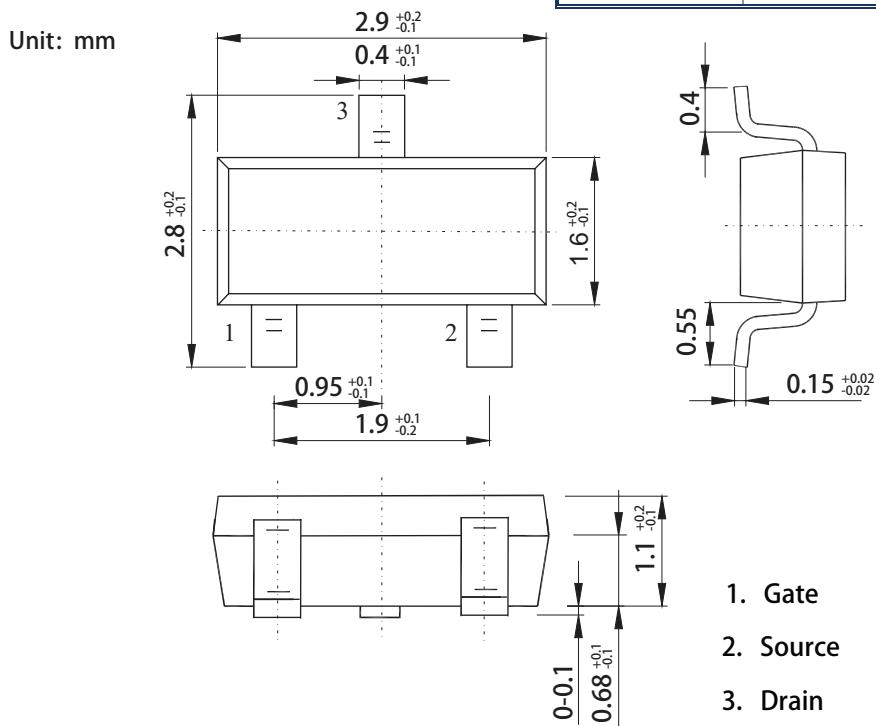
## Applications

- PWM applications
  - Load Switch
  - Power Management
  - PA Switch

## SOT-23-3L Pin Configuration



Type	Package	Marking
CMN3416M	SOT-23-3L	AGSA



## N-Channel Enhancement Mode Field Effect Transistor

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 8$	V
$I_D$	Continuous Drain Current	6.5	A
$I_{DM}$	Pulsed Drain Current	30	A
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation	1.4	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Data

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (PCB mounted)	140	$^\circ\text{C}/\text{W}$

### Electrical Characteristics ( $T_a=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	20	---	---	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=4.5\text{V}$ , $I_D=6.5\text{A}$	---	---	24	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}$ , $I_D=5.5\text{A}$	---	---	28	
		$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	0.4	---	1.0	
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=16\text{V}$ , $V_{GS}=0\text{V}$	---	---	1	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 8\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 10$	$\mu\text{A}$
$R_g$	Gate Resistance	$V_{DS}=0\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	125	---	$\Omega$
$Q_g$	Total Gate Charge	$I_D=6.5\text{A}$	---	10	---	$\text{nC}$
$Q_{gs}$	Gate-Source Charge		---	1.4	---	
$Q_{gd}$	Gate-Drain Charge		---	2.7	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=10\text{V}$ , $I_D=1\text{A}$ $R_L=1.5\Omega$	---	6.2	---	$\text{ns}$
$T_r$	Rise Time		---	12.7	---	
$T_{d(off)}$	Turn-Off Delay Time		---	51.7	---	
$T_f$	Fall Time		---	16	---	
$C_{iss}$	Input Capacitance	$V_{DS}=10\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	470	---	$\text{pF}$
$C_{oss}$	Output Capacitance		---	104	---	
$C_{rss}$	Reverse Transfer Capacitance		---	29	---	

### Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=1\text{A}$	---	---	1.2	V

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