

All-in-One Variable Speed BLDC Motor Driver IC



General Description

FD1252K/Sf is a single-phase full-wave BLDC motor driver IC with embedded Hall-effect sensor and rotation speed(FG) or rotation status(RD) output. It integrates a H-bridge MOS driver, a high and precisely sensitive Hall-effect sensor and a digital control logic with an internal clock for rotor locked driver shutdown and auto re-start, tachometer(FG) or alarm(RD) output logic, and the BLDC motor's speed control function in the TS826 package, it makes the BLDC motors' PCBs(printed circuit boards) design easy and fabrication of the ultra-small BLDC motors and FANs as simple as possible.

For safety, Lock-shutdown function would turn the IC's internal drivers off avoiding over-heat when the rotor is locked, and the IC will try to re-start the rotor's torque after the time of these drivers' shutdown.

The IC contains the PWM variable speed control and the tachometer(FG) or alarm(RD) signal output function, the external succeeding system could control the BLDC motor speed and readout the BLDC motor's speed or status from the signal pin of FD1252K/Sf IC.

Thermal-shutdown protection(TSD) ensures the internal drivers of the IC are operating under a safe operating temperature range, and all the protection mechanisms mentioned above combine to provide a complete protecting scenario for the BLDC motor systems, avoid any possible damages and guarantee a correct and safe operation.

Features

- > Built-in high sensitivity Hall-effect sensor
- H-bridge MOS driver
- > Output dynamic hard-switching to reduce vibration and acoustic noise
- Rotor lock shutdown & auto-restart function
- > PWM variable speed control
- > Tachometer(FG) or alarm(RD) signal output
- Thermal shutdown protection(TSD)
- > Available in TS826 package
- For 12V BLDC motor / FAN systems

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Pin Descriptions



Name	I/O	No.	Description			
FG	0	1	Tachometer Signal Output			
VSS	G	2	IC Ground			
NO	0	3	Driver Output 1			
SO	0	4	Driver Output 2			
PWM	I	5	PWM Variable Speed Control			
VDD	Р	6	IC Power Supply			

Legend: I=input, O=output, I/O=input/output, P=power supply, G=ground



Name	I/O	No.	Description		
RD	0	1	Alarm Signal Output		
VSS	G	2	IC Ground		
NO	0	3	Driver Output 1		
SO	0	4	Driver Output 2		
PWM	Ι	5	PWM Variable Speed Control		
VDD	Р	6	IC Power Supply		

Legend: I=input, O=output, I/O=input/output, P=power supply, G=ground

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Marking Information



Figure 2

Halogen Free: Halogen free product indicator
Lot Number: Wafer lot number's last two digits
For Example: XX680 → 86
Year: Production year's last digit
Internal ID: Internal Identification Code
Part Number Code: Part number identification code for this product.



Block Diagram





Functional Descriptions

Refer to the block diagram (Figure 3), FD1252Kf/Sf is composed of the following building blocks:

• Regulator

The regulator provides a precise, low temperature coefficient bias reference for internal analog/digital blocks.

• Hall-Effect Sensor with Chopper Amplifier

To achieve a higher magnetic sensitivity the chopper amplifier structure is adopted in this design. Use of this structure dynamically removes both the offset and flicker noise at the same time.

• Digital Control Logic with Internal Clock

- > Timer part generates an interval of time when rotor locked event is occurred.
- Signal part generates the tachometer signal(FG) or alarm signal(RD) output.

• H-Bridge MOS Driver with Pre-Driver

The driver provides a BLDC motor / FAN coil driving capability.

• Variable Speed Control

PWM Pulse Mode

When the external PWM signal connects to the PWM pin of FD1252xf IC, it will pass the duty cycle ratio and frequency of the signal to the coil driver and make the BLDC motor speed changing as well.

Figure 4 is a reference example about the relationship between the PWM signal duty cycle and the BLDC motor's speed.

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Note:

- (1) The lower-limit for the PWM pulse frequency is 2KHz, and the recommended frequency range is in the range of 25~30kHz where the PWM input pulses will not generate acoustic noise.
- (2). The PWM pin contains an internal pull-up resistor, the BLDC motor becomes full speed operation when this pin is left un-connected(floating).
- (3). Under low duty(< 20%) the BLDC motor perhaps cannot rotate, but IC still has an on torque to re-start the BLDC motor until the duty cycle is 0%.



Order Information

Part Number	Operating Temperature	Package	Description		MOQ	MSL
FD1252KfR-G1		TOOOC		Tachometer O/P (FG)	3,500EA/	3
FD1252SfR-G1	-20 C to +85 C	15820	±45G (B)	Alarm O/P (RD)	REEL	

Absolute Maximum Ratings

Deremeter	Symphol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Operating Temperature	T _{OP.}	-	-20	-	85	°C	
Storage Temperature	T _{ST.}	-	-40	-	150	°C	
DC Supply Voltage	V _{DD(MAX.)}	-	-0.3	-	18.0	V	
DC Supply Current (No Loading)	I _{DD(AVG.)}	-	-	-	8.0	mA	
Maximum Output Current	I _{O(MAX.)}	-	-	-	400	mA	
Signal Sink Current (FG, RD)	ION(FG, RD)	-	-	-	20	mA	
Signal Off Voltage (FG, RD)	V _{OFF(FG, RD)}	-	-0.3	-	18.0	V	
PWM Input Voltage	V _(PWM)	-	-0.3	-	V _{DD}	V	
Junction Temperature	Tj	-	-	-	170	°C	
Maximum Power Dissipation (%1)	P _{D(TS826)}	TS826	-	-	500	mW	
Thermal Resistance (%1)	θ _{jc}	TS826	-	40	-	°C/W	
Thermal Resistance (%1)	θ _{ja}	TS826	-	250	-	°C/W	
Magnetic Flux Density	В	-	-	-	Unlimited	Gauss	
IR-Reflow Lead Temperature	Τ _Ρ	10sec	-	-	260	°C	

Note1: device mounted with copper area of approximately $35mm^2$, 1oz, no air flow. (room temperature: 25 °C)

Power Derating Curve



Figure 5



Recommended Operating Conditions

Parameter	Symbol	Conditions	Values			Unit
i didilicitei	Cymbol	Conditions	Min.	Тур.	Max.	Onic
Supply Voltage	V _{DD}	-	4.0	-	15.0	V
Operating Temperature Range	T _{OP.}	-	-20	-	85	C

DC Electrical Characteristics V_{DD}=12.0V, T_{OP}=25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
	Cymbol	Conditions	Min.	Тур.	Max.	onit	
DC Supply Current	I _{DD(AVG.)}	No Loading	-	5.0	-	mA	
Signal Saturation Voltage (FG, RD)	V _{+S(ON)}	$I_{+S(ON)} = 5mA$	-	-	0.4	V	
Signal Off Leakage Current	I _{+S(OFF)}	$V_{+S(OFF)} = 5.0V$	-	-	10	μA	
On Registeres (NO, SO pin)	5	$V_{DD} = 5.0 V$	-	6.0	-	Ω	
On Resistance (NO, SO pin)	RDS(ON)	$V_{DD} = 12.0V$	-	5.0	-		
DW/M Throshold Voltage	V _{PWMTH(L)}	-	-	0.8	-	V	
PVVIVI Threshold Voltage	V _{PWMTH(H)}	-	-	2.2	-		
Internal PWM Frequency	f _{PWM}	-	-	40	-	KHz	
Thermal Shutdown Threshold	T _(SHDN.)	T _j Temperature	150	-	-	°C	
Locked Rotor On Period	t _{ON}	-	-	0.4	-	S	
Locked Rotor Off Period	t _{OFF}	-	-	4.0	-	s	
Locked Rotor Off/On Ratio	t _{OFF} /t _{ON}	-	-	10	-	-	

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Magnetic Characteristics

Parameter	Symbol	Conditions	Values			Unit
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Operate Point	B _{OP}		5	30	45	G
Release Point	B _{RP}	Т _{ор.} = 25°С	-45	-30	-5	G
Hysteresis	B _{HYS}		10	-	90	G

Driver Output vs. Magnetic Pole

Parameter	Test Conditions	NO	so
North Pole	B > B _{OP}	Low	High
South Pole	B < B _{RP}	High	Low

Note: The magnetic pole is applied facing the branded side of the package

Hysteresis Characteristics



Figure 6





Performance Graphs



Figure 7

















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Application Circuits Reference



Figure 13

FD1252Kf/Sf 3-Wire DC Bias Variable Speed BLDC Motor Application Circuits

NOTE:

- 1. D1 is a low cut-in Schottky barrier diode for start-up and reverse protection operation.
- 2. R1 and C1 are for power supply filtering function, and must be placed as close to IC1 as possible.

The IC laying aside mode declaration is as follows





FD1252Kf Output Waveforms Description(Operation without PWM Mode)



Figure 15

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FD1252Sf Output Waveforms Description(Operation without PWM Mode)



Figure 16

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FD1252K/Sf Output Waveforms Measurement



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FD1252K/Sf Output Waveforms Measurement (Cont'd)



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Package Outline

TS826 (Halogen Free)



Unit: mm

	Dimension In Millimeters					
Symbols	Min.	Nom.	Max.			
A2	0.700	0.750	0.775			
b	0.350	-	0.500			
с	0.100	-	0.200			
D	2.800	2.900	3.100			
E	3.600	3.800	4.000			
E1	1.500	1.600	1.700			
е	0.950 BSC					
e1	1.900 BSC					
L1	1.100 REF.					
Θ1	4° 10° 12°					

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