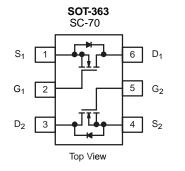
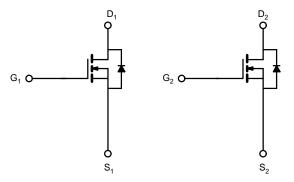


Dual N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω) I _D (mA)			
60	2.5 at V _{GS} = 10 V	300		





FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- •Low On-Resistance:2.5 Ω
- Low Threshold: 2 V (typ.)
- Low Input Capacitance: 25 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- · Easily Driven Without Buffer
- High-Speed Circuits
- Low Error Voltage

APPLICATIONS

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays

ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	60	V		
Gate-Source Voltage	V _{GS}	± 20	v			
Continuous Daria Current (T. 150 °C)b	T _A = 25 °C	- I _D	300			
Continuous Drain Current (T _J = 150 °C) ^b	T _A = 100 °C		190	mA		
Pulsed Drain Current ^a	I _{DM}	800				
Denor D'action that	T _A = 25 °C	D_	0.35	W		
Power Dissipation ^b	T _A = 100 °C	- P _D	0.14	VV		
Maximum Junction-to-Ambient ^b	·	R _{thJA}	350	°C/W		
Operating Junction and Storage Temperature Range		T _{J,} T _{stg}	- 55 to 150	°C		

Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface Mounted on FR4 board.

* Pb containing terminations are not RoHS compliant, exemptions may apply.



9	B	^R VBs	emi
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			Limits				
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static			•		•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 10 \mu A$	60			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1		2.5		
		$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 10		
		$V_{DS} = 0 V, V_{GS} = \pm 15 V$			1	μA	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 10 V$			± 150	nA	
		$V_{DS} = 0 V, V_{GS} = \pm 10 V, T_{J} = 85 °C$			± 1000		
		$V_{DS} = 0 V, V_{GS} = \pm 5 V$			± 100		
Zana Casta Maltana Duain Commant	1	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$			500		
On-State Drain Current ^a	I _{D(on)}	V_{GS} = 10 V, V_{DS} = 7.5 V	800				
		V_{GS} = 4.5 V, V_{DS} = 10 V	500			mA	
	R _{DS(on)}	V _{GS} = 10 V, I _D = 500 mA		2.5		Ω	
Drain-Source On-Resistance ^a		V_{GS} = 4.5 V, I _D = 200 mA		3.2			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 200 mA	100			mS	
Diode Forward Voltage	V _{SD}	I _S = 200 mA, V _{GS} = 0 V			1.3	V	
Dynamic ^a							
Total Gate Charge	Qg	V_{DS} = 10 V, V_{GS} = 4.5 V $I_D \cong 250 \text{ mA}$		0.4	0.6	nC	
Input Capacitance	C _{iss}			30		pF	
Output Capacitance	C _{oss}	$V_{DS} = 25 V, V_{GS} = 0 V$		6			
Reverse Transfer Capacitance	C _{rss}	f = 1 MHz		2.5		1	
Switching ^{a, b, c}	•		•		<u> </u>		
Turn-On Time	t _{d(on)}	V_{DD} = 30 V, R _L = 150 Ω			25		
Turn-Off Time	t _{d(off)}	$I_D \cong 200 \text{ mA}, V_{GEN} = 10 \text{ V}, \text{ R}_G = 10 \Omega$		1	35	ns	

Notes:

a. For DESIGN AID ONLY, not subject to production testing.

b. Pulse test: PW $\leq 300~\mu s$ duty cycle $\leq 2~\%.$

c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



25 °C

5

6

125 °C

T_J = - 55 °C

2

Coss

10

25

50

75

100

125

Capacitance

V_{GS} = 10 V at 500 mA

15

20

-V_{GS} = 4.5 ∖ at 200 mA

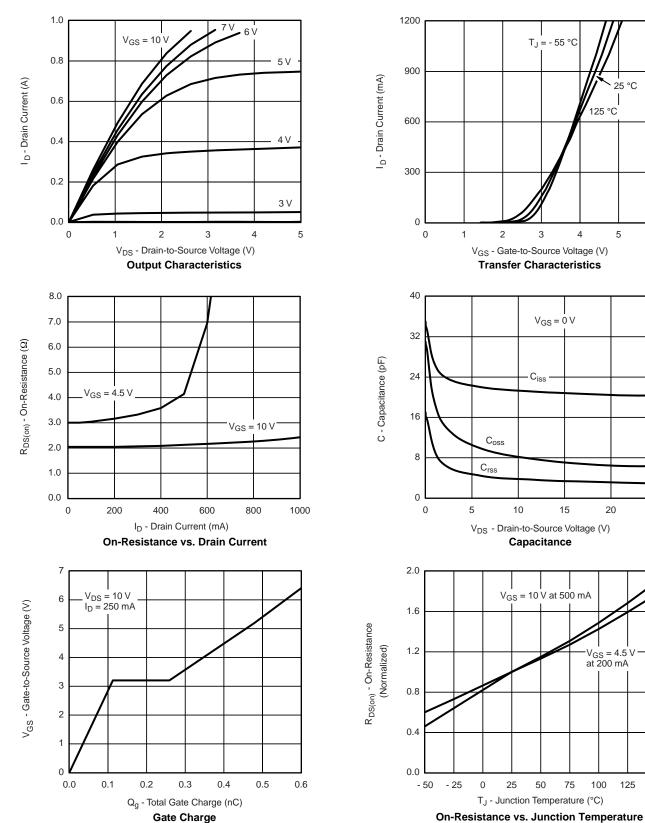
25

3

 $V_{GS} = 0 V$

Ciss

4

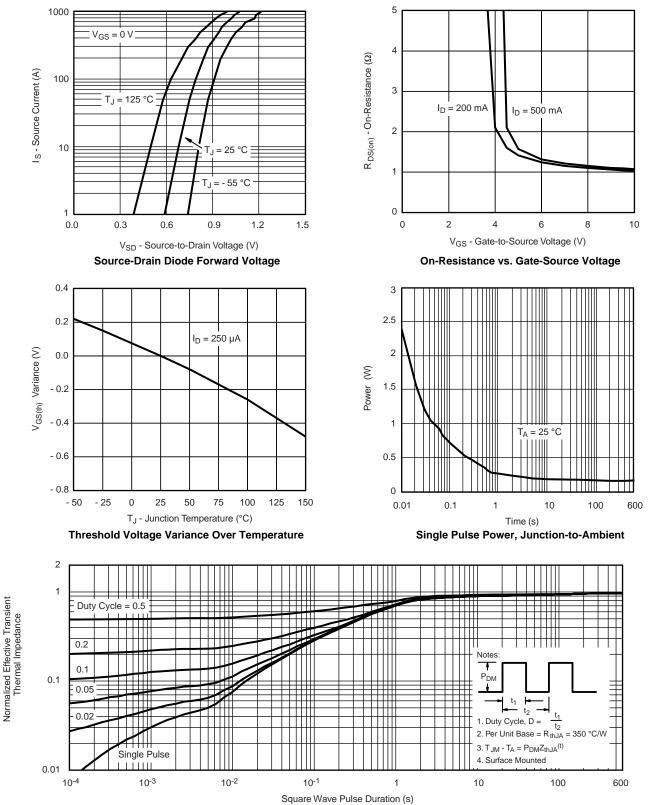


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

150

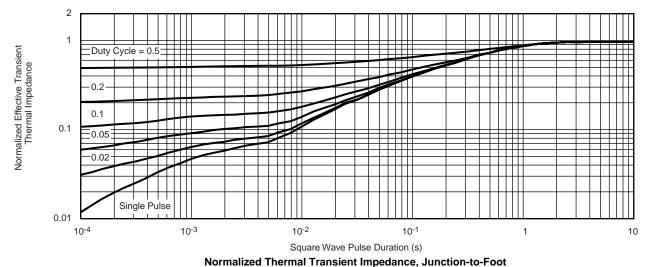


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted









THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

Note

• The characteristics shown in the two graphs

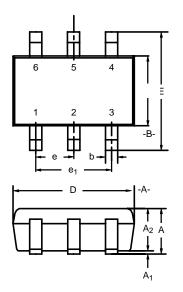
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

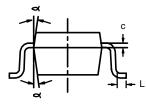
- Normalized Transient Thermal Impedance Junction-to-Foot (25 C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



SC-70: 6-LEADS





	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	-	0.043
A 1	-	-	0.10	-	-	0.004
A ₂	0.80	-	1.00	0.031	-	0.039
b	0.15	-	0.30	0.006	-	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Е	1.80	2.10	2.40	0.071	0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC			0.026BSC		
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
م	7°Nom 7°Nom					



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