



General Description

The TPS7B69 is a high voltage, low quiescent current, low dropout regulator with 200mA output driving capacity. The TPS7B69 , which operates over an input range up to 40V, is stable with any capacitors, whose capacitance is larger than 1 μ F, and suitable for

powering battery-management ICs because of the virtue of its low quiescent current consumption and low dropout voltage.

The TPS7B69 is available in SOT23-5

Features

- Up to 40V input voltage range
- 200mA output current driving capacity
- Ultra low quiescent current (typical 1.5 μ A)
- 400mV typical dropout at $I_{OUT} = 100$ mA
- Thermal shutdown protection
- Short circuit protection
- Stable with 1 μ F output capacitor
- Pin to pin TPS7B6950DBVR and TPS7B6933DBVR

Applications

- E-meters, Water Meters and Gas Meters
- Appliances and White Goods

Ordering Information

TPS7B6950DBVR-TP

TP: TECH PUBLIC

DBVR:SOT23-5

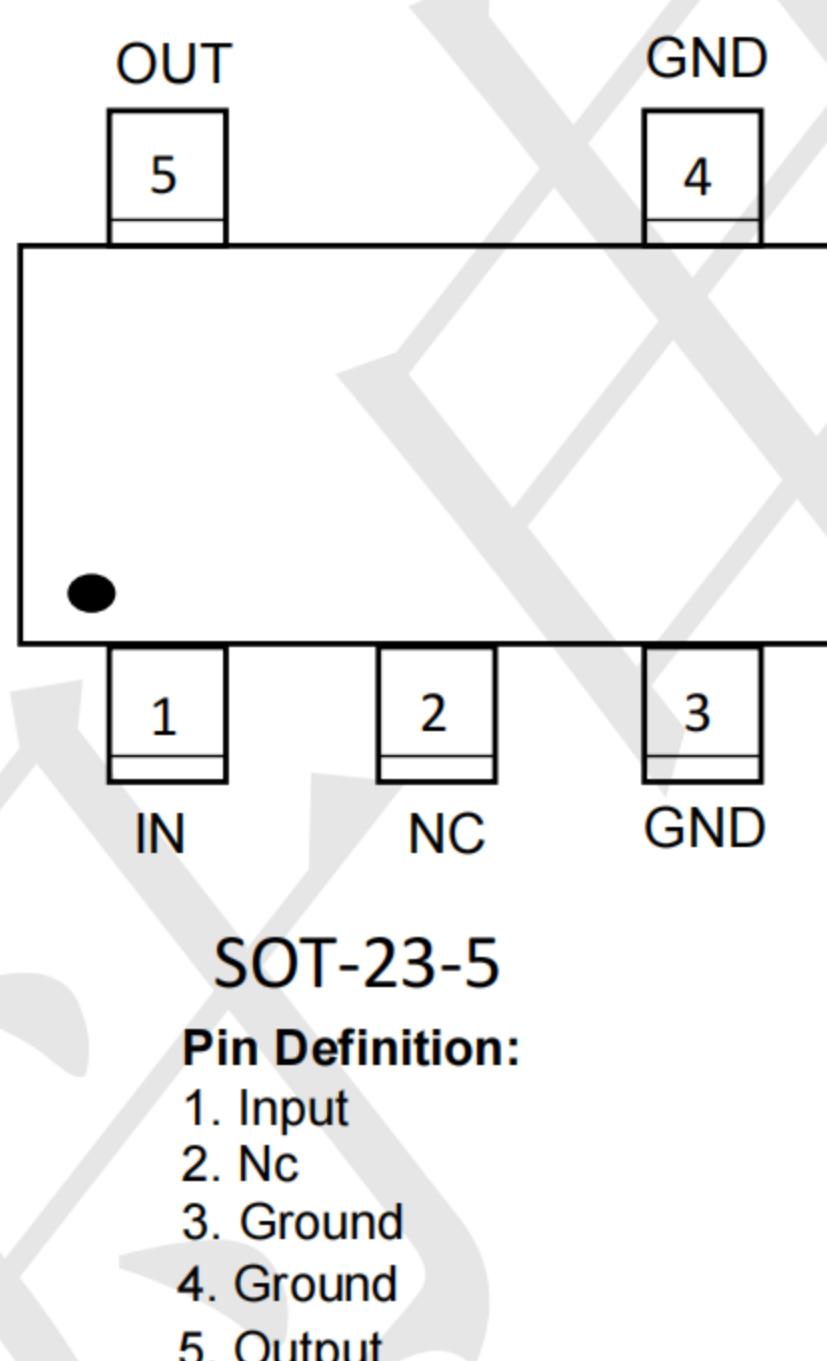
Output voltage: 3.3=3.3V
5.0=5.0V



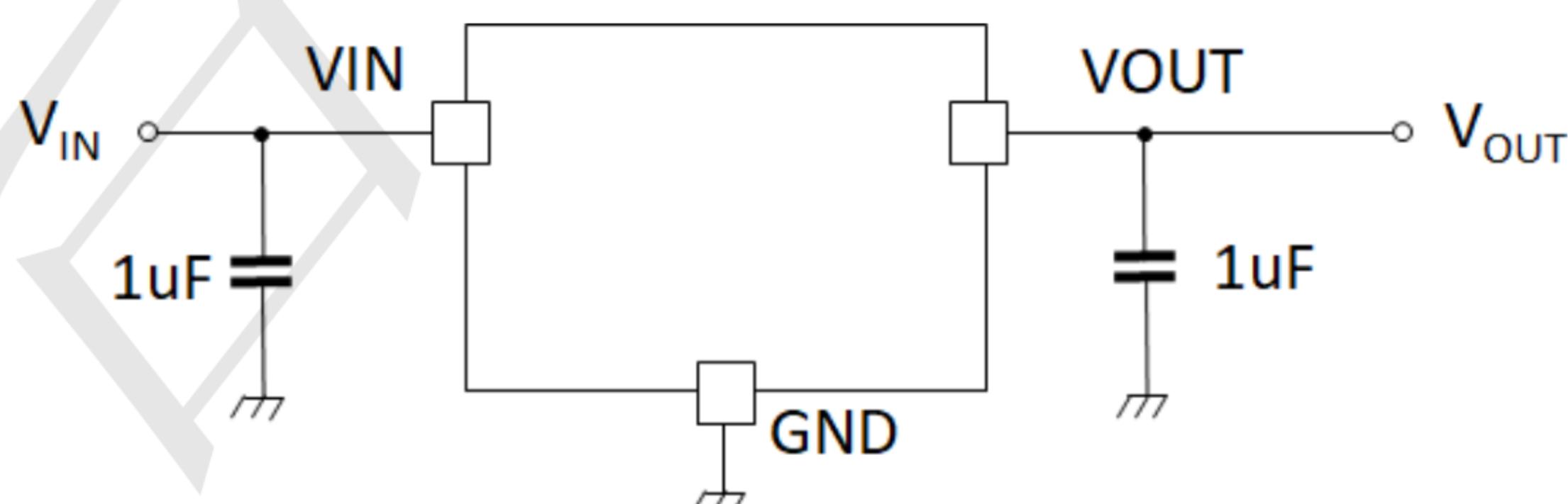
Packaging Information

Product ID.	Package	Vout	Marking	Packing
TPS7B6933DBVR-TP	SOT23-5	3.3V	ZBFY	Tape & Reel
TPS7B6950DBVR-TP	SOT23-5	5.0V	ZAZT	3Kpcs

Pin Configuration



Typical Application circuit





Absolute Maximum Ratings

V _{IN} , Range	-0.3V to 45V	Junction Temperature (T _J)	125°C
Storage Temperature Range	-65°C to 150°C	Lead Temperature(Soldering, 10 sec.)	260°C
		ESD Rating	
		Human Body Model	2KV

Recommended Operating Conditions (Note 1, 2)

Supply Voltage	2.7V to 40V	Operating Temperature Range	-40°C to 85°C
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Electrical Characteristics

V_{IN}=12V, I_{OUT}=1mA, C_{IN}=C_{OUT}=1μF, T_a = 25°C, unless otherwise specified

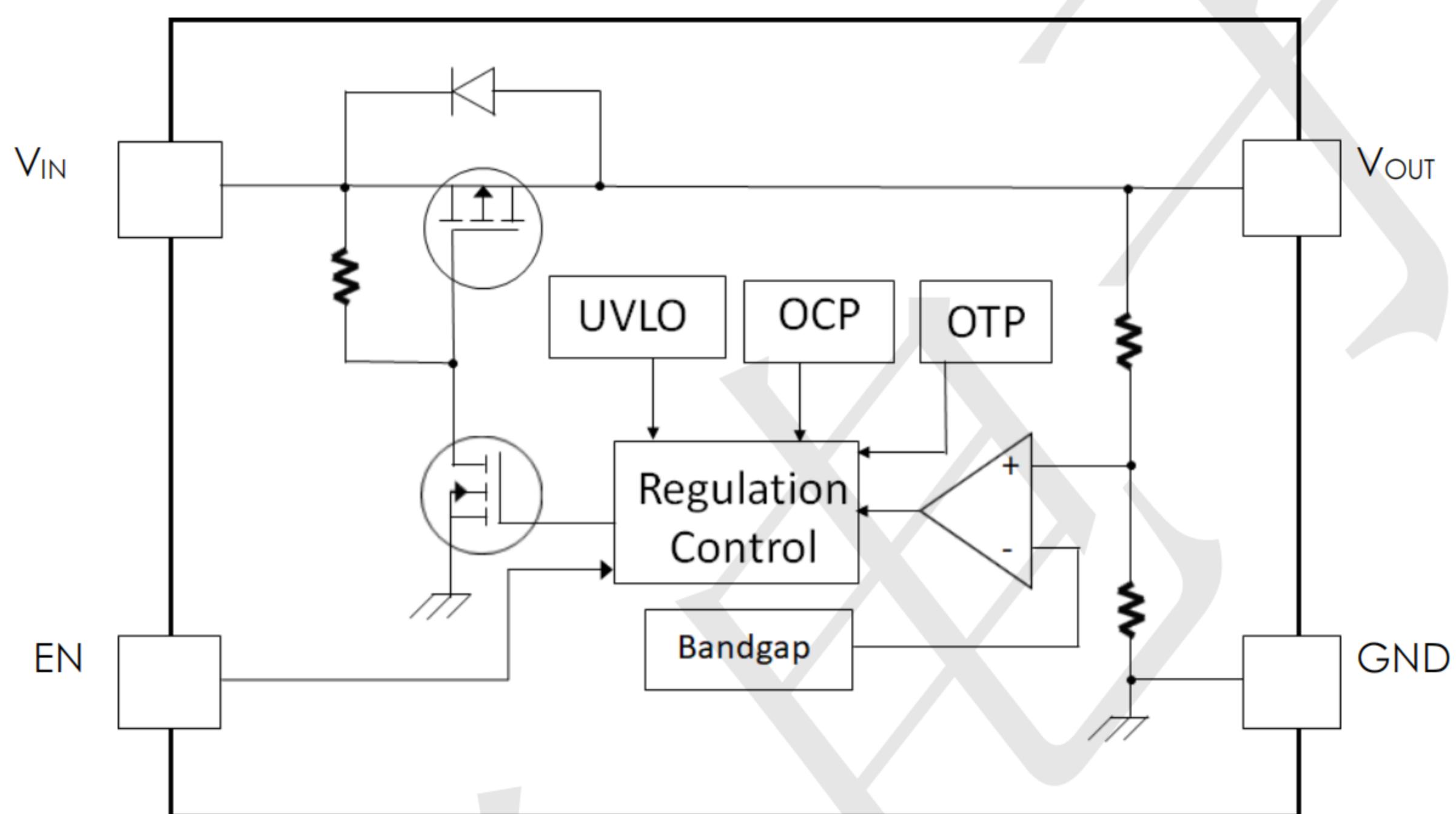
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Input Voltage	V _{IN}		2.7		40	V
Output Voltage	V _{OUT}		-2%		2%	V
Line Regulation	△V _{LINE}	V _{IN} =V _{OUT} + 1V to 40V,		0.1		%
Load Regulation	△V _{LOAD}	I _{OUT} = 1mA to 100mA		0.5		%
Dropout Voltage	V _{DROP}	I _{OUT} =50mA		220	380	mV
		I _{OUT} =100mA		400		mV
		I _{OUT} =250mA		1200		mV
Quiescent Current	I _Q	T _a = 25°C		1.5	4.0	μA
Current Limit	I _{CL}		200	300		mA
Thermal Shutdown	T _{SD}			140		°C
Thermal Shutdown Hysteresis	T _{HY}			20		°C
Power-supply rejection ratio	PSRR	f = 1kHz		80		dB
		f = 10kHz		60		dB

Note 1: Absolute Maximum ratings indicate limits beyond which damage may occur. Electrical specifications do not apply when operating the device outside of its rated operating conditions.

Note 2: All voltages are with respect to the potential at the ground pin.

Note 3: θ_{JA} is measured in the natural convection at T_J=25°C on a high effective thermal conductivity test board (2 layers, 2SOP).

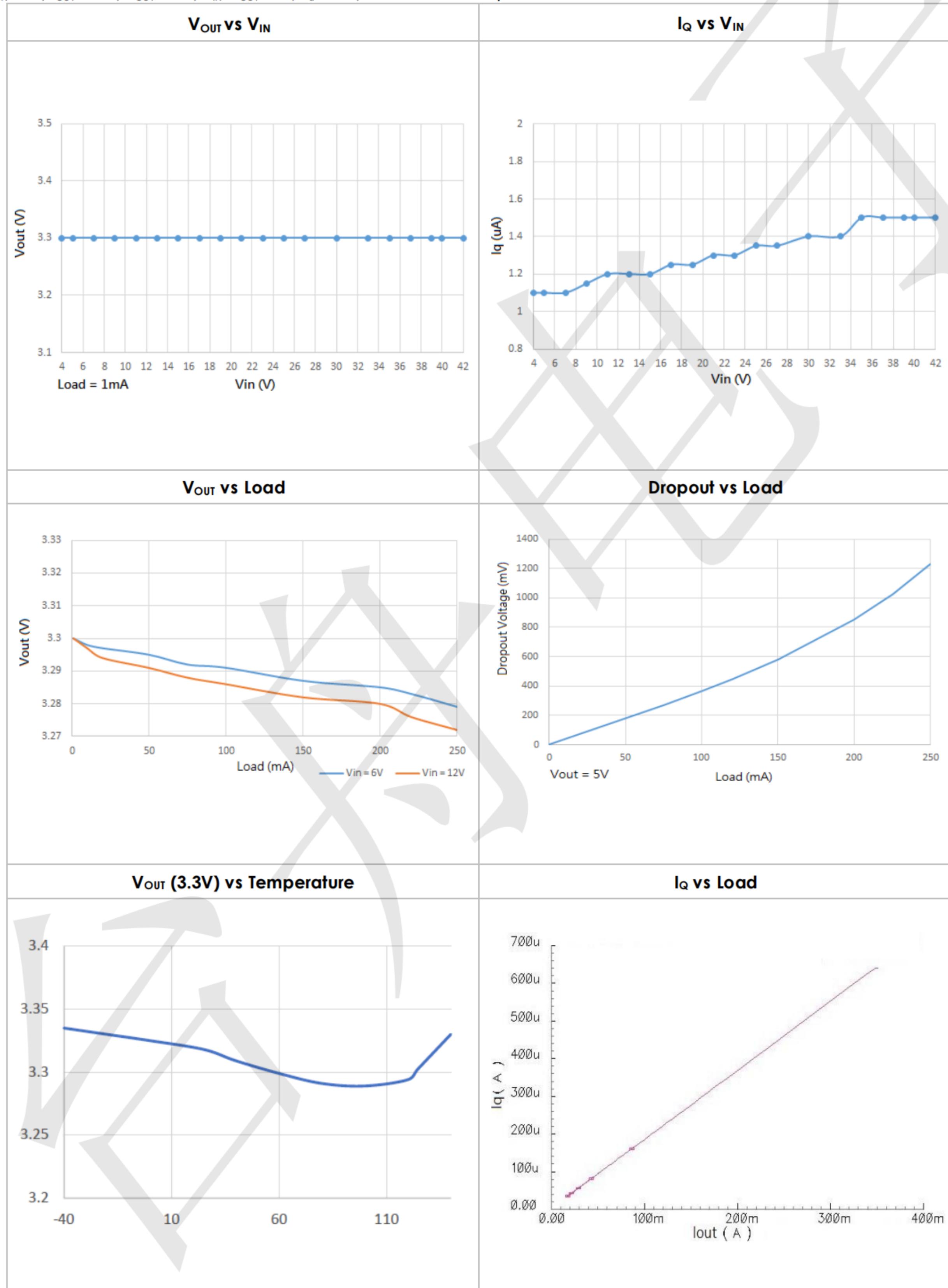
Note 4: θ_{JC} represents the resistance to the heat flows the chip to package top case.

Functional Block Diagram



Typical Performance Characteristics

$V_{IN}=12V$, $I_{OUT}=1mA$, $V_{OUT}=3.3V$, $C_{IN}=C_{OUT}=1\mu F$, $T_a=25^\circ C$, unless otherwise specified





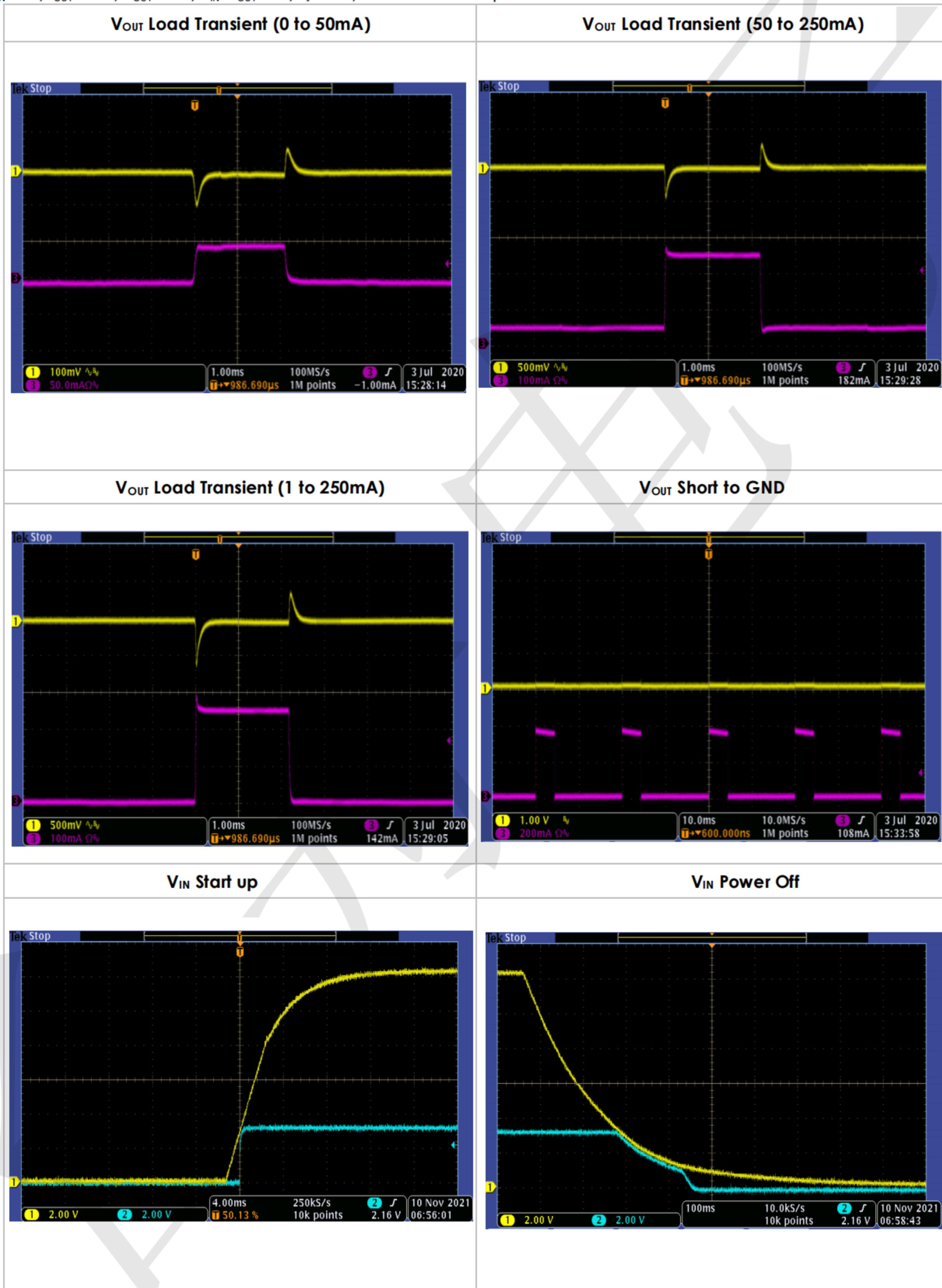
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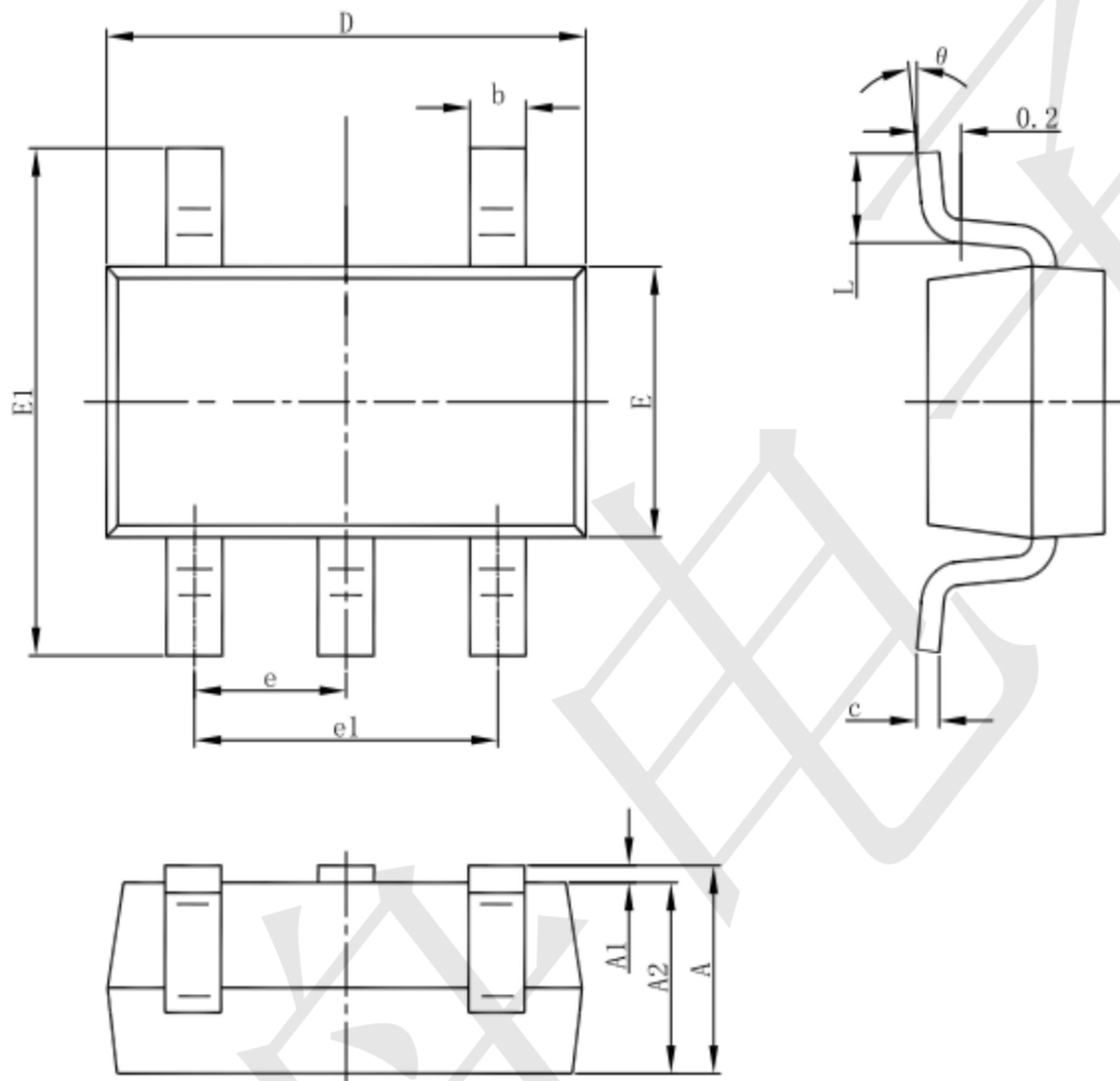
TPS7B69 Series
200mA,40V Ultra-low-Quiescent-Current LDO

www.sot23.com.tw

Typical Performance Characteristics(cont.)

$V_{IN}=12V$, $I_{OUT}=1mA$, $V_{OUT}=3.3V$, $C_{IN}=C_{OUT}=1\mu F$, $T_J=25^{\circ}C$, unless otherwise specified



Package information
SOT23-5


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°