

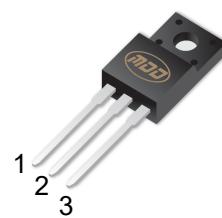


# MDD4N65F/MDD4N65P/MDD4N65D

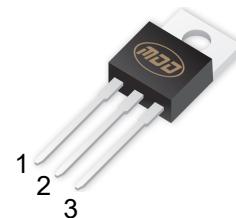
650V N-Channel Enhancement Mode MOSFET

<b>V<sub>DS</sub></b>	<b>650 V</b>
<b>I<sub>D(TC=25°C)</sub></b>	<b>4A</b>
<b>R<sub>DS(on),max</sub></b>	<b>2.8Ω@V<sub>GS</sub>=10V</b>
<b>Q<sub>g,typ</sub></b>	<b>12nC</b>

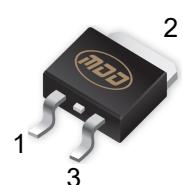
TO-220F-3L



TO-220-3L



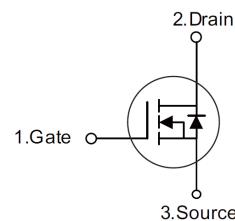
TO-252



## General Features

- Ultra low gate charge
- Low reverse transfer Capacitance
- Fast switching capability
- Avalanche energy tested
- Improved dv/dt capability, high ruggedness

## Equivalent Circuit



## Application

- High efficiency switch mode power supplies
- Electronic lamp ballasts based on half bridge
- LED power supplies

## Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	650	V
Gate-Source Voltage	V <sub>GS</sub>	±30	V
Continuous Drain Current	I <sub>D</sub>	4	A
Pulsed Drain Current(Note 1)	I <sub>DM</sub>	16	A
Avalanche Energy Single Pulsed (Note 2)	E <sub>AS</sub>	198	mJ
Continuous diode forward current	I <sub>S</sub>	4	A
Diode pulse current	I <sub>S,pulse</sub>	16	A
Peak Diode Recovery dv/dt (Note 3)	dv/dt	5	V/ns
Power Dissipation TO-220F	P <sub>D</sub>	32	W
Power Dissipation TO-220/TO-252		77	W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 ~ 150	°C

## Thermal Characteristics

Parameter	Symbol	Value		Unit
		TO-220F	TO-220/TO-252	
Thermal resistance, Junction-to-case	R <sub>θJC</sub>	3.8	1.62	°C/W
Thermal resistance, Junction-to-ambient	R <sub>θJA</sub>	62.5	110	°C/W

**Notes:** 1. Pulse width limited by maximum junction temperature.

2. L=10mH, IAS = 6.3A, Starting T<sub>j</sub>= 25°C.

3. ISD = 4A, di/dt≤100A/us, VDD≤BVDS, Starting T<sub>j</sub>= 25°C.



# MDD4N65F/MDD4N65P/MDD4N65D

650V N-Channel Enhancement Mode MOSFET

**T<sub>a</sub> = 25°C unless otherwise specified**

Symbol	Parameter		Condition	Min	Typ	Max	Unit
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage		V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	650	--	--	V
I <sub>GS</sub>	Gate-Source Leakage Current	Forward	V <sub>GS</sub> =30V, V <sub>DS</sub> =0V	--	--	100	nA
		Reverse	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V	--	--	-100	nA
I <sub>DSS</sub>	Drain-Source Leakage Current		V <sub>DS</sub> =650V, V <sub>GS</sub> =0V	--	--	1	uA
V <sub>GS(TH)</sub>	Gate Threshold Voltage		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	--	4.0	V
R <sub>DSON</sub>	Drain-Source On-State Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =2A	--	2.5	2.8	Ω

## Dynamic Electrical Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V V <sub>GS</sub> =0V f=1MHz	--	600		pF
C <sub>oss</sub>	Output Capacitance		--	55		pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	3.2		pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =520V, V <sub>GS</sub> =10V, I <sub>D</sub> =4A (Note1,2)	--	12	--	nC
Q <sub>gs</sub>	Gate Source Charge		--	3.2	--	nC
Q <sub>gd</sub>	Gate Drain Charge		--	5.1	--	nC

## Switching Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
t <sub>d(on)</sub>	Turn on Delay Time	V <sub>DS</sub> =325V, I <sub>D</sub> =4A, R <sub>G</sub> =10Ω (Note1,2)	--	--	12	ns
t <sub>r</sub>	Turn on Rise Time		--	--	31	ns
t <sub>d(off)</sub>	Turn Off Delay Time		--	--	42	ns
t <sub>f</sub>	Turn Off Fall Time		--	--	15	ns

## Source Drain Diode Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
I <sub>SD</sub>	Source drain current(Body Diode)		--	--	4	A
I <sub>SM</sub>	Pulsed Current		--	--	16	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	I <sub>S</sub> =4A, V <sub>GS</sub> =0V	--	0.8	1.5	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	V <sub>R</sub> =400	--	282	--	ns
		I <sub>F</sub> =4A, -dI/dt=100A/μs	--	1.4	--	uC

### Notes:

1.Pulse test ; Pulse width≤300us, duty cycle≤2%.

2.Essentially independent of operating temperature.

### Electrical Characteristics Diagrams

Figure 1. Typical Output Characteristics

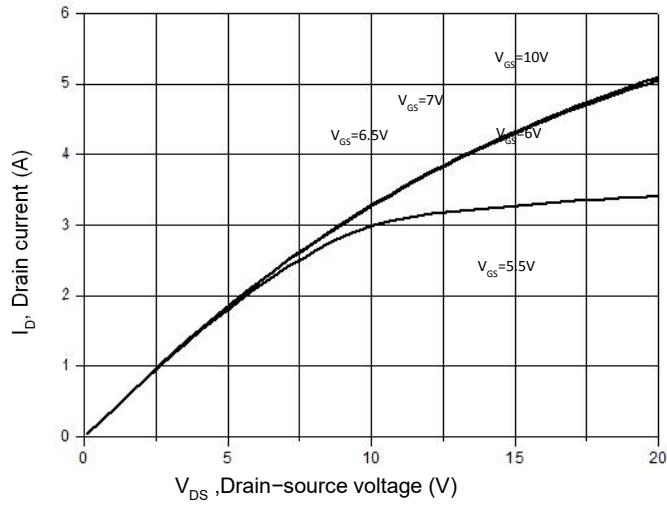


Figure 2. Transfer Characteristics

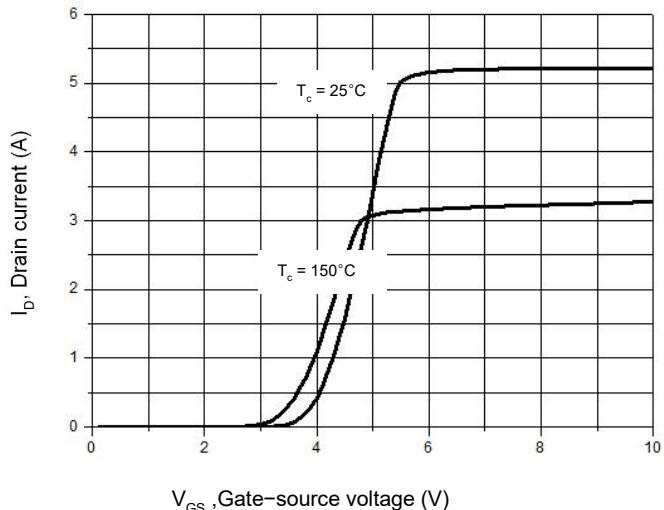


Figure 3. On-Resistance Variation vs. Drain Current

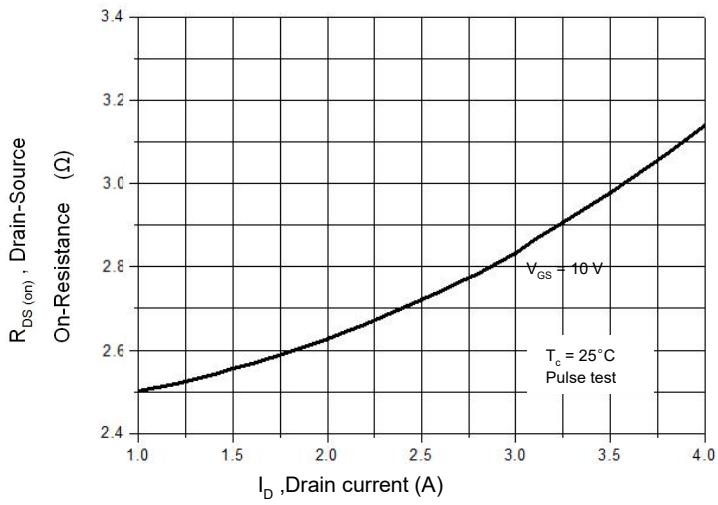


Figure 4. Threshold Voltage vs. Temperature

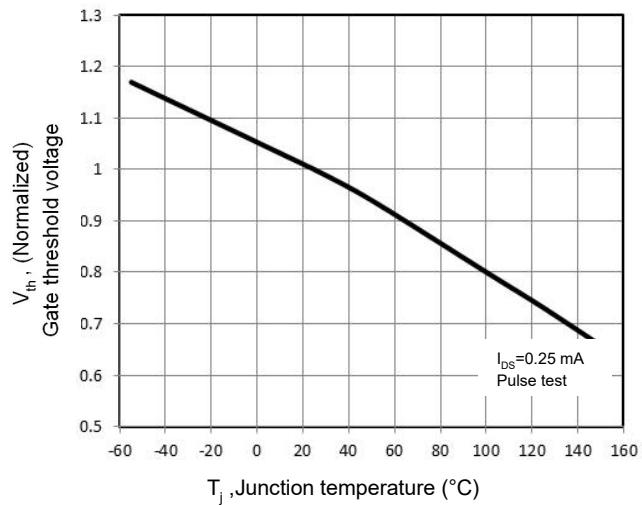


Figure 5. Breakdown Voltage vs. Temperature

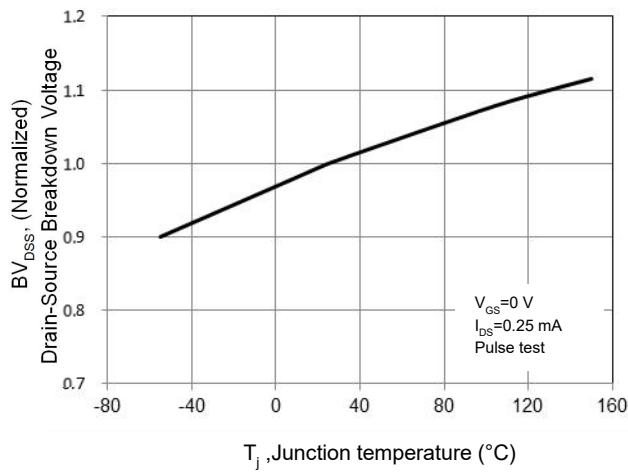


Figure 6. On-Resistance vs. Temperature

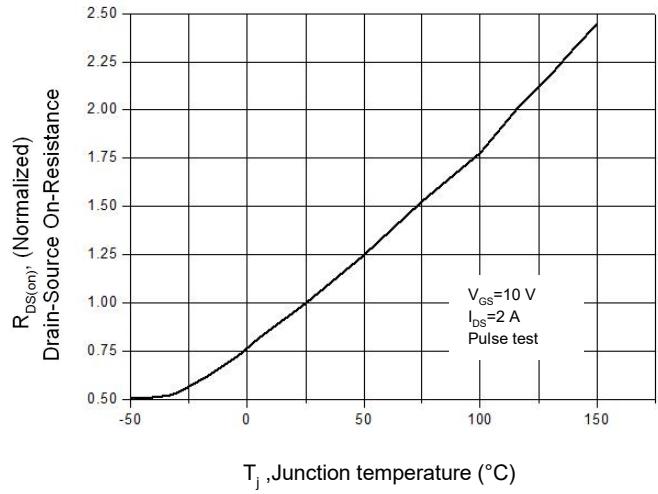


Figure 7. Capacitance Characteristics

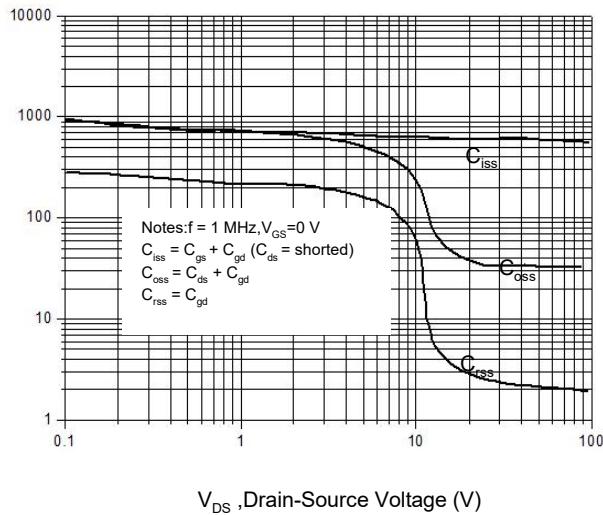


Figure 9. Maximum Safe Operating Area

TO-220F

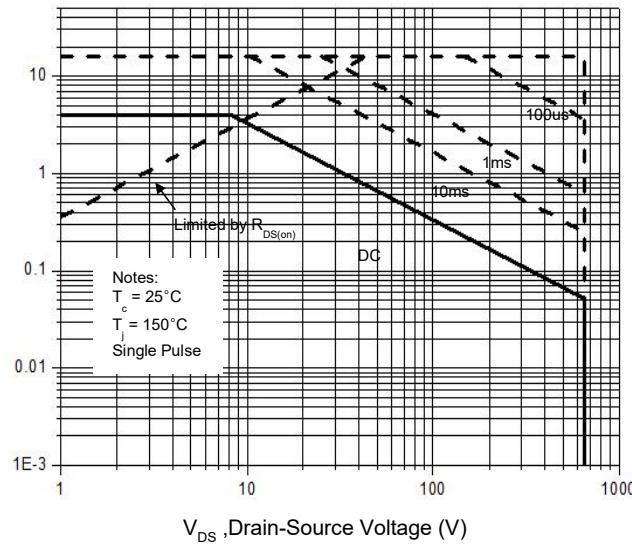


Figure 11. Power Dissipation vs. Temperature

TO-220F

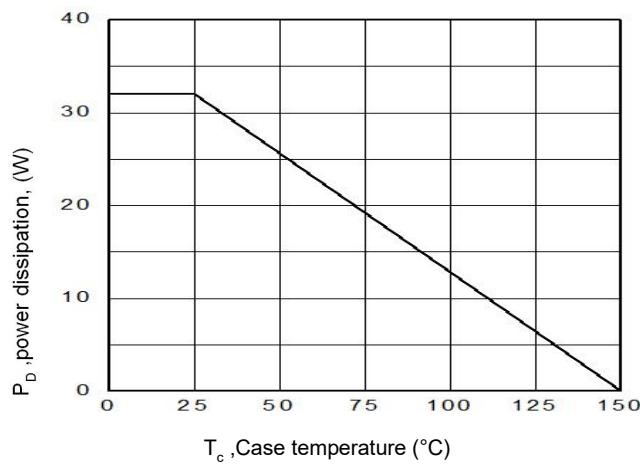


Figure 8. Gate Charge Characterist

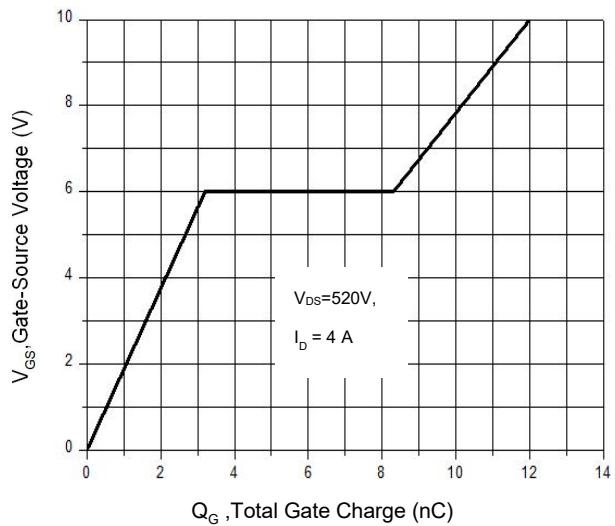


Figure 10. Maximum Safe Operating

Area TO-220/ TO-252

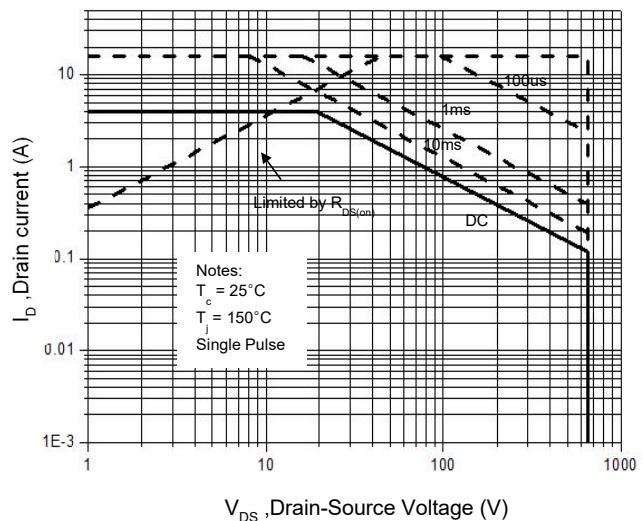


Figure 12. Power Dissipation vs. Temperature

TO-220/ TO-252

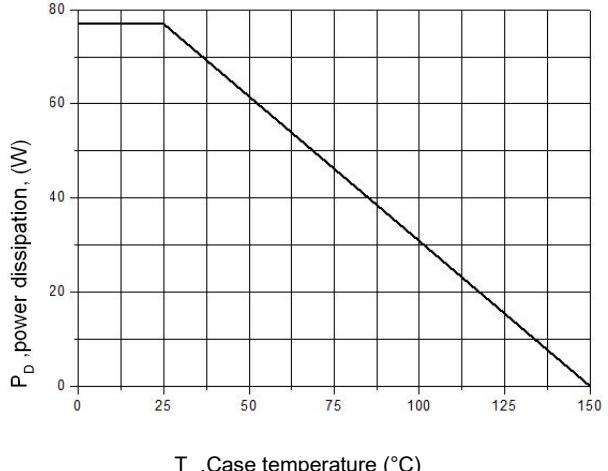


Figure 13. Continuous Drain Current vs. Temperature

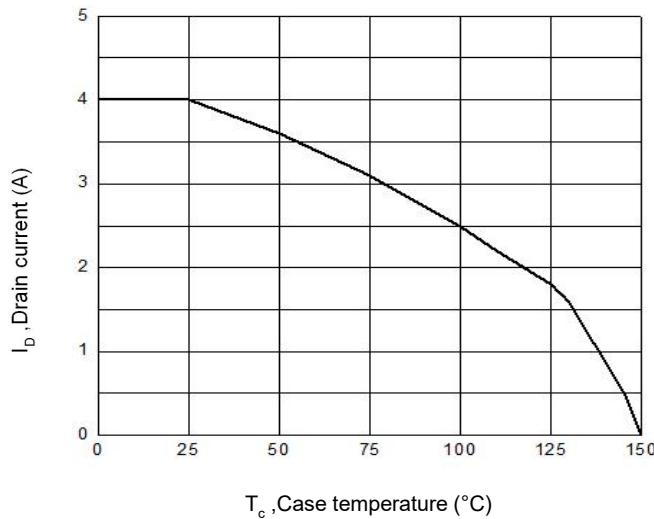
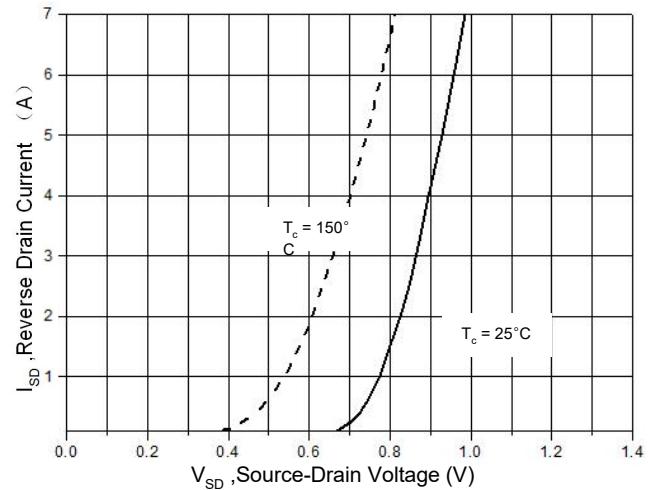


Figure 14. Body Diode Transfer Characteristics



T<sub>c</sub>, Case temperature (°C)

Figure 15 Transient Thermal Impedance, Junction to Case, TO-220F

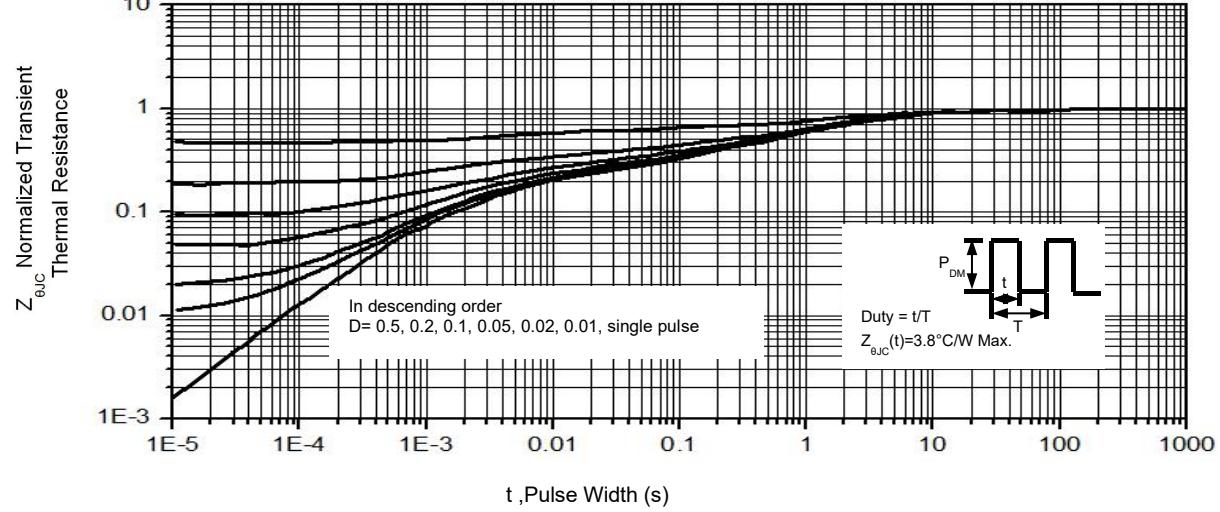
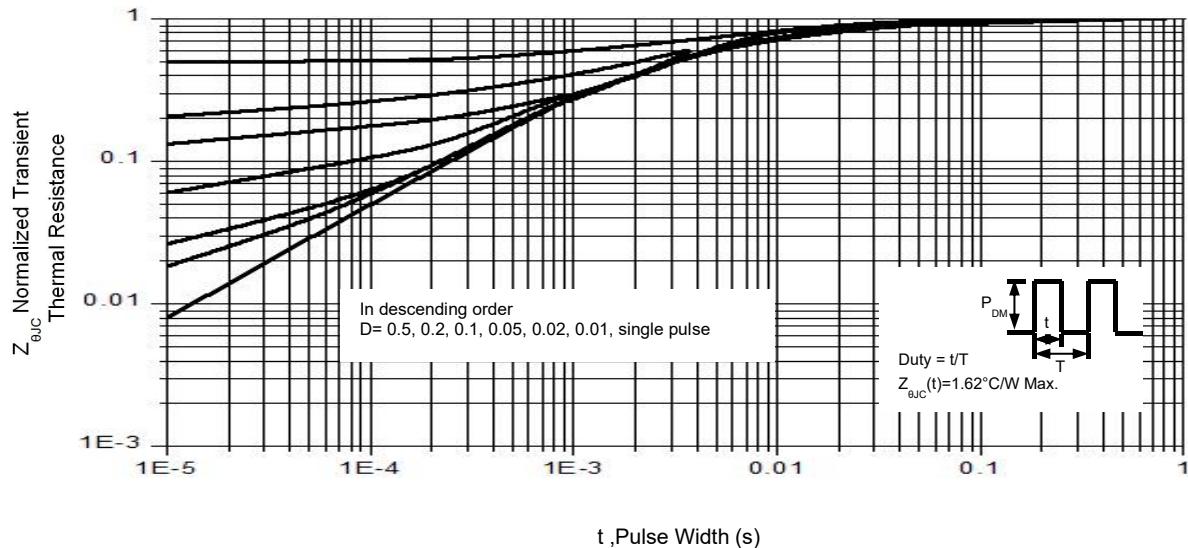
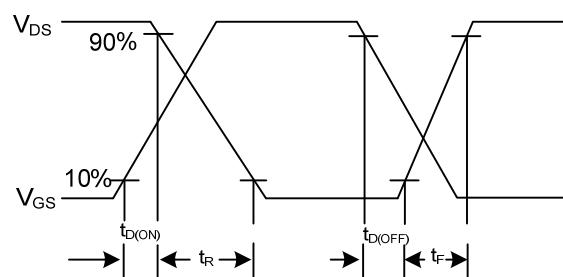
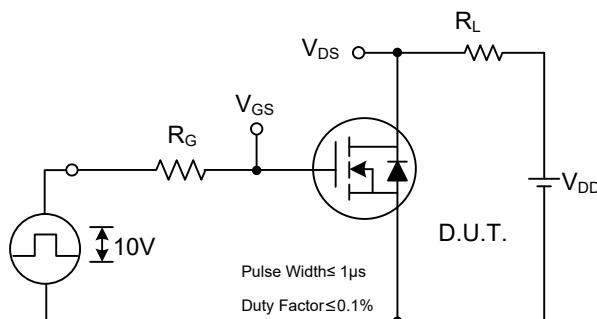


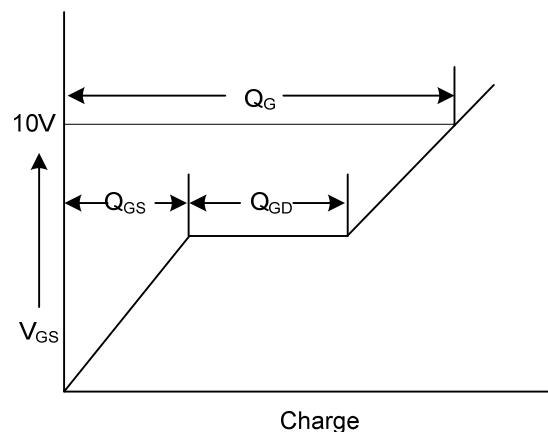
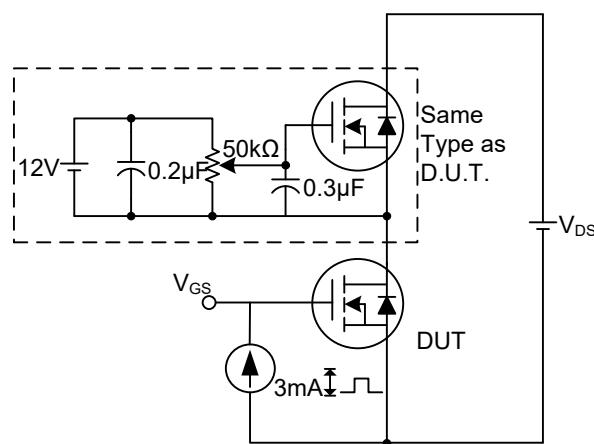
Figure 16. Transient Thermal Impedance, Junction to Case, TO-220/ TO-252





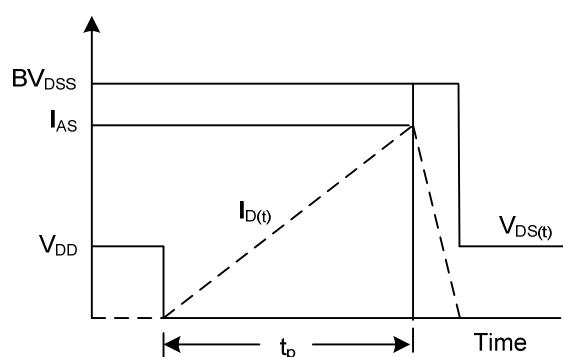
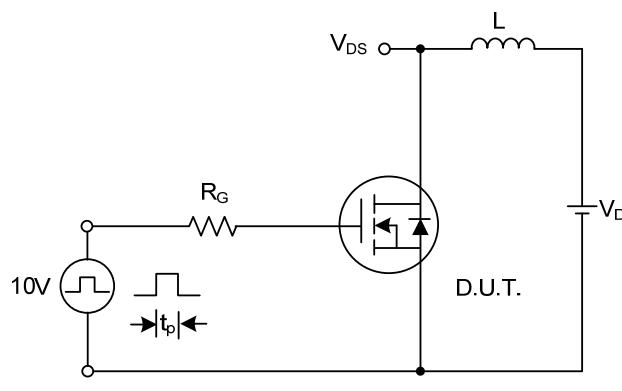
**Switching Test Circuit**

**Switching Waveforms**



**Gate Charge Test Circuit**

**Gate Charge Waveform**



**Unclamped Inductive Switching Test Circuit**

**Unclamped Inductive Switching Waveforms**

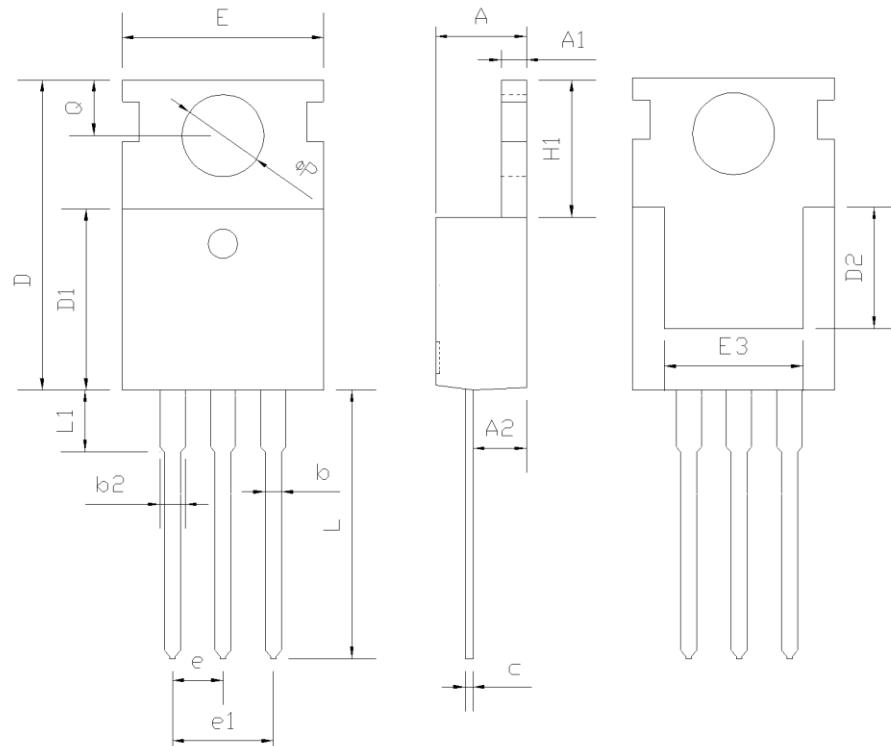
The curve above is for reference only.



# MDD4N65F/MDD4N65P/MDD4N65D

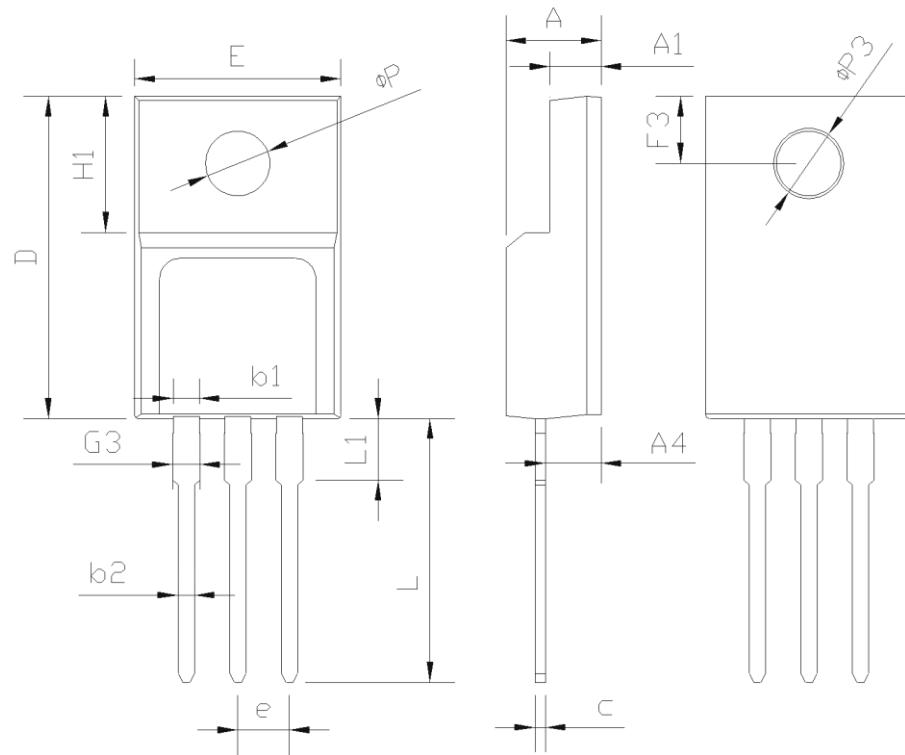
650V N-Channel Enhancement Mode MOSFET

## Mechanical Dimensions for TO-220



SYMBOL	mm		
	MIN	NOM	MAX
A	4.37	4.57	4.70
A1	1.25	1.30	1.40
A2	2.20	2.40	2.60
b	0.70	0.80	0.95
b2	1.17	1.27	1.47
c	0.45	0.50	0.60
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.50	-	-
E	9.70	10.00	10.30
E3	7.00	-	-
e	2.54 BSC		
e1	5.08 BSC		
H1	6.25	6.50	6.85
L	12.75	13.50	13.80
L1	-	3.10	3.40
ΦP	3.40	3.60	3.80
Q	2.60	2.80	3.00

### Mechanical Dimensions for TO-220F



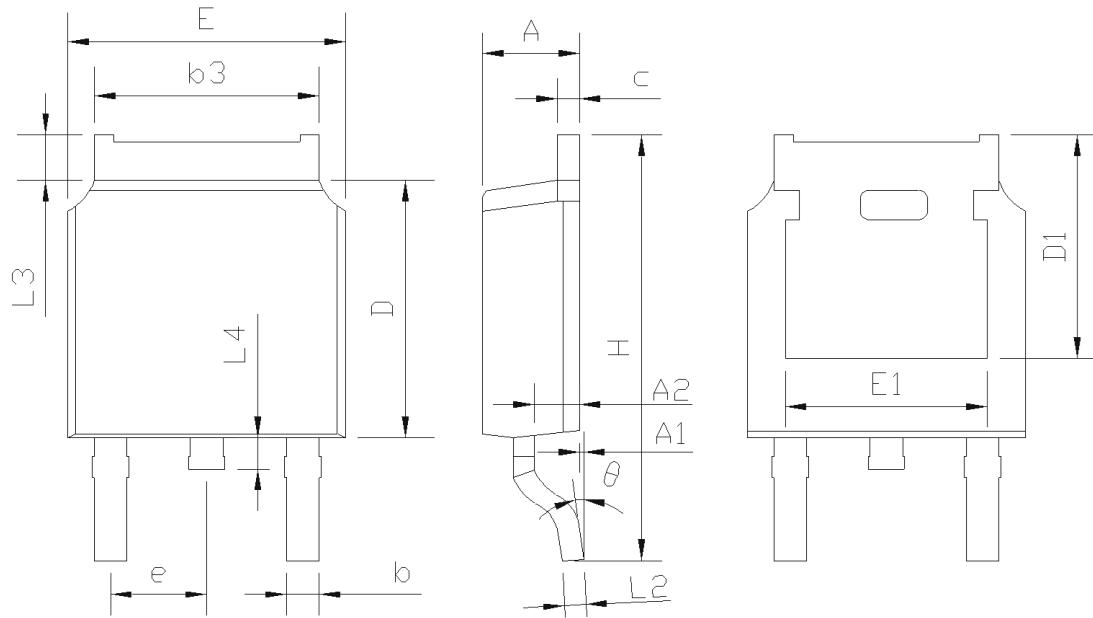
SYMBOL	mm		
	MIN	NOM	MAX
E	9.96	10.16	10.36
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.56	2.76	2.96
c	0.40	0.50	0.65
D	15.57	15.87	16.17
H1		6.70REF	
e		2.54BSC	
L	12.68	12.98	13.28
L1	2.88	3.03	3.18
$\Phi P$	3.03	3.18	3.38
$\Phi P_3$	3.15	3.45	3.65
$F_3$	3.15	3.30	3.45
G3	1.25	1.35	1.55
b1	1.18	1.28	1.43
b2	0.70	0.80	0.95



# MDD4N65F/MDD4N65P/MDD4N65D

650V N-Channel Enhancement Mode MOSFET

## Mechanical Dimensions for TO-252



SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286BSC		
H	9.40	10.10	10.50
L2	0.51BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
θ	0°	-	8°

## Package Marking and Ordering Information

Part Number	Marking	Package	Units/Tube	Units/Reel
MDD4N65F	4N65F	TO-220F	50	
MDD4N65P	4N65P	TO-220-3L	50	
MDD4N65D	4N65D	TO-252		2500