

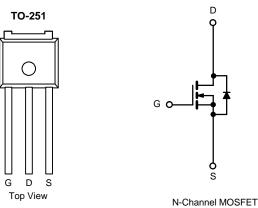
N-Channel 30-V (D-S) MOSFET

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PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ)	
30	0.0035 at V _{GS} = 10 V	100	95nC	
	0.0045 at V _{GS} = 4.5 V	97	30110	



FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
 Compliant to RoHS Directive 2011/65/EU



- OR-ing
- Server
- DC/DC

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		100 ^{a, e}	A	
Continuous Drain Current (T ₁ = 175 °C)	T _C = 70 °C		95 ^e		
	T _A = 25 °C	I _D	35 ^{b, c}		
	T _A = 70 °C		26 ^{b, c}		
Pulsed Drain Current		I _{DM}	197		
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	39		
Single Pulse Avalanche Energy	L = 0.1 IIIH	E _{AS}	94.8	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C	l.	90 ^{a, e}		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	3.13 ^{b, c}	— A	
	T _C = 25 °C		250 ^a	w	
Mariana Dissisting	T _C = 70 °C	P	175		
Maximum Power Dissipation	T _A = 25 °C	P _D	3.75 ^{b, c}		
	T _A = 70 °C		2.63 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 \text{ sec}$	R _{thJA}	32	40	°C/W
Maximum Junction-to-Case	Steady State	R _{thJC}	0.5	0.6	°C/W

Notes:

a. Based on $T_C = 25 \text{ °C}$. b. Surface mounted on 1" x 1" FR4 board.

a. t = 10 sec.
d. Maximum under steady state conditions is 90 °C/W.
e. Calculated based on maximum junction temperature. Package limitation current is 90 A.

COMPLIANT

STD95NH02L

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Peromotor	Cymra b a l	Test Conditions	Mim	Tree	Mex	ا ا ما ا
Parameter Static	Symbol	lest Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 µA	30		[V
V _{DS} Temperature Coefficient	V _{DS} ΔV _{DS} /T _J	VGS = 0 V, 10 = 200 µA	30	35		v
V _{GS(th)} Temperature Coefficient		I _D = 250 μA				mV/°C
(<i>)</i>	$\Delta V_{GS(th)}/T_J$	V _{DS} = V _{GS} , I _D = 250 μA	4.0	- 7.5	0.5	N
Gate-Source Threshold Voltage	V _{GS(th)}		1.0		2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			μ <i>μ</i>	
		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 V$, $V_{GS} = 10 V$	90			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 38.8 A		0.0035		Ω
	B0(01)	V _{GS} = 4.5 V, I _D = 37 A		0.0045		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 38.8 \text{ A}$		160		S
Dynamic ^b						
Input Capacitance	C _{iss}			3000		
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		710		pF
Reverse Transfer Capacitance	C _{rss}			170		
Total Gate Charge	Qg	V_{DS} = 15 V, V_{GS} = 10 V, I_{D} = 38.8 A		171	257	1
Total Gate Charge	_			81.5	123	nC
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_{D} = 28.8 A		34		ne
Gate-Drain Charge	Q _{gd}			29		
Gate Resistance	Rg	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t _{d(on)}			18	27	
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.625 Ω		11	17	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}{\cong}24$ A, V_GEN = 10 V, R_g = 1 Ω		70	105	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			55	83	ns
Rise Time	t _r	V_{DD} = 15 V, R_{L} = 0.67 Ω		180	270	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 22.5 \text{ A}, \text{ V}_{\text{GEN}} = 4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		55	83	
Fall Time	t _f	Ŭ		12	18	
Drain-Source Body Diode Characteristic					I	L
Continuous Source-Drain Diode Current	ا _S	T _C = 25 °C			120	
Pulse Diode Forward Current ^a	I _{SM}				120	A
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	-		52	78	ns
Body Diode Reverse Recovery Charge	Q _{rr}			70.2	105	nC
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 \text{ °C}$		27		
Reverse Recovery Rise Time	t _a			25		ns

Notes:

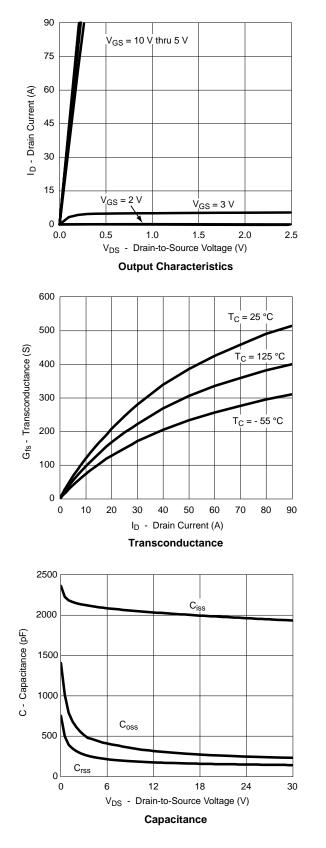
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle ≤ 2 %.

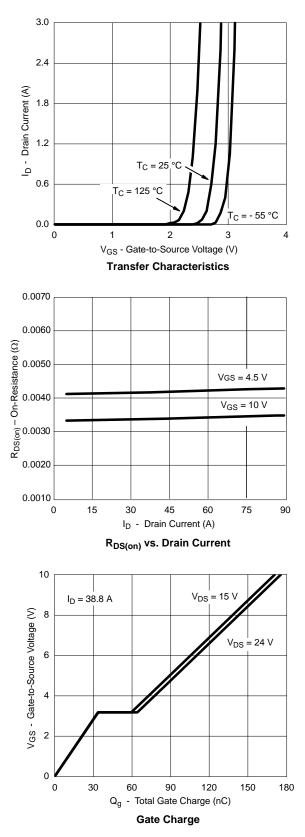
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



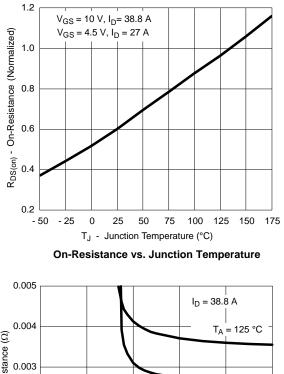
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

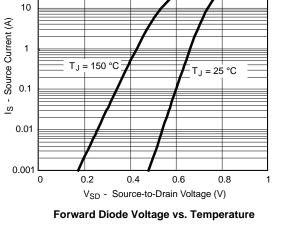




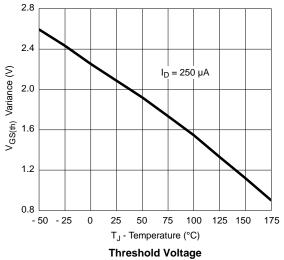


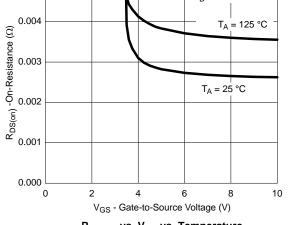
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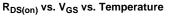


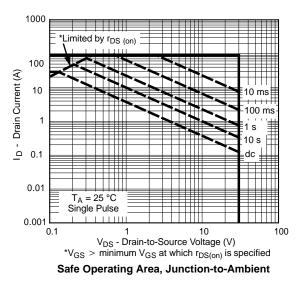


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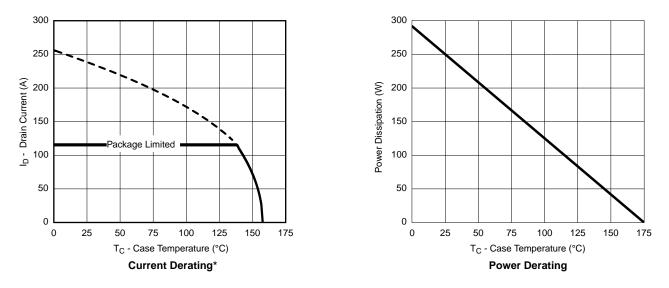






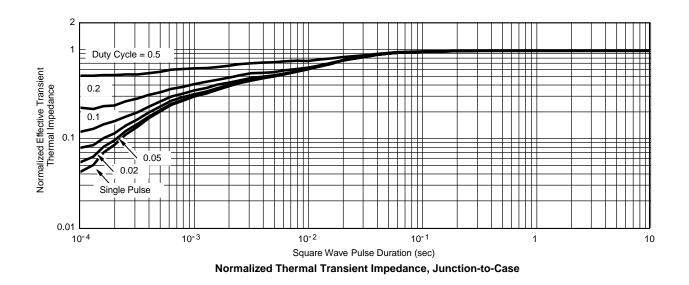






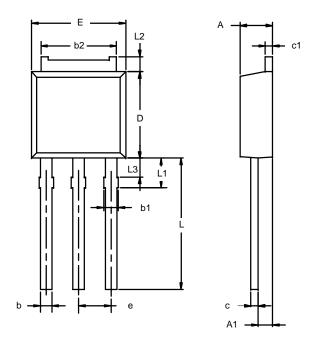
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

*The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

Min 2.21 0.89 0.71 0.76	Max 2.38 1.14 0.89	Min 0.087 0.035	Max 0.094 0.045
0.89 0.71	1.14		
0.71		0.035	0.045
	0.89		0.040
0.76		0.028	0.035
	1.14	0.030	0.045
5.23	5.43	0.206	0.214
0.46	0.58	0.018	0.023
0.46	0.58	0.018	0.023
5.97	6.22	0.235	0.245
6.48	6.73	0.255	0.265
2.28 BSC		0.090 BSC	
8.89	9.53	0.350	0.375
1.91	2.28	0.075	0.090
0.89	1.27	0.035	0.050
1.15	1.52	0.045	0.060
	0.46 0.46 5.97 6.48 2.28 8.89 1.91 0.89 1.15	0.46 0.58 0.46 0.58 5.97 6.22 6.48 6.73 2.28 BSC 8.89 9.53 1.91 2.28 0.89 1.27 1.15 1.52 46—Rev. E, 09-Jul-01	0.46 0.58 0.018 0.46 0.58 0.018 0.46 0.58 0.018 5.97 6.22 0.235 6.48 6.73 0.255 2.28 BSC 0.090 8.89 9.53 0.350 1.91 2.28 0.075 0.89 1.27 0.035 1.15 1.52 0.045 46—Rev. E, 09-Jul-01 0.015

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