

## P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
- 60	0.0195 at V <sub>GS</sub> = - 10 V	- 10	76 nC			
- 00	0.0250 at V <sub>GS</sub> = - 4.5 V	- 9	70110			

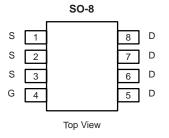
#### FEATURES

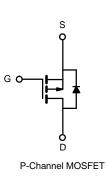
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % UIS Tested

#### **APPLICATIONS**

· Load Switch







Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 60	V		
Gate-Source Voltage		V <sub>GS</sub>	± 20	v	
	T <sub>C</sub> = 25 °C		- 10 <sup>a</sup>		
Continuous Droin Current (T 150 °C)	T <sub>C</sub> = 70 °C		- 9		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	Ι <sub>D</sub>	8 <sup>b</sup>	_	
	T <sub>A</sub> = 70 °C		- 8 <sup>b</sup>	- A	
Pulsed Drain Current	I <sub>DM</sub>	- 50	1		
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	- 45		
Single Pulse Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	101	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	L.	69 <sup>a</sup>	А	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	2.1 <sup>b</sup>	A	
	T <sub>C</sub> = 25 °C		104.2 <sup>a</sup>		
Maximum Dawar Dissinction	T <sub>C</sub> = 70 °C	Б	66.7 <sup>a</sup>	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	PD	3.1 <sup>b</sup>	vv	
	T <sub>A</sub> = 70 °C		2 <sup>b</sup>	1	
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	R <sub>thJA</sub>	33	40	°C/W		
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.98	1.2	C/VV		

Notes:

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a. Based on  $T_C = 25 \ ^{\circ}C$ .

b. Surface mounted on 1" x 1" FR4 board.

### QM6015S

<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	- 60			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		68		- mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η - 200 μΛ		- 5.2			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zana Cata Maltana Drain Comant	I <sub>DSS</sub>	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
Zero Gate Voltage Drain Current		$V_{DS}$ = - 60 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -10 V$	- 120			Α	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 30 A		0.0160			
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 20 A		0.0200		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 50 A	20			S	
Dynamic <sup>b</sup>				1			
Input Capacitance	C <sub>iss</sub>			3500		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = - 25 V, $V_{GS}$ = 0 V, f = 1 MHz		390			
Reverse Transfer Capacitance	C <sub>rss</sub>			290			
	Qg	$V_{DS}$ = - 30 V, $V_{GS}$ = - 10 V, $I_{D}$ = - 55 A		76	115	nC	
Total Gate Charge				38	60		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 30 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 55 A		16			
Gate-Drain Charge	Q <sub>gd</sub>			19			
Gate Resistance	Rg	f = 1 MHz		5.2		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			10	15		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 2 V, $R_L$ = 2 $\Omega$		7	15	- ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D$ $\cong$ - 10 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		70	110		
Fall Time	t <sub>f</sub>			40	60		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	ا <sub>S</sub>	T <sub>C</sub> = 25 °C			- 69	^	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 150	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 30 A		- 1	- 1.5	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			45	68	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	-1. <u></u>		59	120	nC	
Reverse Recovery Fall Time	ta	I <sub>F</sub> = - 50 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		29		1	
Reverse Recovery Rise Time t <sub>b</sub>				16		ns	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

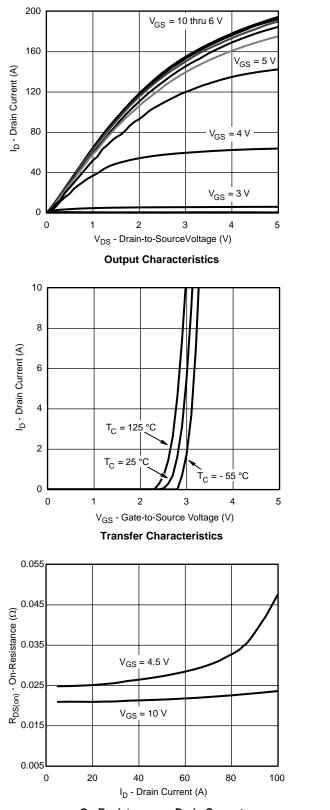
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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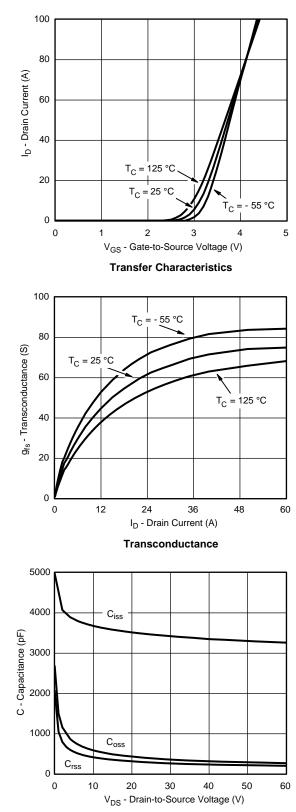
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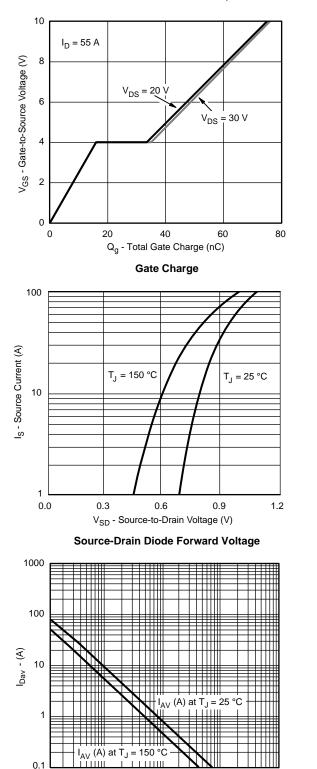
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

**On-Resistance vs. Drain Current** 



Capacitance





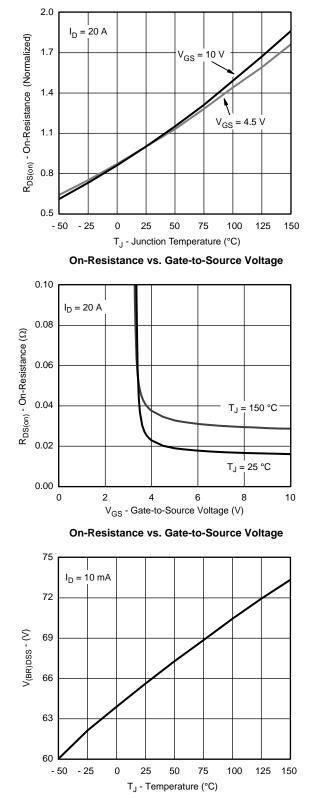
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

 $\label{eq:Tin-s} T_{in} \text{ - } (s)$  Single Pulse Avalanche Current Capability vs. Time

0.01

0.1

1

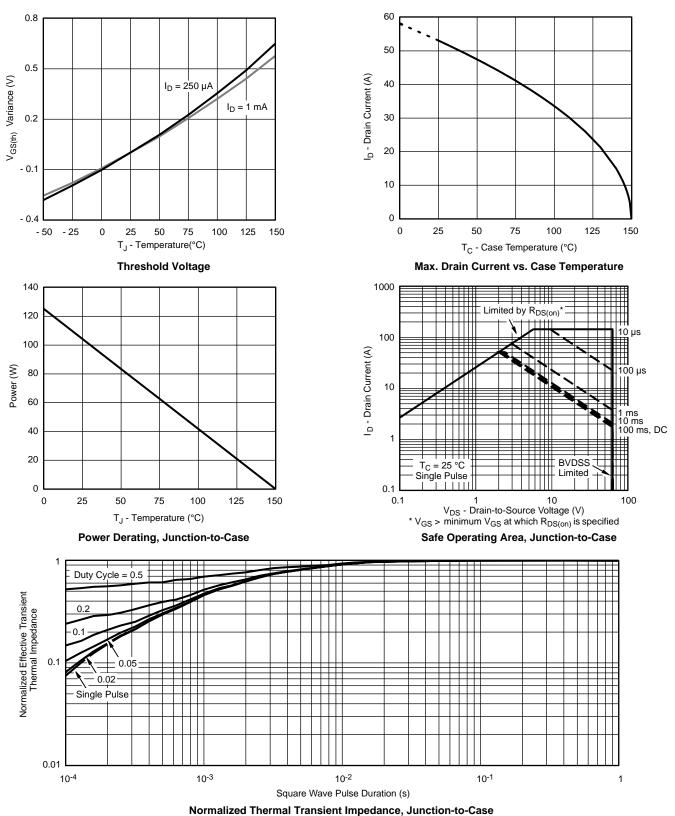


Drain-Source Breakdown Voltage vs. Junction Temperature

0.0001

0.001

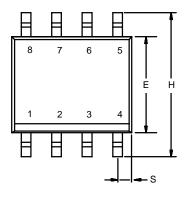




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						



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