

## P-Channel 60-V (D-S) MOSFET

PRODU	ICT SUMMARY		
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
- 60	0.055 at V <sub>GS</sub> = - 10 V	- 7.0	30 nC
- 00	0.065 at V <sub>GS</sub> = - 4.5 V	- 6.0	30 110

#### FEATURES

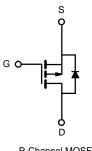
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % UIS Tested

#### **APPLICATIONS**

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATING		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 60	
Gate-Source Voltage		V <sub>GS</sub>	± 20	- V
	T <sub>C</sub> = 25 °C		- 7.0 <sup>a</sup>	
Continuous Drain Current (T 150 °C)	T <sub>C</sub> = 70 °C	_	- 5.2	
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 4.8 <sup>b</sup>	A
	T <sub>A</sub> = 70 °C		- 4.1 <sup>b</sup>	A .
Pulsed Drain Current		I <sub>DM</sub>	- 25	
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	- 4.5	
Single Pulse Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	10.1	mJ
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	6.9 <sup>a</sup>	A
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	'S	3.5 <sup>b</sup>	~
	T <sub>C</sub> = 25 °C		10.4 <sup>a</sup>	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	PD	6.6 <sup>a</sup>	w
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	2.1 <sup>b</sup>	vv
	T <sub>A</sub> = 70 °C		1.1 <sup>b</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	R <sub>thJA</sub>	33	40	°C/W
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.98	1.2	C/VV

Notes:

a. Based on  $T_C = 25 \ ^{\circ}C$ .

b. Surface mounted on 1" x 1" FR4 board.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V$ , $I_D = -250 \mu A$	- 60			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μΑ		68		m)//90	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = -250 \mu A$		- 5.2		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current	1	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = - 60 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 25			Α	
Drain-Source On-State Resistance <sup>a</sup>	Б	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3 A		0.055		Ω	
	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -2 \text{ A}$		0.065			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A	20			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			1500			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = - 25 V, $V_{GS}$ = 0 V, f = 1 MHz		200		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			150		1	
Total Cata Charge	0	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = -10 \text{ V}, \text{ I}_{D} = -5 \text{ A}$	38 56		56		
Total Gate Charge	Qg			19	30	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 30 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 5 A		9			
Gate-Drain Charge	Q <sub>gd</sub>			10			
Gate Resistance	Rg	f = 1 MHz		5.2		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			10	15		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 2 V, $R_L$ = 2 $\Omega$		7	15	1	
Turn-Off Delay Time	t <sub>d(off)</sub>	${\rm I_D}\cong$ - 5 A, ${\rm V_{GEN}}$ = - 10 V, ${\rm R_g}$ = 1 $\Omega$		70	110	ns	
Fall Time	t <sub>f</sub>			40	60		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	ا <sub>S</sub>	T <sub>C</sub> = 25 °C			- 6.9	٨	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 15	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A		- 1	- 1.5	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			45	68	ns	
Body Diode Reverse Recovery Charge Q <sub>rr</sub>				59	120	nC	
Reverse Recovery Fall Time	ta	I <sub>F</sub> = - 5 A, di/dt = 10 A/μs, T <sub>J</sub> = 25 °C		29			
Reverse Recovery Rise Time	t <sub>b</sub>			16		ns	

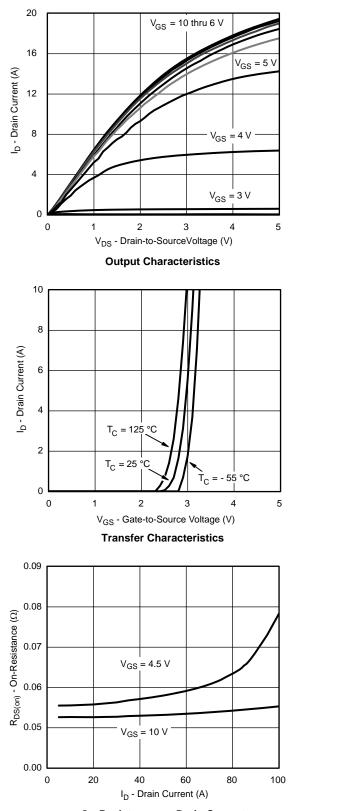
Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

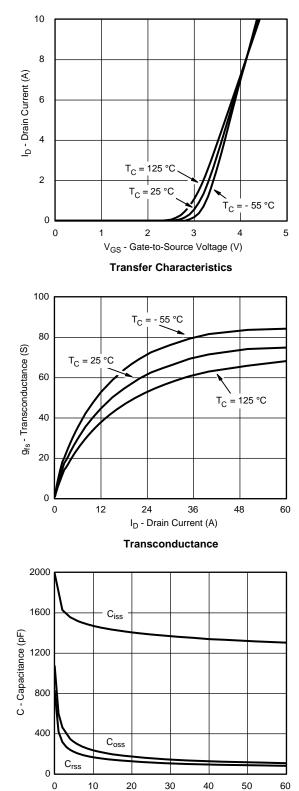
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

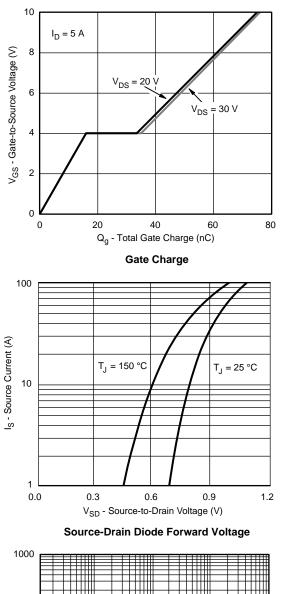
**On-Resistance vs. Drain Current** 



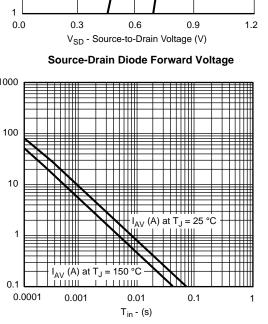
V<sub>DS</sub> - Drain-to-Source Voltage (V)

Capacitance

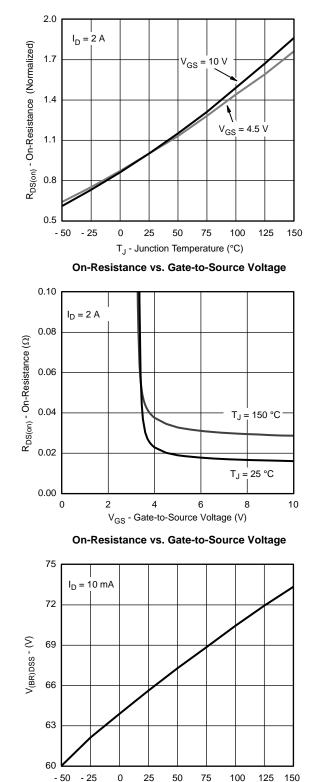




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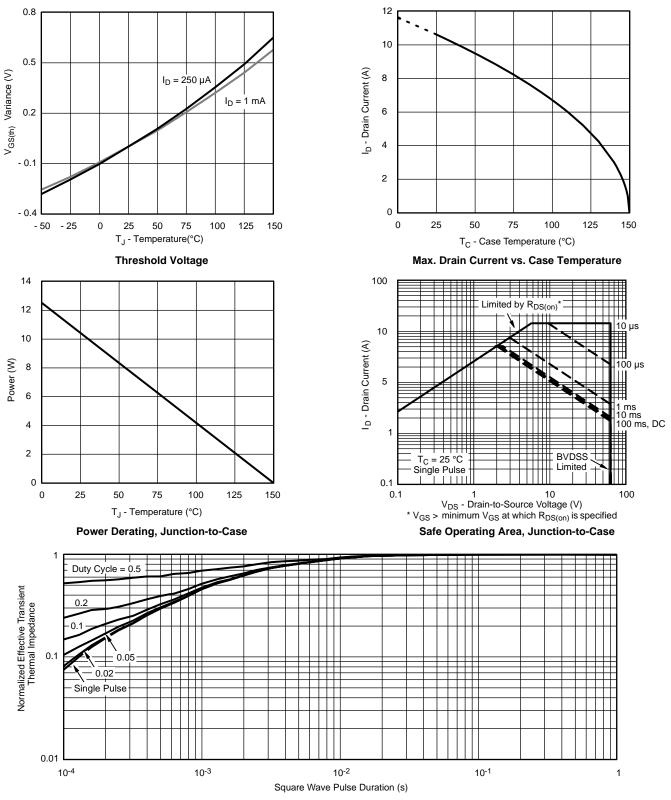
Single Pulse Avalanche Current Capability vs. Time



T<sub>J</sub> - Temperature (°C) Drain-Source Breakdown Voltage vs. Junction Temperature

I<sub>Dav</sub> - (A)



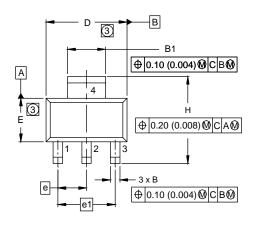


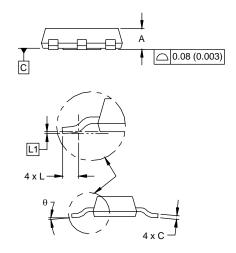
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Normalized Thermal Transient Impedance, Junction-to-Case



### SOT-223 (HIGH VOLTAGE)





DIM.	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
А	1.55	1.80	0.061	0.071	
В	0.65	0.85	0.026	0.033	
B1	2.95	3.15	0.116	0.124	
С	0.25	0.35	0.010	0.014	
D	6.30	6.70	0.248	0.264	
E	3.30	3.70	0.130	0.146	
е	2.30 BSC		0.0905 BSC		
e1	4.60 BSC		0.181 BSC		
Н	6.71	7.29	0.264	0.287	
L	0.91	-	0.036	-	
L1	0.061 BSC		0.002	4 BSC	
θ	-	10'	-	10'	

#### Notes

1. Dimensioning and tolerancing per ASME Y14.5M-1994.

2. Dimensions are shown in millimeters (inches).

3. Dimension do not include mold flash.

4. Outline conforms to JEDEC outline TO-261AA.



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