

N-Channel Er with Schottky		ode Fiel	d Effect Tra	ansistor		
General Description The AO6706 uses advanced trench technology to provide excellent R _{DS(ON)} and low gate charge. A Schottky diode is provided to facilitate the implementation of a bidirectional blocking switch, or for DC-DC conversion applications.			$\label{eq:started} \begin{array}{l} \textbf{Features} \\ V_{DS}\left(V\right) = 20V \\ I_{D} = 6.0A \; (V_{GS} = 4.5V) \\ R_{DS(ON)} < 26m\Omega \; (V_{GS} = 4.5V) \\ R_{DS(ON)} < 35m\Omega \; (V_{GS} = 2.5V) \\ R_{DS(ON)} < 45m\Omega \; (V_{GS} = 1.8V) \\ \textbf{SCHOTTKY} \\ V_{DS} \; (V) = 20V, \; I_{F} = 1A, \; V_{F} < 0.5V @ 0.5A \end{array}$			
A $\begin{bmatrix} 1 & 6 \\ S & 2 & 5 \\ G & 3 & 4 \end{bmatrix} D$ TSOP6 Absolute Maximum Ratings $T_A=2$	o G		К А			
Parameter	25 C unless otherw	Symbol	MOSFET	Schottky	Units	
Drain-Source Voltage		V _{DS}	20		V	
Gate-Source Voltage		V _{GS}	±8		V	
Continuous Drain Current ^A	T _A =25°C T _A =70°C	- I _D	6.0 4.5		А	
Pulsed Drain Current ^B		I _{DM}	18			
Schottky reverse voltage	T _A =25°C	V _{KA}		20 2	V	
Continuous Forward Current ^A	T _A =23 C	I _F		1	А	
Pulsed Forward Current ^B		I _{FM}		10		
	T _A =25°C		1.50	1.0	1.44	
Power Dissipation	T _A =70°C	P _D	0.9	0.6	W	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	-55 to 150	°C	
Parameter: Thermal Characterist	ICS MOSFET	Symbol	Тур	Мах	Units	
Maximum Junction-to-Ambient ^A	t ≤ 10s		80.3	110		
Maximum Junction-to-Ambient ^A	Steady-State	R _{θJA}	117	150	°C/W	
Maximum Junction-to-Lead ^C	Steady-State	$R_{ ext{ hetaJL}}$	43	80	1	
Thermal Characteristics Schottky		1	1	1		
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{0JA}	109.4	135	4	
Maximum Junction-to-Ambient A	Steady-State	Ala	136.5	175	°C/W	
Maximum Junction-to-Lead ^C	Steady-State	$R_{\theta JL}$	58.5	80		





Electrical Characteristics (T J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Units	
STATIC I	PARAMETERS				-		
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0V	20			V	
1	Zero Gate Voltage Drain Current	V _{DS} =16V, V _{GS} =0V			1	μA	
I _{DSS} Zei	Zero Gale Voltage Drain Current	T _J =55°C			5		
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} =±8V			100	nA	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS}=V_{GS}$ I _D =250µA	0.4		1.5	V	
I _{D(ON)}	On state drain current	V_{GS} =4.5V, V_{DS} =5V	10			Α	
		V _{GS} =4.5V, I _D =3.8A		23		mΩ	
R _{DS(ON)}	Static Drain-Source On-Resistance	T _J =125°C		42		1115.2	
US(ON)	Static Drain-Source On-Resistance	V _{GS} =2.5V, I _D =3.3A		27		mΩ	
		V _{GS} =1.8V, I _D =2.8A		40		mΩ	
g fs	Forward Transconductance	V _{DS} =5V, I _D =3.8A		10.5		S	
V _{SD}	Diode Forward Voltage	I _S =1A,V _{GS} =0V		0.8	1	V	
I _S	Maximum Body-Diode Continuous Curre	ent			1.8	Α	
DYNAMI	C PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =10V, f=1MHz		449		pF	
C _{oss}	Output Capacitance			74		pF	
C _{rss}	Reverse Transfer Capacitance	1		51.6		pF	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		4.9		Ω	
SWITCH	ING PARAMETERS						
Q _g	Total Gate Charge			5.9		nC	
Q _{gs}	Gate Source Charge	V _{GS} =4.5V, V _{DS} =10V, I _D =3.8A		0.36		nC	
Q _{gd}	Gate Drain Charge			1.3		nC	
t _{D(on)}	Turn-On DelayTime			4.5		ns	
t _r	Turn-On Rise Time	V _{GS} =5V, V _{DS} =10V, R _L =2.6Ω, R _{GEN} =0Ω		6		ns	
t _{D(off)}	Turn-Off DelayTime			32.7		ns	
t _f	Turn-Off Fall Time	1		7.1		ns	
t _{rr}	Body Diode Reverse Recovery Time	I _F =3.8A, dI/dt=100A/μs		13		ns	
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =3.8A, dI/dt=100A/μs		3.3		nC	
SCHOTT	KY PARAMETERS						
V _F	Forward Voltage Drop	I _F =0.5A		0.39	0.5	V	
I _m	Maximum reverse leakage current	V _R =16V			0.02	2 mA	
		V _R =16V, T _J =125°C			20		
CT	Junction Capacitance	V _R =10V		34		pF	
t _{rr}	SchottkyReverse Recovery Time	I _F =1A, dl/dt=100A/µs		5.2	10	ns	
Q _{rr}	Schottky Reverse Recovery Charge	I _F =1A, dI/dt=100A/µs		0.8		nC	

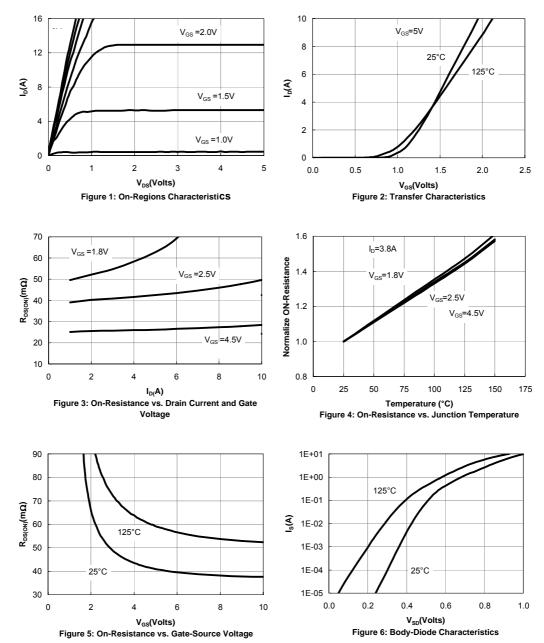
A: The value of R_{oJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}C$. The value in any given application depends on the user's specific board design. The current rating is based on the t < 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The R $_{\text{0JA}}$ is the sum of the thermal impedence from junction to lead R $_{\text{0JL}}$ and lead to ambient. D. The static characteristics in Figures 1 to 6 are obtained using 80µs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}C$. The SOA curve provides a single pulse rating.





TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



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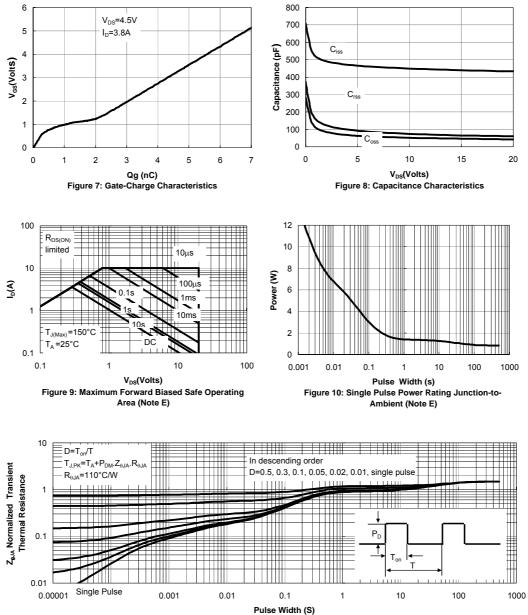
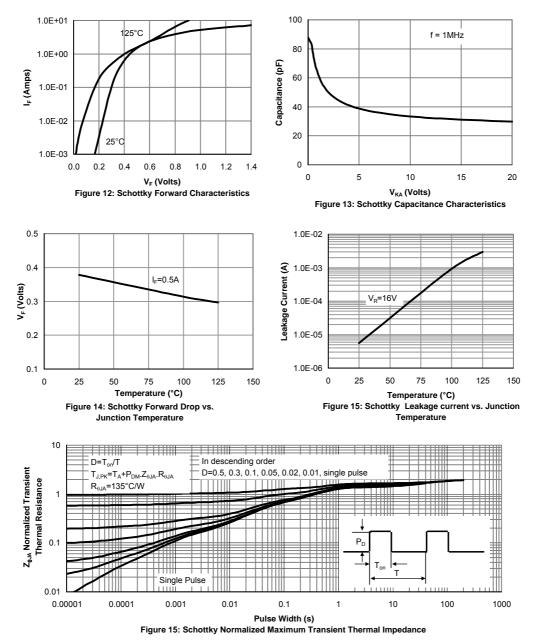


Figure 11: Normalized Maximum Transient Thermal Impedence



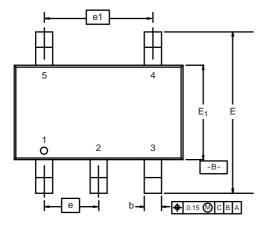


TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: SCHOTTKY

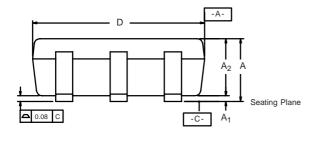


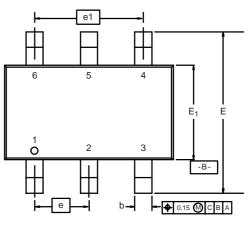


TSOP: 5/6–LEAD JEDEC Part Number: MO-193C

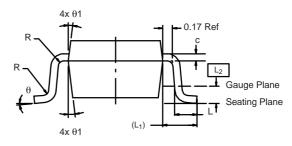


5-LEAD TSOP





6-LEAD TSOP



	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref		0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ1		7° Nom		7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							



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