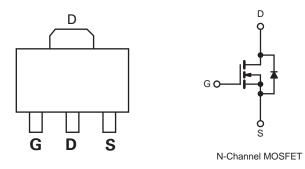


## N-Channel 100 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY							
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) Typ.	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)				
	0.102 at V <sub>GS</sub> = 10 V	4.2					
100	0.120 at V <sub>GS</sub> = 6 V	/ <sub>GS</sub> = 6 V 3.8 2					
	0.125 at V <sub>GS</sub> = 4.5 V	3.6					



#### FEATURES

- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested



RoHS COMPLIANT HALOGEN FREE

#### APPLICATIONS

- DC/DC Converters / Boost Converters
- Load Switch
- LED Backlighting in LCD TVs
- Power Management for Mobile Computing

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	± 20		
	T <sub>C</sub> = 25 °C		4.2	
Continuous Drain Current (T <sub>1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	3.5	
Continuous Drain Gunenic (1) = 100 °C)	T <sub>A</sub> = 25 °C	U	3.2 <sup>b,c</sup>	
	T <sub>A</sub> = 70 °C		2.8 <sup>b,c</sup>	^
Pulsed Drain Current (t = 300 µs)	I <sub>DM</sub>	15	— A	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	la la	2.1	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	۱ <sub>S</sub>	1 <sup>b, c</sup>	
Single Pulse Avalanche Current		I <sub>AS</sub>	3	
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	0.45	mJ
	T <sub>C</sub> = 25 °C		2.5	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	PD	1.6	w
	T <sub>A</sub> = 25 °C	. 0	1.25 <sup>b, c</sup>	~ ~ ~
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS									
Parameter	Symbol	Typical	Maximum	Unit					
Maximum Junction-to-Ambient <sup>b, d</sup>	≤ 5 s	R <sub>thJA</sub>	75	100	°C/W				
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	40	50	C/W				

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 166  $^{\circ}\text{C/W.}$ 

<b>MOSFET SPECIFICATIONS</b>	(T <sub>J</sub> = 25 °C,	, unless otherwise noted)					
Parameter	Symbol	Min.	Тур.	Max.	Unit		
Static	· · ·						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_{D} = 250 \mu A$	100			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			59		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 4.8			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.2		3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V			± 100	nA	
Zero Gate Voltage Drain Current	la a a	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
	DSS	$V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	5			Α	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 2 \text{ A}$		0.102		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 6 V, I <sub>D</sub> = 1 A		0.120			
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1 A		0.125			
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 2 \text{ A}$		5		S	
Dynamic <sup>b</sup>	<u> </u>				L	1	
Input Capacitance	C <sub>iss</sub>		1	196		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz		67			
Reverse Transfer Capacitance				14		1 '	
		$V_{DS} = 50$ V, $V_{GS} = 10$ V, $I_{D} = 2.2$ A		5.2	10.4	+	
Total Gate Charge	Qg			2.9	5.8	- nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 50$ V, $V_{GS} = 4.5$ V, $I_{D} = 2.2$ A		1			
Gate-Drain Charge	Q <sub>gd</sub>			1.4			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.9	4.3	8.6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			40	60		
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, \text{ R}_{\text{I}} = 27.7 \Omega$		68	102	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{D} = 1.8 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_{g} = 1 \Omega$		14	21		
Fall Time	t <sub>f</sub>	-		20	30	-	
Turn-On Delay Time	t <sub>d(on)</sub>			8	16	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, \text{ R}_{\text{I}} = 27.7 \Omega$		10	20	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{D} = 1.8 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$		10	20	1	
Fall Time	t <sub>f</sub>	· ·		7	14	1	
Drain-Source Body Diode Characteristi			1	1	1	1	
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.1	_	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 8	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1.8 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			23	35	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 1.8 A, dl/dt = 100 A/μs,		21	32	nC	
	1	T <sub>J</sub> = 25 °C		1		ns	
Reverse Recovery Fall Time	t <sub>a</sub>	1 ] = 25 0		17			

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

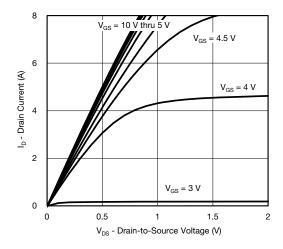
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

emi

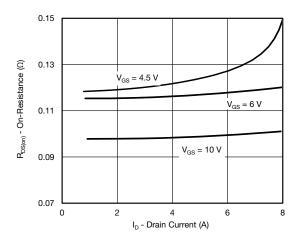
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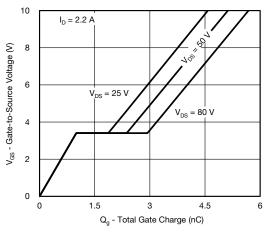


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

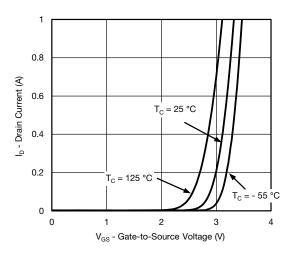




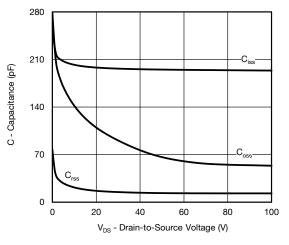
**On-Resistance vs. Drain Current and Gate Voltage** 



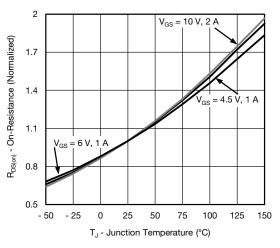
**Gate Charge** 



**Transfer Characteristics** 



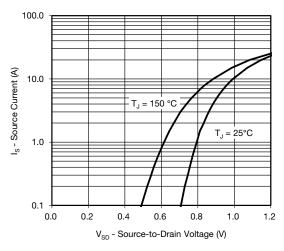




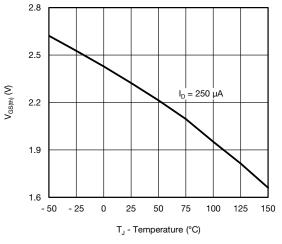
**On-Resistance vs. Junction Temperature** 



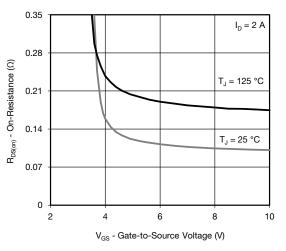




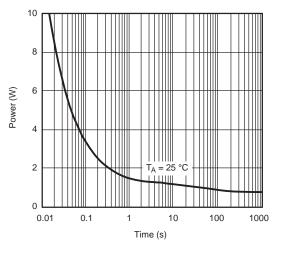




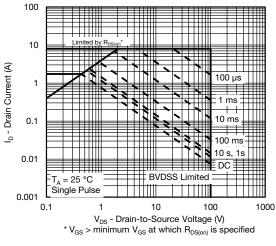
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage



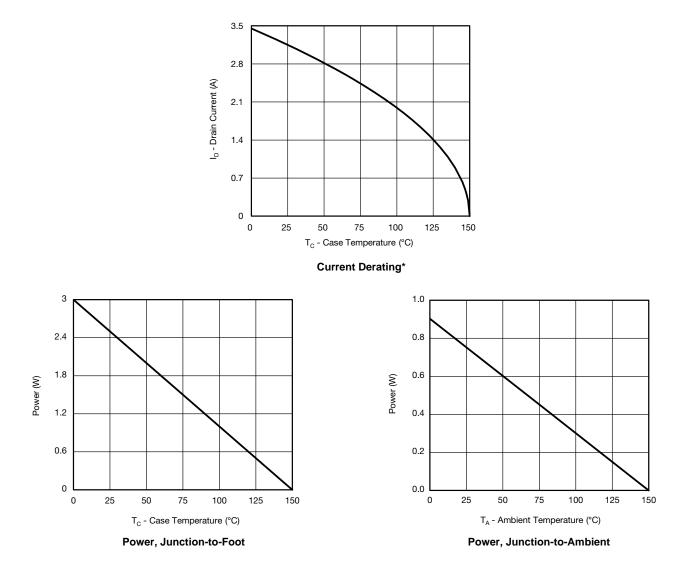




Safe Operating Area



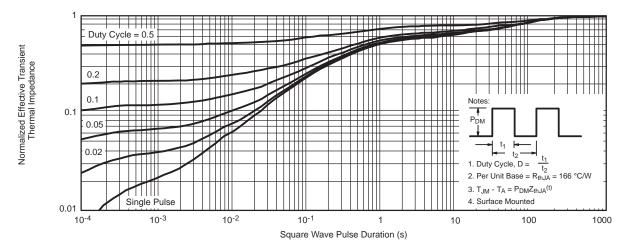
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



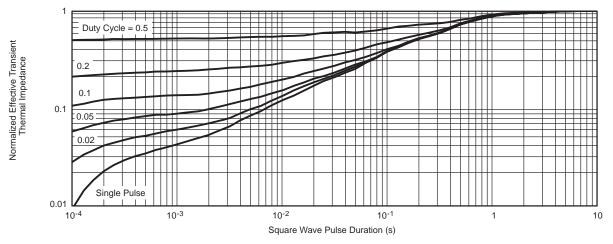
\* The power dissipation  $P_D$  is based on  $T_{J(max.)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



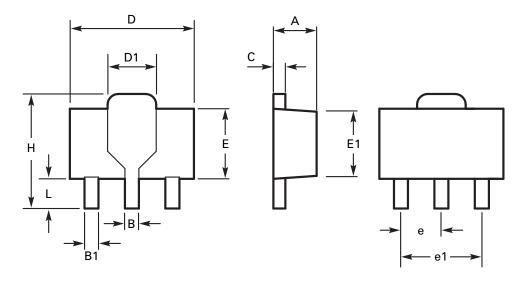
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



### Package outline - SOT89



DIM	Millim	neters	Inc	Inches DIM Millimeters Incl		Millimeters		hes	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	E	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches



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