RoHS

COMPLIANT

HALOGEN

FREE

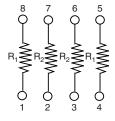


Molded, 50 mil Pitch, High Temperature (215 °C); Thin Film Surface Mount, Dual-In-Line Resistor Network



HTRN series resistor networks feature four isolated resistors with standard 50 mil pitch lead spacing. HTRN is ideal to be used in oil/gas exploration industry, automotive under the hood applications, and aerospace engine control high temperature applications. The networks feature close TCR tracking and tight ratio tolerance and are ideally suited for unity gain operational amplifier circuitry. The standard resistance offering listed are available for immediate delivery.

SCHEMATIC



FEATURES

- Ratio tolerance to ± 0.05 %
- Ratio stability ± 0.1 %
- - 55 °C to 215 °C operating temperature range
- 0.068" (1.73 mm) maximum seated height
- Rugged molded case construction with no internal solder
- Low temperature coefficient (± 25 ppm/°C)
- JEDEC MS-012 STD variation AA package
- Gold terminations for durable attach bonds
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

TYPICAL PERFORMANCE

| | ABSOLUTE | TRACKING |
|------|----------|----------|
| TCR | 25 | 5 |
| | ABSOLUTE | RATIO |
| TOL. | 0.1 | 0.05 |

| STANDARD RESISTANCE OFFERING (R_1/R_2) | | |
|--|----------------|----------------|
| RATIO | R ₁ | R ₂ |
| 100:1 | 100K | 1K |
| 50:1 | 50K | 1K |
| 25:1 | 25K | 1K |
| 20:1 | 20K | 1K |
| 10:1 | 10K | 1K |
| 5:1 | 10K | 2K |
| 2:1 | 10K | 5K |
| 4:1 | 4K | 1K |

Note

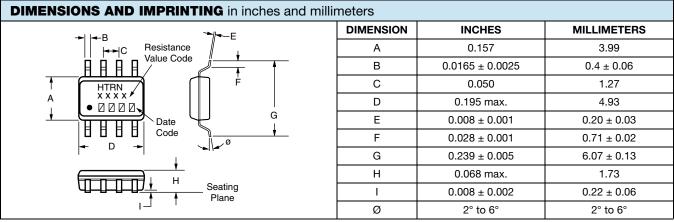
Consult factory for additional values and schematics

| STANDARD ELECTRICAL SPECIFICATIONS | | |
|------------------------------------|--|--|
| TEST | SPECIFICATIONS | CONDITIONS |
| Material | Passivated nichrome | - |
| Pin/Lead Number | 8 | - |
| Resistance Range | 1000 Ω to 100 k Ω per resistor | - |
| TCR: Absolute | ± 25 ppm/°C | - 55 °C to + 125 °C |
| TCR: Tracking | ± 5 ppm/°C | - 55 °C to + 125 °C |
| Tolerance: Absolute | 0.1 % | + 25 °C |
| Tolerance: Ratio | 0.05 % | + 25 °C |
| Power Rating: Resistor | 100 mW | Maximum at + 70 °C |
| Power Rating: Package | 400 mW | Maximum at + 70 °C |
| Stability: Absolute | ΔR ± 0.5 % | 2000 h at + 215 °C at 25 % rated power |
| Stability: Ratio | $\Delta R \pm 0.1 \%$ | 2000 h at + 215 °C at 25 % rated power |
| Voltage Coefficient | 0.1 ppm/V (typical) | - |
| Working Voltage | 100 V max. not to exceed $\sqrt{P \times R}$ | - |
| Operating Temperature Range | - 55 °C to + 215 °C | - |
| Storage Temperature Range | - 55 °C to + 215 °C | - |
| Noise | < - 30 dB | - |
| Thermal EMF | 0.08 μV/°C | - |
| Shelf Life Stability: Absolute | $\Delta R \pm 0.01 \%$ | 1 year at + 25 °C |
| Shelf Life Stability: Ratio | $\Delta R \pm 0.002 \%$ | 1 year at + 25 °C |

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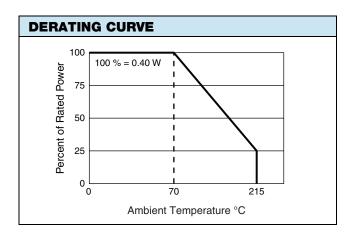
Vishay Dale Thin Film

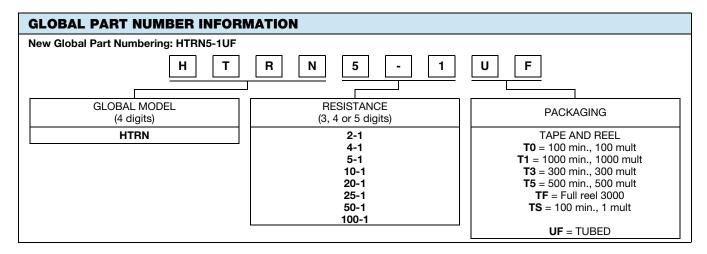


Note

• Marking - Vishay symbol, part number from ordering information

| MECHANICAL SPECIFICATIONS | | |
|---------------------------|---------------------|--|
| Resistive Element | Passivated nichrome | |
| Substrate Material | Silicon | |
| Body | Molded epoxy | |
| Terminals | Copper | |
| Termination Finish | Plated Ni/Pd/Au | |







Legal Disclaimer Notice

Vishay

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