LM185-1.2-N/LM285-1.2-N/LM385-1.2-N Micropower Voltage Reference Diode

Check for Samples: LM185-1.2-N, LM285-1.2-N, LM385-1.2-N

FEATURES

- **±1% and 2% Initial Tolerance**
- **Operating Current of 10μA to 20mA**
- **1Ω Dynamic Impedance**
- **Low Temperature Coefficient**
- **Low Voltage Reference—1.235V**
- **2.5V Device and Adjustable Device Also Available**
- **LM185-2.5 Series and LM185 Series, respectively**

DESCRIPTION

The LM185-1.2-N/LM285-1.2-N/LM385-1.2-N are micropower 2-terminal band-gap voltage regulator diodes. Operating over a 10μA to 20mA current range, they feature exceptionally low dynamic impedance and good temperature stability. On-chip trimming is used to provide tight voltage tolerance. Since the LM185-1.2-N band-gap reference uses only transistors and resistors, low noise and good long term stability result.

Careful design of the LM185-1.2-N has made the device exceptionally tolerant of capacitive loading, making it easy to use in almost any reference application. The wide dynamic operating range allows its use with widely varying supplies with excellent regulation.

The extremely low power drain of the LM185-1.2-N makes it useful for micropower circuitry. This voltage reference can be used to make portable meters, regulators or general purpose analog circuitry with battery life approaching shelf life.

Further, the wide operating current allows it to replace older references with a tighter tolerance part.

The LM185-1.2-N is rated for operation over a −55°C to 125°C temperature range while the LM285-1.2-N is rated −40°C to 85°C and the LM385-1.2-N 0°C to 70°C. The LM185-1.2-N/LM285-1.2-N are available in a hermetic TO package and the LM285-1.2-N/LM385-1.2-N are also available in a low-cost TO-92 molded package, as well as SOIC and SOT-23.

CONNECTION DIAGRAM

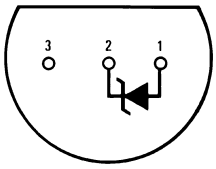
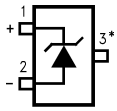


Figure 1. T0-92 Package (LP)
(Bottom View)



* Pin 3 is attached to the Die Attach Pad (DAP) and should be connected to Pin 2 or left floating.

Figure 2. SOT-23

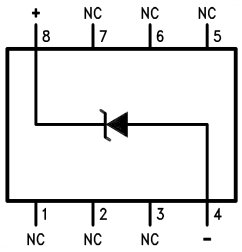


Figure 3. SOIC Package

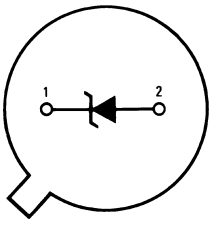


Figure 4. TO Package (NDV)
(Bottom View)

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ABSOLUTE MAXIMUM RATINGS(1)(2)(3)

Reverse Current	30mA
Forward Current	10mA
Operating Temperature Range (4)	
LM185-1.2-N	−55°C to +125°C
LM285-1.2-N	−40°C to +85°C
LM385-1.2-N	0°C to 70°C
ESD Susceptibility (5)	2kV
Storage Temperature	−55°C to +150°C
Soldering Information	
TO-92 package: 10 sec.	260°C
TO package:10 sec.	300°C
SOIC and SOT-23 Pkg.	
Vapor phase (60 sec.)	215°C
Infrared (15 sec.)	220°C
See AN-450 “Surface Mounting Methods and Their Effect on Product Reliability” for other methods of soldering surface mount devices.	

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional. For specifications and test conditions, see the Electrical Characteristics. The specifications apply only for the test conditions listed.
(2) Refer to RETS185H-1.2 for military specifications.
(3) If Military/Aerospace specified devices are required, please contact the TI Sales Office/Distributors for availability and specifications.
(4) For elevated temperature operation, see Table 1.
(5) The human body model is a 100 pF capacitor discharged through a 1.5 kΩ resistor into each pin.

Table 1. $T_{J(max)}$ for Elevated Temperature Operation

DEVICE	$T_{J(max)}$ (°C)
LM185-1.2-N	150
LM285-1.2-N	125
LM385-1.2-N	100

ELECTRICAL CHARACTERISTICS(1)

Parameter	Conditions	Typ	LM185-1.2-N LM185BX-1.2-N LM185BY-1.2-N LM285-1.2-N LM285BX-1.2-N LM285BY-1.2-N		LM385B-1.2-N LM385BX-1.2-N LM385BY-1.2-N		LM385-1.2-N		Units (Limit)
			Tested Limit (2) (3)	Design Limit (4)	Tested Limit (2)	Design Limit (4)	Tested Limit (2)	Design Limit (4)	
Reverse Breakdown Voltage	T_A = 25°C, 10µA ≤ I_R ≤ 20mA	1.235	1.223 1.247		1.223 1.247		1.205 1.260		V(Min) V(Max)
Minimum Operating Current		8	10	**20**	15	**20**	15	**20**	µA
	LM385M3-1.2-N						10	**15**	(Max)
Reverse Breakdown Voltage Change with Current	10µA ≤ I_R ≤ 1mA		1	**1.5**	1	**1.5**	1	**1.5**	mV (Max)
	1mA ≤ I_R ≤ 20mA		10	**20**	20	**25**	20	**25**	mV (Max)
Reverse Dynamic Impedance	I_R = 100µA, f = 20Hz	1							Ω
Wideband Noise (rms)	I_R = 100µA, 10Hz ≤ f ≤ 10kHz	60							µV
Long Term Stability	I_R = 100µA, T = 1000 Hr, T_A = 25°C ±0.1°C	20							ppm
Average Temperature Coefficient (5)	I_R = 100µA X Suffix Y Suffix All Others		 **30** **50**	 **150**	 **30** **50**	 **150**	 	 **150**	 ppm/°C ppm/°C ppm/°C (Max)

(1) Parameters identified with boldface type apply at temperature extremes. All other numbers apply at T_A = T_J = 25°C.
(2) Production tested.
(3) A military RETS electrical specification is available on request.
(4) Specified by design. Not production tested. These limits are not used to calculate average outgoing quality levels.
(5) The average temperature coefficient is defined as the maximum deviation of reference voltage at all measured temperatures between the operating T_{MAX} and T_{MIN}, divided by T_{MAX} − T_{MIN}. The measured temperatures are −55°C, −40°C, 0°C, 25°C, 70°C, 85°C, 125°C.

THERMAL CHARACTERISTICS

Thermal Resistance	TO-92	TO	SOIC	SOT-23
θ_{JA} (junction to ambient)	180°C/W (0.4″ leads) 170°C/W (0.125″ leads)	440°C/W	165°C/W	283°C/W
θ_{JC} (junction to case)	N/A	80°C/W	N/A	N/A

TYPICAL PERFORMANCE CHARACTERISTICS

Reverse Characteristics

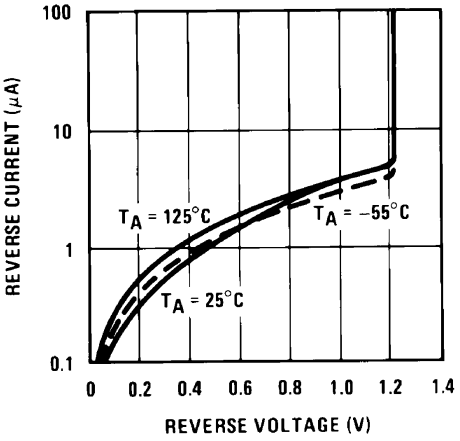


Figure 5.

Reverse Characteristics

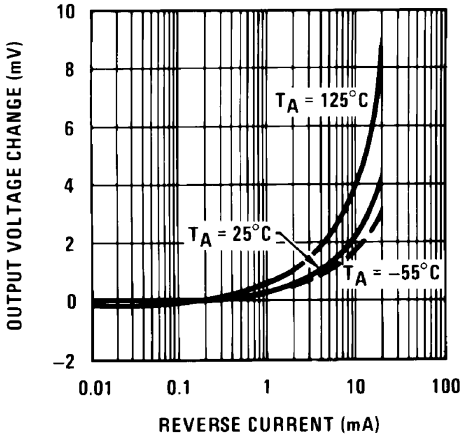


Figure 6.

Forward Characteristics

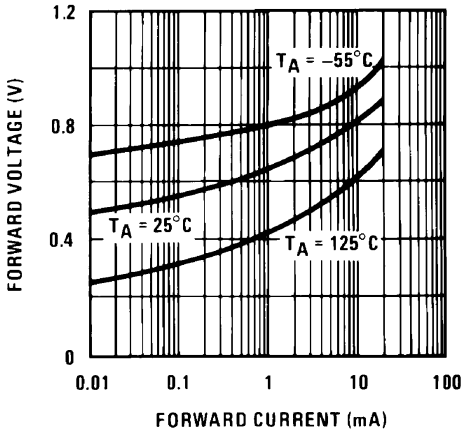


Figure 7.

Temperature Drift of 3 Representative Units

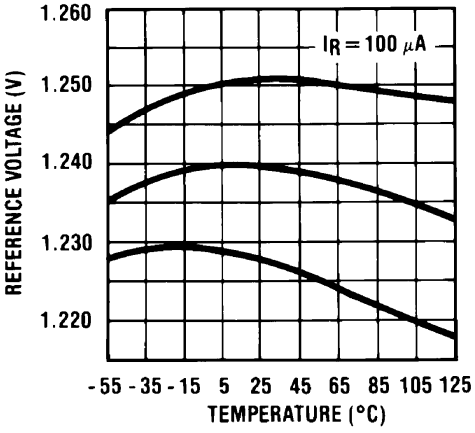


Figure 8.

Reverse Dynamic Impedance

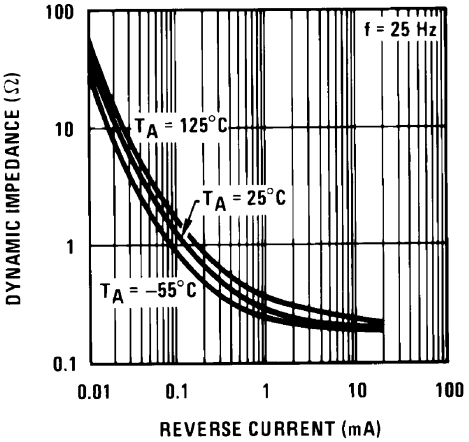


Figure 9.

Reverse Dynamic Impedance

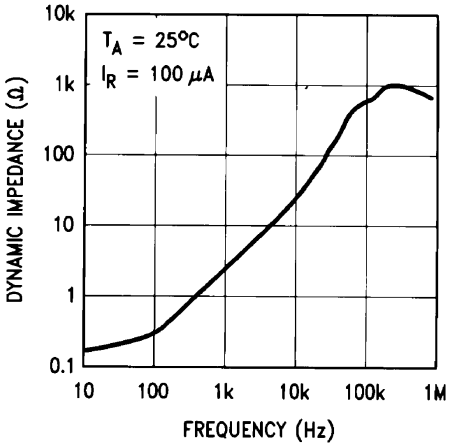


Figure 10.

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Noise Voltage

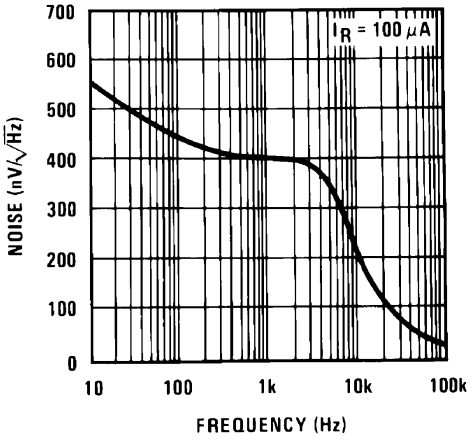


Figure 11.

Filtered Output Noise

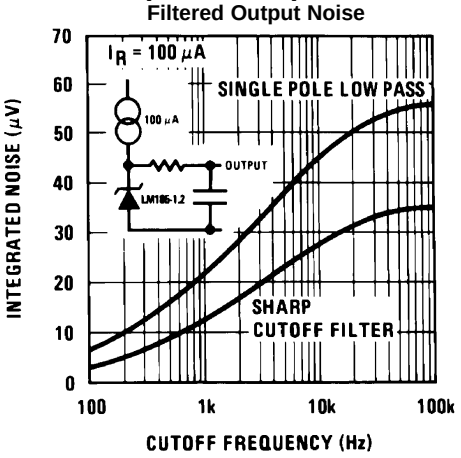


Figure 12.

Response Time

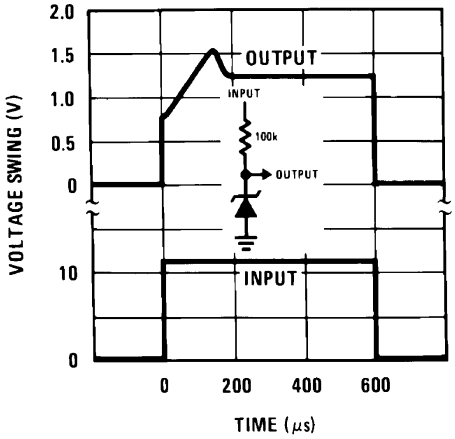


Figure 13.

TYPICAL APPLICATIONS

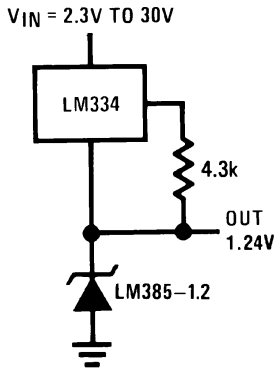


Figure 14. Wide Input Range Reference

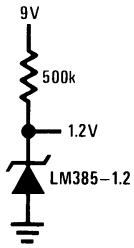


Figure 15. Micropower Reference from 9V Battery

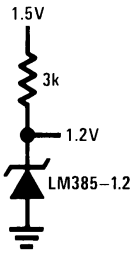
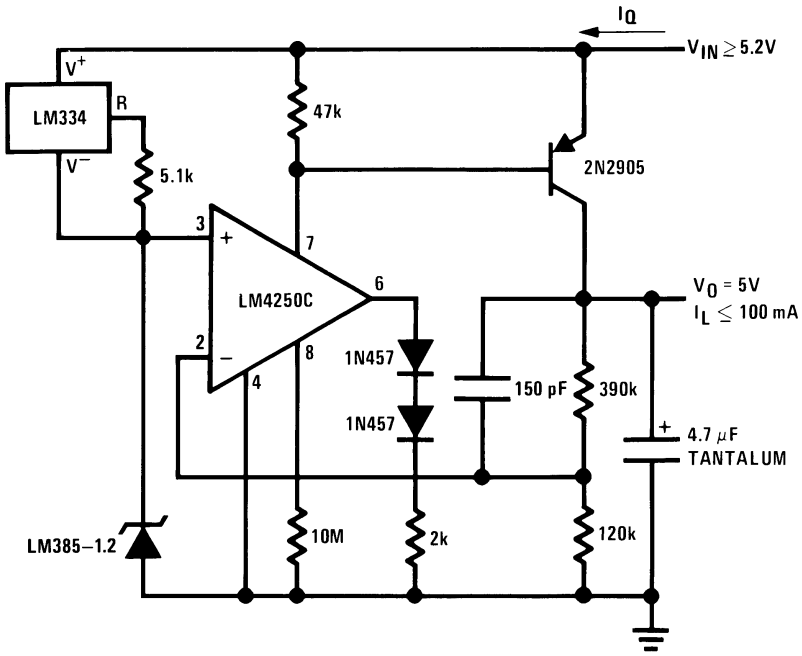
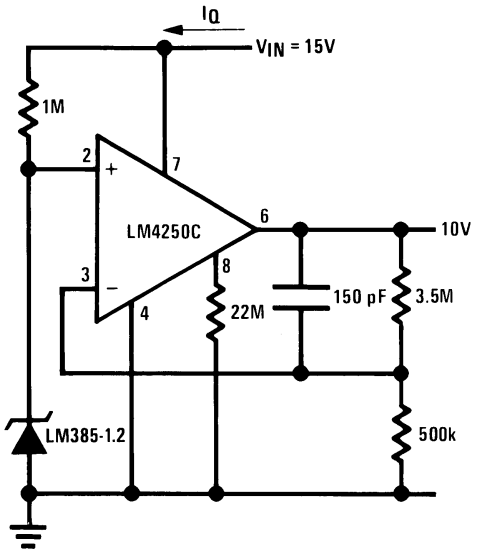


Figure 16. Reference from 1.5V Battery



*$I_Q \simeq 30\mu A$

Figure 17. Micropower* 5V Regulator



*I_Q ≈20µA standby current

Figure 18. Micropower* 10V Reference

*$I_{OUT} = \frac{1.23V}{R2}$

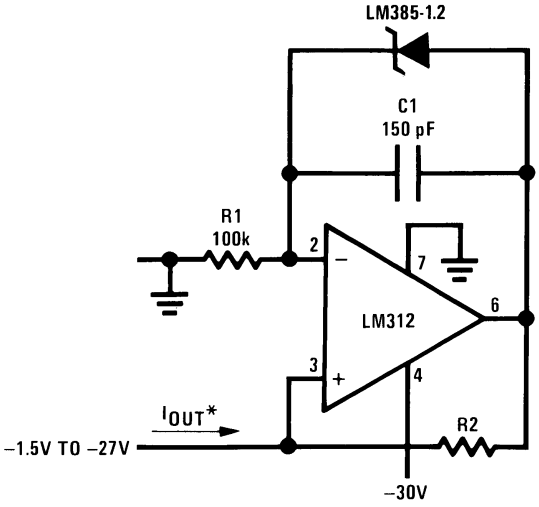


Figure 19.

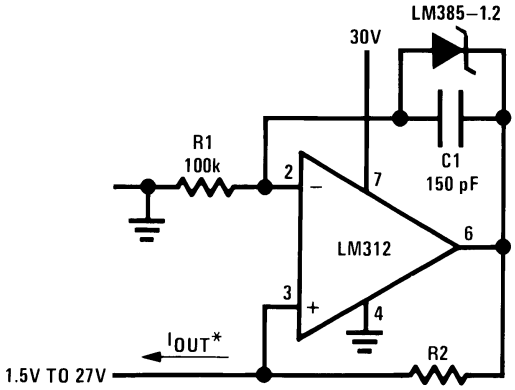
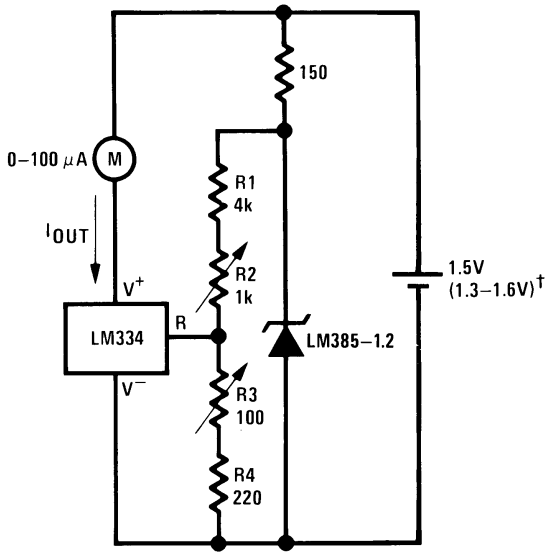


Figure 20. Precision 1µA to 1mA Current Sources

METER THERMOMETERS



Calibration

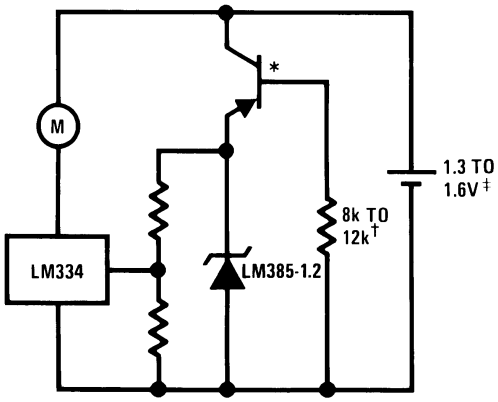
1. Short LM385-1.2-N, adjust R3 for I_{OUT}= temp at 1μA/°K
2. Remove short, adjust R2 for correct reading in centigrade

†I_Q at 1.3V≅500μA

I_Q at 1.6V≅2.4mA

Figure 21. 0°C−100°C Thermometer

Figure 22.

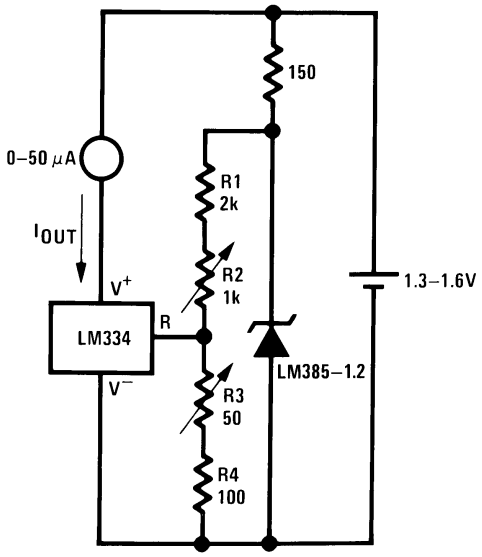


*2N3638 or 2N2907 select for inverse H_{FE} ≅ 5

†Select for operation at 1.3V

‡I_Q ≅ 600μA to 900μA

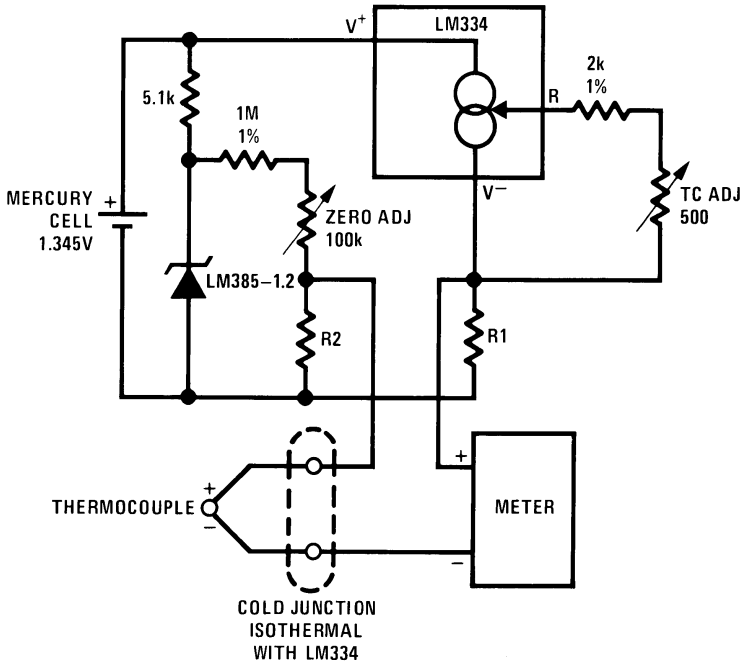
Figure 23. Lower Power Thermometer



Calibration

1. Short LM385-1.2-N, adjust R3 for I_{OUT}= temp at 1.8μA/°K
2. Remove short, adjust R2 for correct reading in °F

Figure 24. 0°F−50°F Thermometer



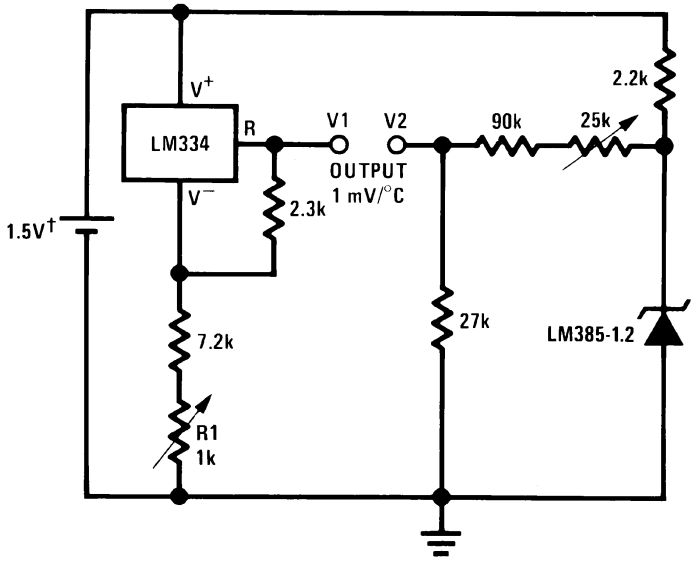
Adjustment Procedure

1. Adjust TC ADJ pot until voltage across R1 equals Kelvin temperature multiplied by the thermocouple Seebeck coefficient.
2. Adjust zero ADJ pot until voltage across R2 equals the thermocouple Seebeck coefficient multiplied by 273.2.

Figure 25. Micropower Thermocouple Cold Junction Compensator

Thermocouple Type	Seebeck Coefficient (μV/°C)	R1 (Ω)	R2 (Ω)	Voltage Across R1 @ 25°C (mV)	Voltage Across R2 (mV)
J	52.3	523	1.24k	15.60	14.32
T	42.8	432	1k	12.77	11.78
K	40.8	412	953Ω	12.17	11.17
S	6.4	63.4	150Ω	1.908	1.766

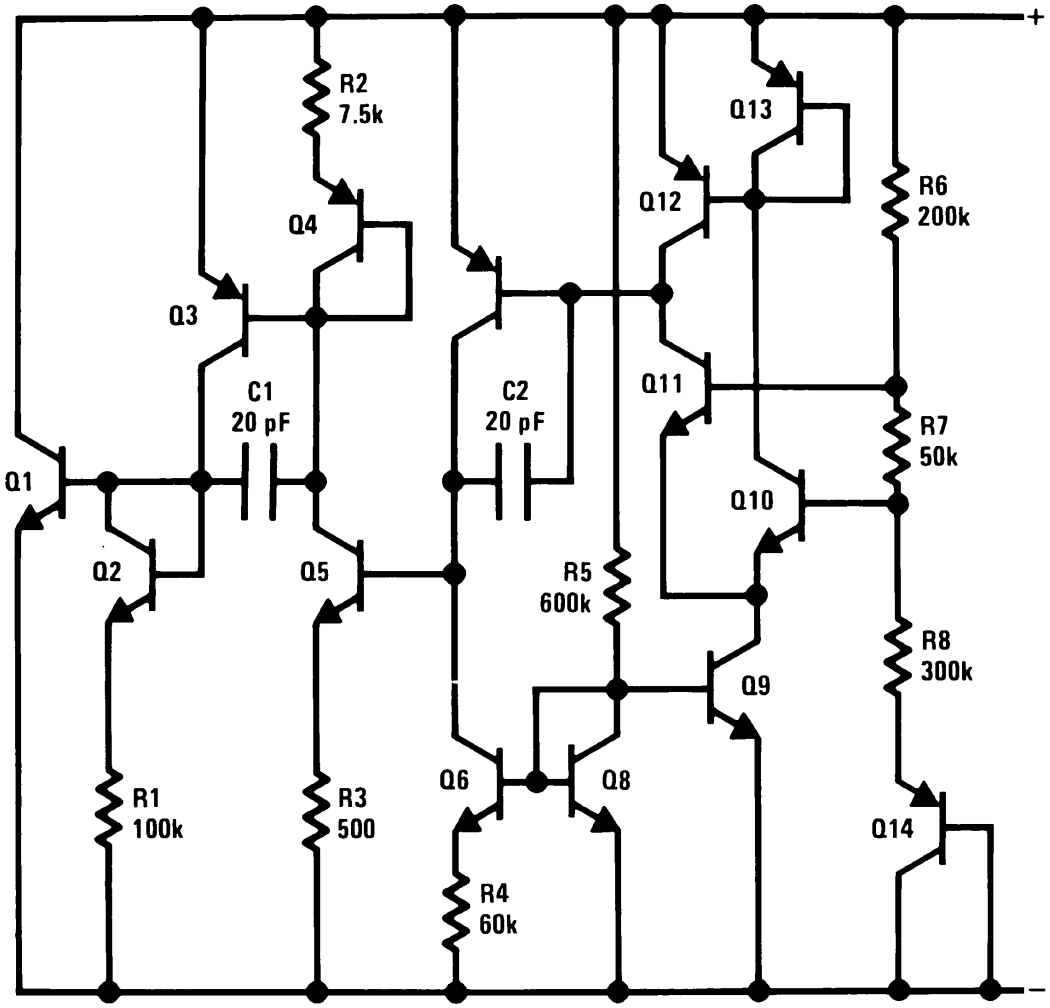
Typical supply current 50μA



Calibration
1. Adjust R1 so that V1 = temp at 1mV/°K
2. Adjust V2 to 273.2mV
†I_Q for 1.3V to 1.6V battery voltage = 50μA to 150μA

Figure 26. Centigrade Thermometer

SCHEMATIC DIAGRAM



REVISION HISTORY

Changes from Revision D (April 2013) to Revision E	Page
• Changed layout of National Data Sheet to TI format	11

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM185BYH-1.2/NOPB	ACTIVE	TO	NDU	2	1000	RoHS & Green	SNAGCU	Level-1-NA-UNLIM	-55 to 125	(LM185BYH1.2, LM185BYH1.2)	Samples
LM185H-1.2/NOPB	ACTIVE	TO	NDU	2	1000	RoHS & Green	SNAGCU	Level-1-NA-UNLIM	-55 to 125	(LM185H1.2, LM185H1.2)	Samples
LM285BXM-1.2/NOPB	ACTIVE	SOIC	D	8	95	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 85	285BX M1.2	Samples
LM285BXMX-1.2/NOPB	ACTIVE	SOIC	D	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 85	285BX M1.2	Samples
LM285BXZ-1.2/LFT4	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type		285BX Z-1.2	Samples
LM285BXZ-1.2/NOPB	ACTIVE	TO-92	LP	3	1800	RoHS & Green	SN	N / A for Pkg Type	-40 to 85	285BX Z-1.2	Samples
LM285BYM-1.2/NOPB	ACTIVE	SOIC	D	8	95	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 85	285BY M1.2	Samples
LM285BYMX-1.2/NOPB	ACTIVE	SOIC	D	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 85	285BY M1.2	Samples
LM285M-1.2/NOPB	ACTIVE	SOIC	D	8	95	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 85	LM285 M1.2	Samples
LM285MX-1.2/NOPB	ACTIVE	SOIC	D	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 85	LM285 M1.2	Samples
LM285Z-1.2/NOPB	ACTIVE	TO-92	LP	3	1800	RoHS & Green	SN	N / A for Pkg Type	-40 to 85	LM285Z-1.2	Samples
LM385BM-1.2/NOPB	ACTIVE	SOIC	D	8	95	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	LM385 BM1.2	Samples
LM385BMX-1.2	NRND	SOIC	D	8	2500	Non-RoHS & Green	Call TI	Call TI	0 to 70	LM385 BM1.2	
LM385BMX-1.2/NOPB	ACTIVE	SOIC	D	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	LM385 BM1.2	Samples
LM385BXM-1.2/NOPB	ACTIVE	SOIC	D	8	95	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	385BX M1.2	Samples
LM385BXMX-1.2/NOPB	ACTIVE	SOIC	D	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	385BX M1.2	Samples
LM385BXZ-1.2/NOPB	ACTIVE	TO-92	LP	3	1800	RoHS & Green	SN	N / A for Pkg Type	0 to 70	385BX	Samples

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
										Z-1.2	
LM385BYM-1.2/NOPB	ACTIVE	SOIC	D	8	95	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	385BY M1.2	Samples
LM385BYMX-1.2/NOPB	ACTIVE	SOIC	D	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	385BY M1.2	Samples
LM385BYZ-1.2/NOPB	ACTIVE	TO-92	LP	3	1800	RoHS & Green	SN	N / A for Pkg Type	0 to 70	385BY Z-1.2	Samples
LM385BZ-1.2/NOPB	ACTIVE	TO-92	LP	3	1800	RoHS & Green	SN	N / A for Pkg Type	0 to 70	LM385 BZ1.2	Samples
LM385M-1.2	NRND	SOIC	D	8	95	Non-RoHS & Green	Call TI	Call TI	0 to 70	LM385 M1.2	
LM385M-1.2/NOPB	ACTIVE	SOIC	D	8	95	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	LM385 M1.2	Samples
LM385M3-1.2	NRND	SOT-23	DBZ	3	1000	Non-RoHS & Green	Call TI	Call TI	0 to 70	R11	
LM385M3-1.2/NOPB	ACTIVE	SOT-23	DBZ	3	1000	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	R11	Samples
LM385M3X-1.2	NRND	SOT-23	DBZ	3	3000	Non-RoHS & Green	Call TI	Call TI	0 to 70	R11	
LM385M3X-1.2/NOPB	ACTIVE	SOT-23	DBZ	3	3000	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	R11	Samples
LM385MX-1.2/NOPB	ACTIVE	SOIC	D	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	0 to 70	LM385 M1.2	Samples
LM385Z-1.2/LFT3	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type		LM385 Z-1.2	Samples
LM385Z-1.2/LFT4	ACTIVE	TO-92	LP	3	2000	RoHS & Green	SN	N / A for Pkg Type		LM385 Z-1.2	Samples
LM385Z-1.2/NOPB	ACTIVE	TO-92	LP	3	1800	RoHS & Green	SN	N / A for Pkg Type	0 to 70	LM385 Z-1.2	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

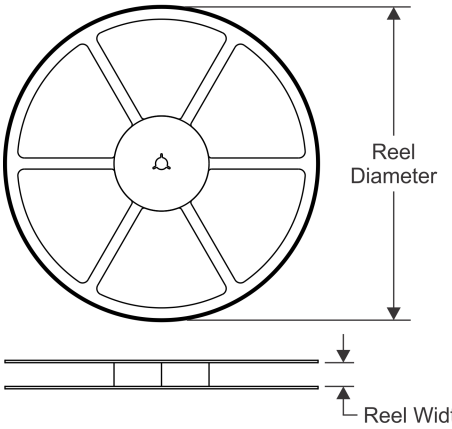
(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

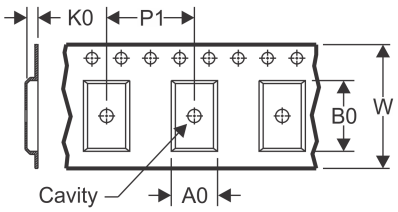
(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

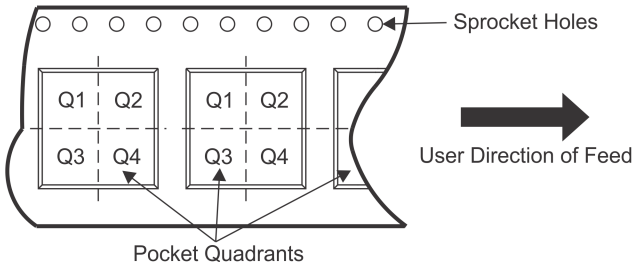


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

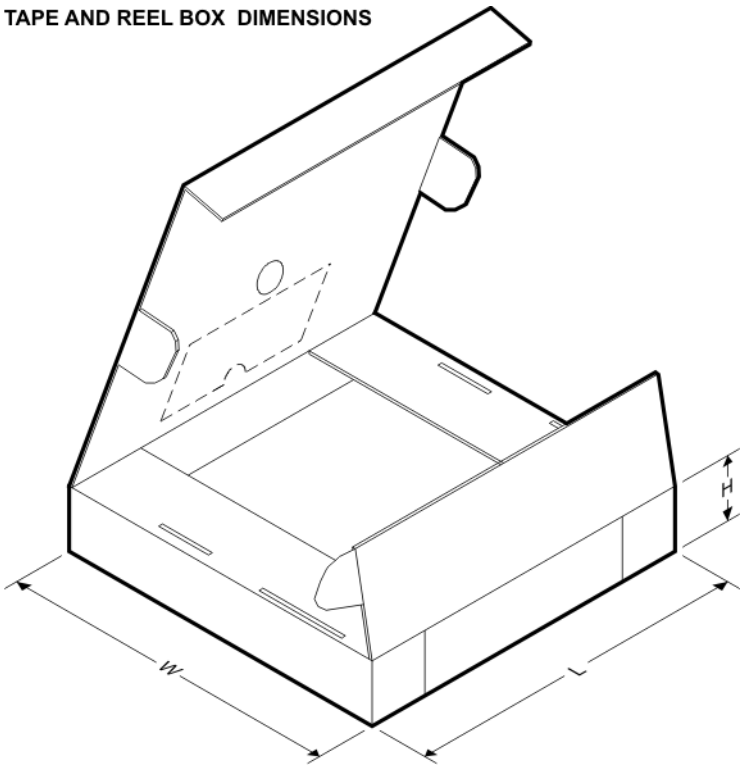
QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

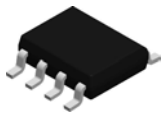
Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM285BXMX-1.2/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM285BYMX-1.2/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM285MX-1.2/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM385BMX-1.2	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM385BMX-1.2/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM385BXMX-1.2/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM385BYMX-1.2/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LM385M3-1.2	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM385M3-1.2/NOPB	SOT-23	DBZ	3	1000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM385M3X-1.2	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM385M3X-1.2/NOPB	SOT-23	DBZ	3	3000	178.0	8.4	3.3	2.9	1.22	4.0	8.0	Q3
LM385MX-1.2/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM285BXMX-1.2/NOPB	SOIC	D	8	2500	367.0	367.0	35.0
LM285BYMX-1.2/NOPB	SOIC	D	8	2500	367.0	367.0	35.0
LM285MX-1.2/NOPB	SOIC	D	8	2500	367.0	367.0	35.0
LM385BMX-1.2	SOIC	D	8	2500	367.0	367.0	35.0
LM385BMX-1.2/NOPB	SOIC	D	8	2500	367.0	367.0	35.0
LM385BXMX-1.2/NOPB	SOIC	D	8	2500	367.0	367.0	35.0
LM385BYMX-1.2/NOPB	SOIC	D	8	2500	367.0	367.0	35.0
LM385M3-1.2	SOT-23	DBZ	3	1000	210.0	185.0	35.0
LM385M3-1.2/NOPB	SOT-23	DBZ	3	1000	210.0	185.0	35.0
LM385M3X-1.2	SOT-23	DBZ	3	3000	210.0	185.0	35.0
LM385M3X-1.2/NOPB	SOT-23	DBZ	3	3000	210.0	185.0	35.0
LM385MX-1.2/NOPB	SOIC	D	8	2500	367.0	367.0	35.0

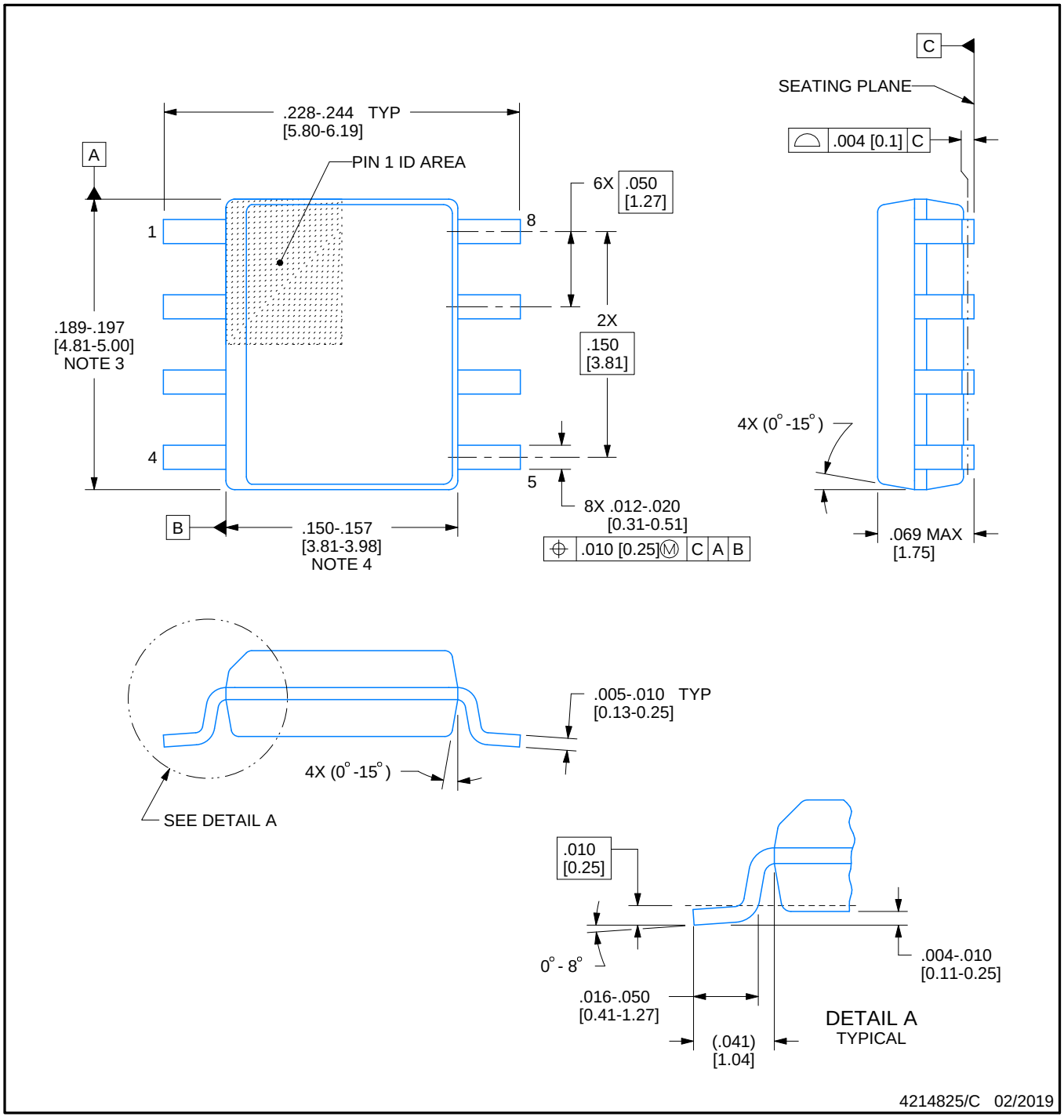


PACKAGE OUTLINE

D0008A

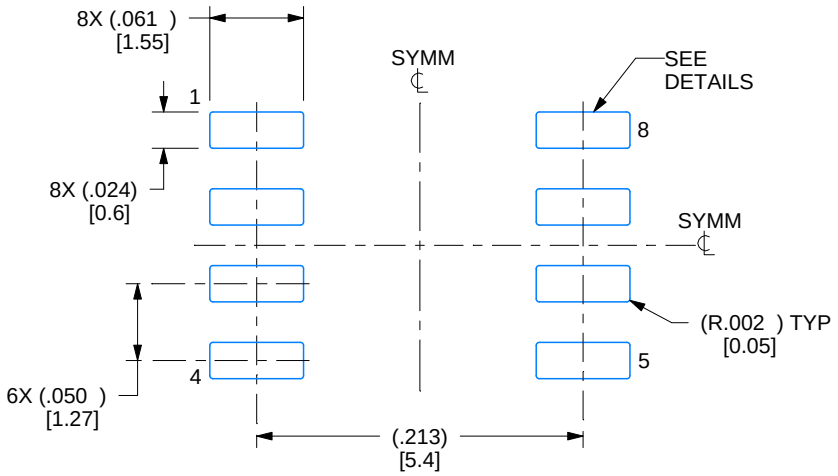
SOIC - 1.75 mm max height

SMALL OUTLINE INTEGRATED CIRCUIT

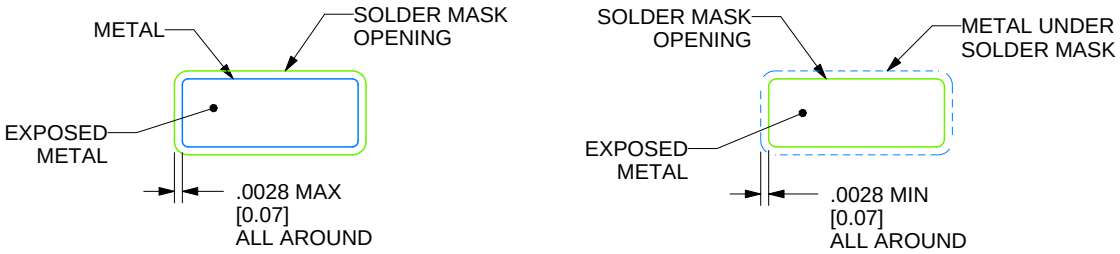


NOTES:

1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 [0.15] per side.
4. This dimension does not include interlead flash.
5. Reference JEDEC registration MS-012, variation AA.



LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE:8X



SOLDER MASK DETAILS

4214825/C 02/2019

NOTES: (continued)

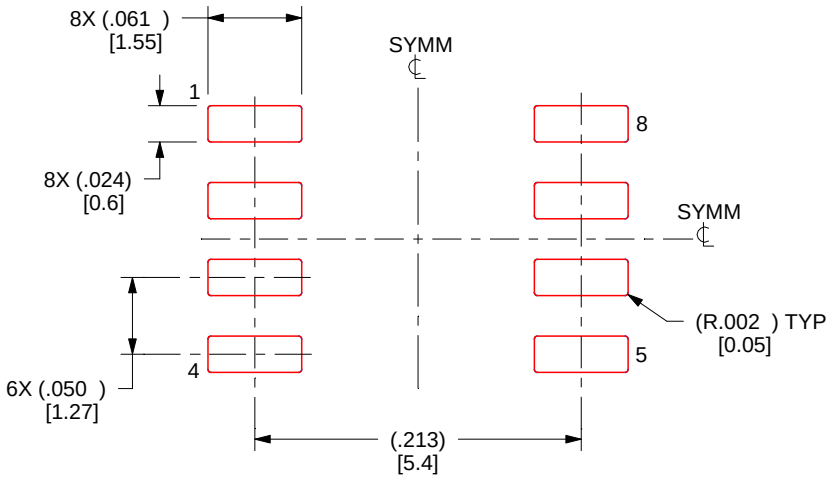
6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

EXAMPLE STENCIL DESIGN

D0008A

SOIC - 1.75 mm max height

SMALL OUTLINE INTEGRATED CIRCUIT



SOLDER PASTE EXAMPLE
BASED ON .005 INCH [0.125 MM] THICK STENCIL
SCALE:8X

4214825/C 02/2019

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.

GENERIC PACKAGE VIEW

LP 3 TO-92 - 5.34 mm max height

TRANSISTOR OUTLINE

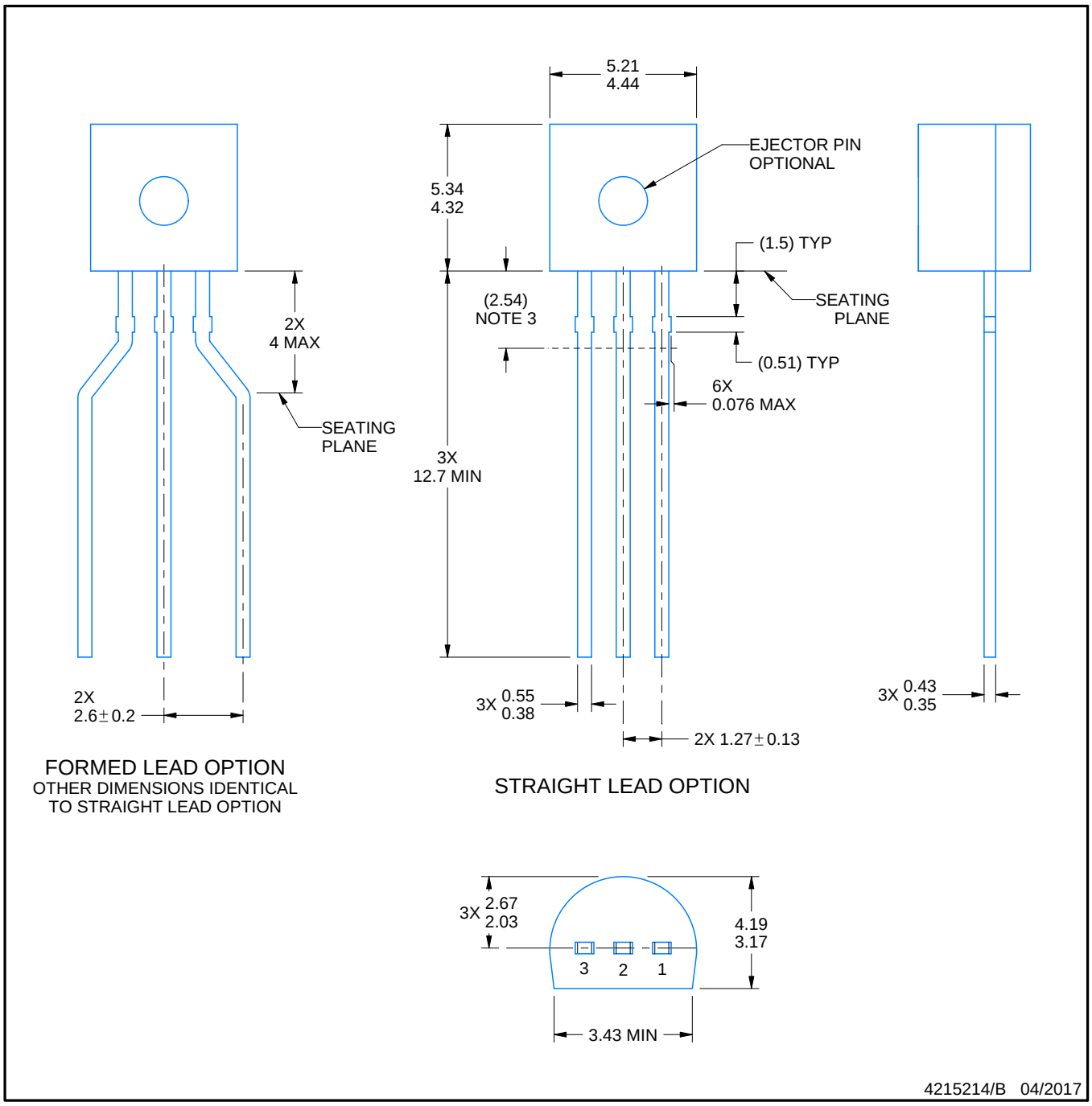
Images above are just a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.

LP0003A

PACKAGE OUTLINE

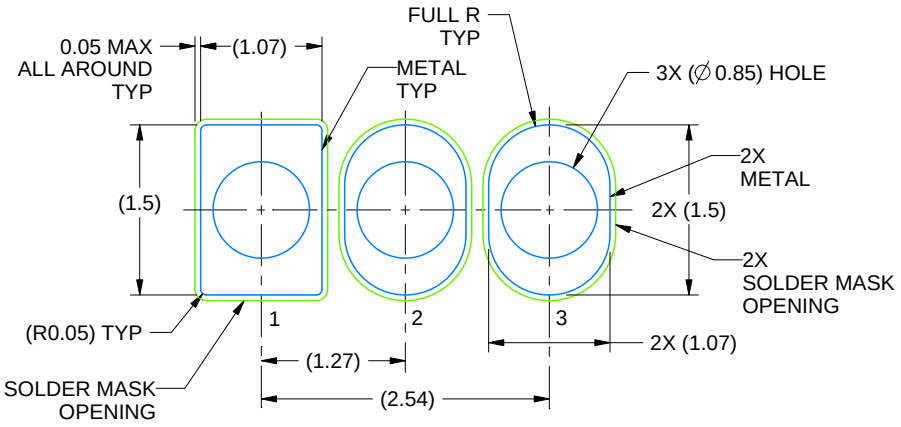
TO-92 - 5.34 mm max height

TO-92

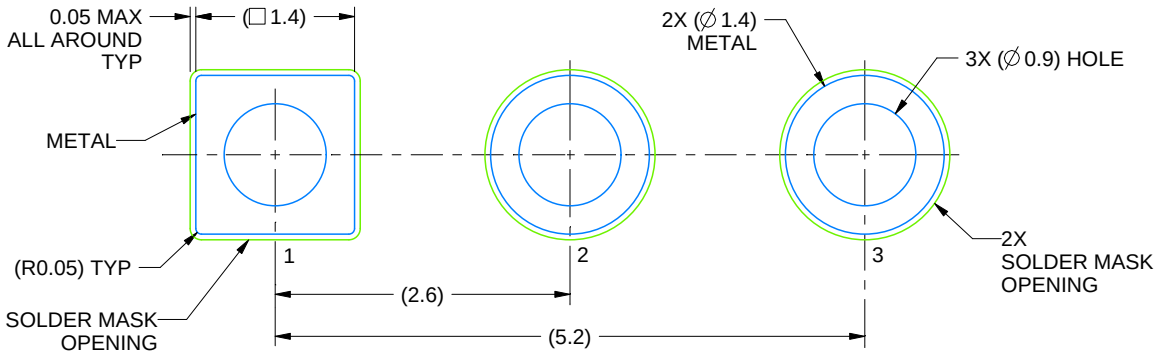


NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Lead dimensions are not controlled within this area.
4. Reference JEDEC TO-226, variation AA.
5. Shipping method:
   a. Straight lead option available in bulk pack only.
   b. Formed lead option available in tape and reel or ammo pack.
   c. Specific products can be offered in limited combinations of shipping medium and lead options.
   d. Consult product folder for more information on available options.



LAND PATTERN EXAMPLE
STRAIGHT LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X



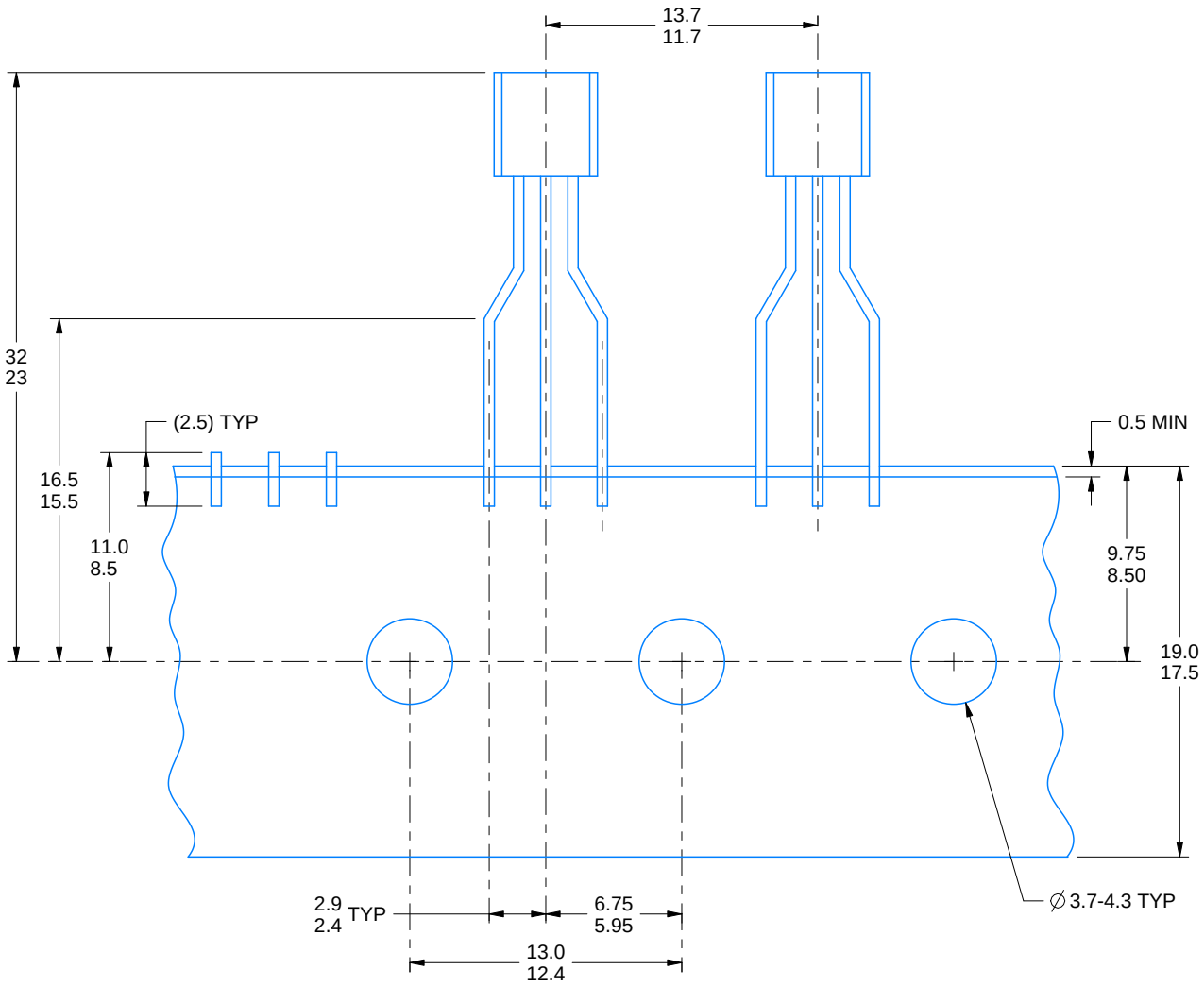
LAND PATTERN EXAMPLE
FORMED LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X

4215214/B 04/2017

LP0003A

TO-92 - 5.34 mm max height

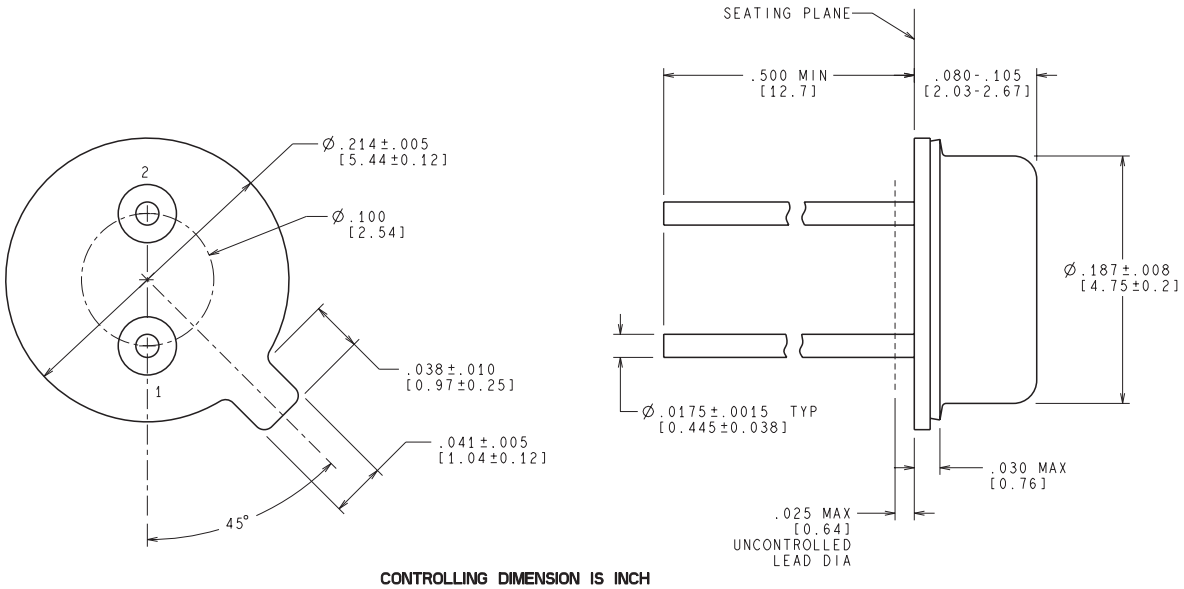
TO-92



FOR FORMED LEAD OPTION PACKAGE

4215214/B 04/2017

NDU0002A



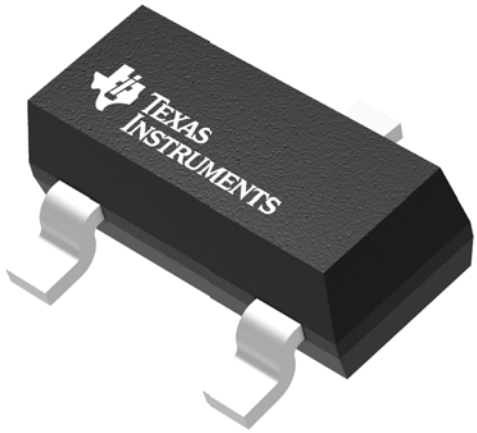
CONTROLLING DIMENSION IS INCH
VALUES IN [ ] ARE MILLIMETERS

H02A (Rev F)

GENERIC PACKAGE VIEW

DBZ 3 SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



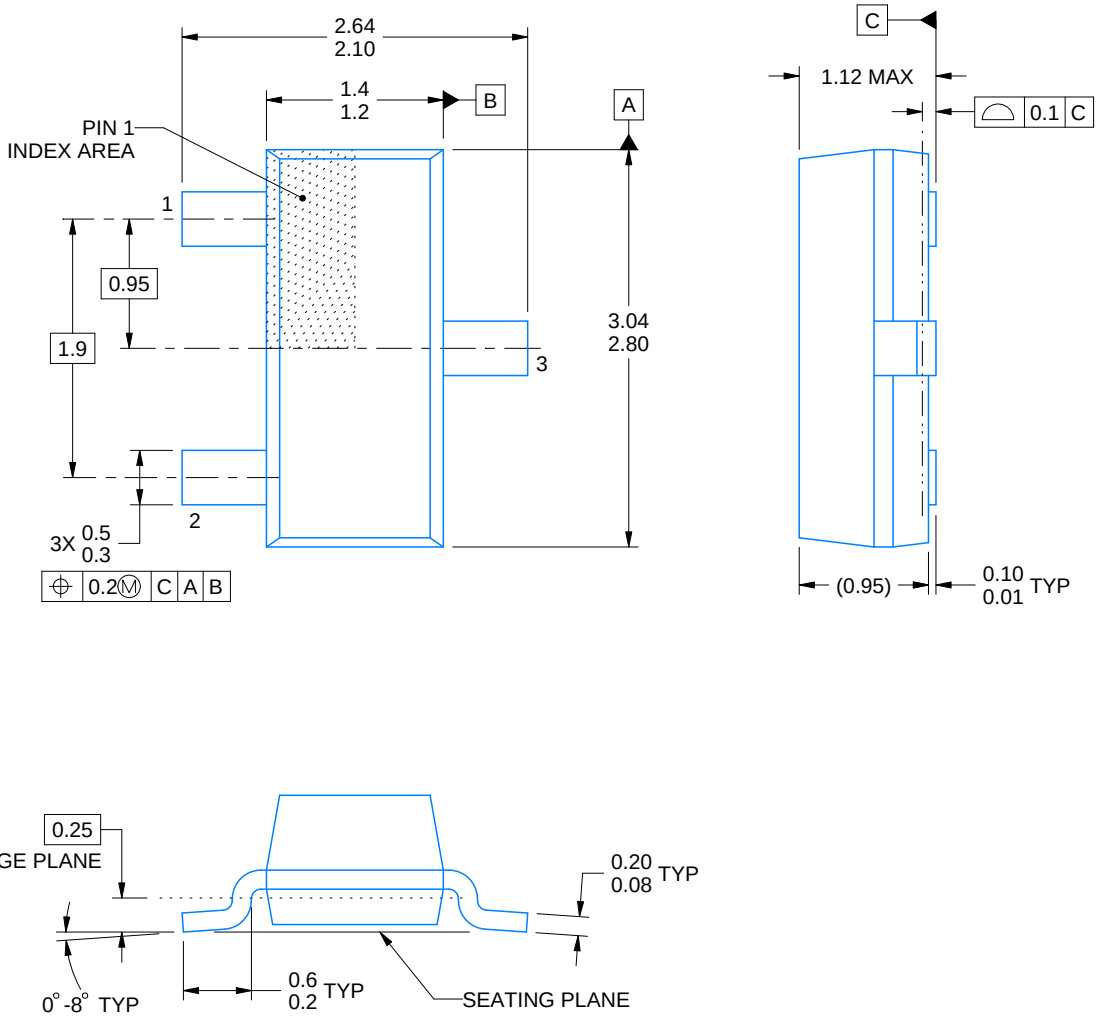
Images above are just a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.

PACKAGE OUTLINE

DBZ0003A

SOT-23 - 1.12 mm max height

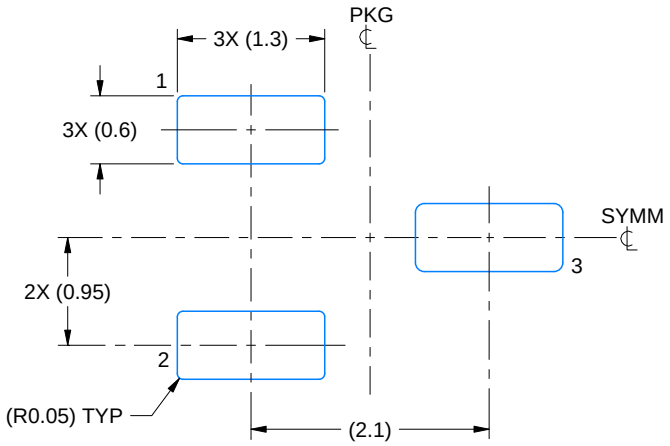
SMALL OUTLINE TRANSISTOR



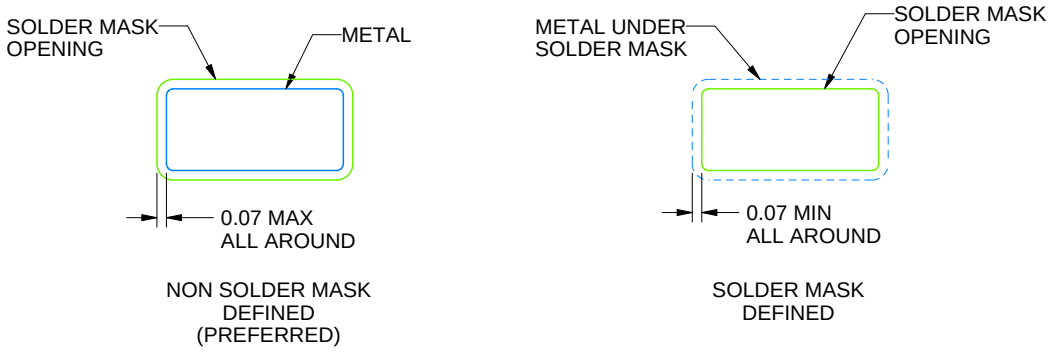
4214838/C 04/2017

NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-236, except minimum foot length.



LAND PATTERN EXAMPLE
SCALE:15X

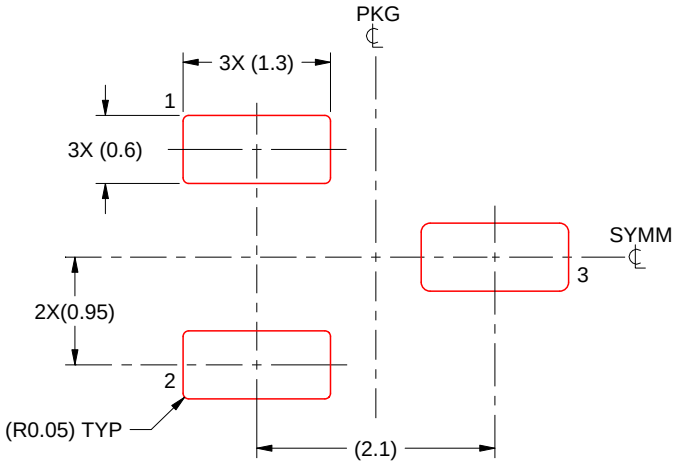


SOLDER MASK DETAILS

4214838/C 04/2017

NOTES: (continued)

4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SOLDER PASTE EXAMPLE
BASED ON 0.125 THICK STENCIL
SCALE:15X

4214838/C 04/2017

NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
7. Board assembly site may have different recommendations for stencil design.

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