

Reference Specification

Leaded MLCC for Automotive with AEC-Q200 RCE Series

Product specifications in this catalog are as of Dec. 2017, and are subject to change or obsolescence without notice.

Please consult the approval sheet before ordering. Please read rating and Cautions first.

⚠ CAUTION

1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for each equipment should be taken into considerations.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage(1)	Pulse Voltage(2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. In case of Class 2 capacitors (Temp.Char. : X7R,X7S,X8L, etc.), applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. Please contact us if self-generated heat is occurred with Class 1 capacitors (Temp.Char. : C0G,U2J,X8G, etc.). When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.

3. Fail-safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.

4. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 °C and 20 to 70%. Use capacitors within 6 months.

5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

7. BONDING AND RESIN MOLDING, RESIN COAT

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of a bonded or molded product in the intended equipment. In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive or molding resin may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

8. TREATMENT AFTER BONDING AND RESIN MOLDING, RESIN COAT

When the outer coating is hot (over 100 °C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

1. Aircraft equipment 2. Aerospace equipment

3. Undersea equipment 4. Power plant control equipment

5. Medical equipment6. Transportation equipment (vehicles, trains, ships, etc.)7. Traffic signal equipment8. Disaster prevention / crime prevention equipment

9. Data-processing equipment exerting influence on public

10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

2. Soldering and Mounting

Insertion of the Lead Wire

- When soldering, insert the lead wire into the PCB without mechanically stressing the lead wire.
- Insert the lead wire into the PCB with a distance appropriate to the lead space.

3. CAPACITANCE CHANGE OF CAPACITORS

Class 2 capacitors (Temp.Char. : X7R,X7S,X8L, etc.)

Class 2 capacitors an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit.

Please contact us if you need a detail information.

⚠ NOTE

- 1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

1. Application

This specification is applied to Leaded MLCC RCE series in accordance with AEC-Q200 requirements used for Automotive Electronic equipment.

2. Rating

• Part number configuration

ex.) RCE	R7	2E	103	K	1	K1	H03	В
Series	Temperature	Rated	Capacitance	Capacitance	Dimension	Lead	Individual	Packing
	Characteristic	voltage		tolerance	code	code	specification code	style code

• Temperature characteristic

Code	Temp. Char.	Temp. Range	Cap. Change (Within%)	Standard Temp.	Operating Temp. Range
R7	X7R	-55 ∼ 125°C	+/-15	25°C	-55 ∼ 125°C

• Rated voltage

Code	Rated voltage
2E	DC250V
2J	DC630V
3A	DC1000V

Capacitance

The first two digits denote significant figures; the last digit denotes the multiplier of 10 in pF. ex.) In case of 103.

$$10 \times 10^3 = 10000 pF$$

Capacitance tolerance

Code	Capacitance Tolerance
K	+/-10%
М	+/-20%

• Dimension code

Code	Dimensions (LxW) mm max.
1	4.0 x 3.5
2	5.5 x 4.0
3	5.5 x 5.0
4	7.5 x 5.5
5	7.5 x 7.5 *
U	7.7 x 12.5 *

^{*}DC630V, DC1000V: W+0.5mm

Lead code

Code	Lead style	Lead spacing (mm)
B1	Straight type	5.0+/-0.8
E1	Straight taping type	5.0+0.6/-0.2
K1	Inside crimp type	5.0+/-0.8
M1	Inside crimp taping type	5.0+0.6/-0.2

Lead wire is solder coated CP wire.

Individual specification code
 Murata's control code
 Please refer to [Part number list].

• Packing style code

Code	Packing style
Α	Taping type of Ammo
В	Bulk type

3. Marking

Temp. char. : Letter code : C (X7R char. Except dimension code : 1)

Capacitance : 3 digit numbers

Capacitance tolerance : Code

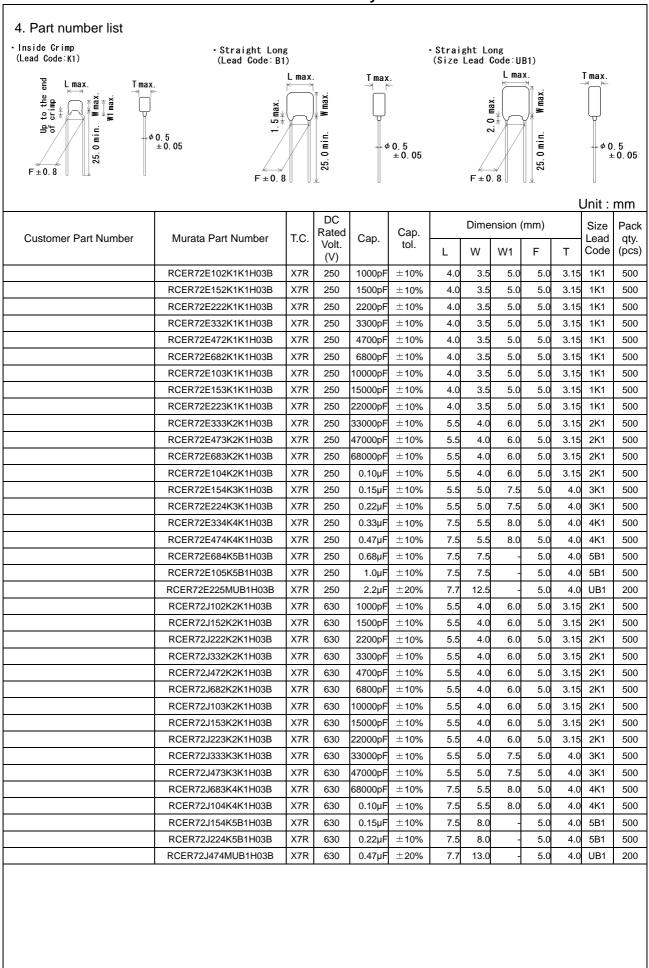
Rated voltage : Letter code : 4 (DC250V only. Except dimension code : 1)

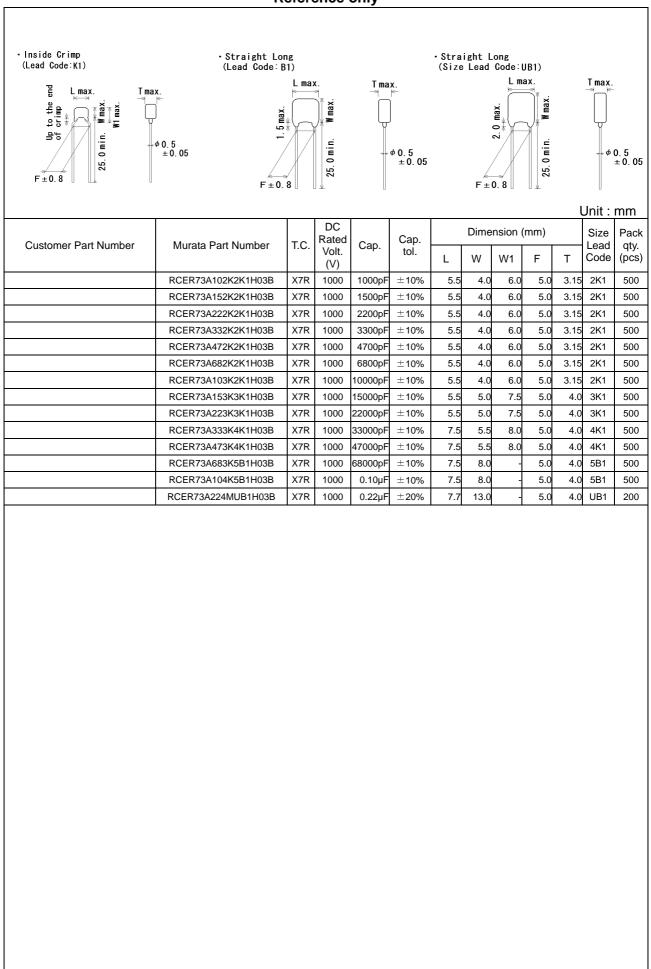
Letter code: 7 (DC630V only.) Letter code: A (DC1000V only.)

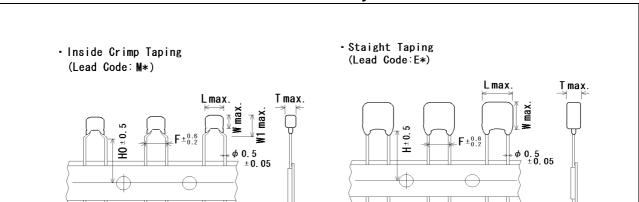
Company name code : Abbreviation : (Except dimension code : 1)

(Ex.)

Ex.)			
Rated voltage Dimensions	DC250V	DC630V	DC1000V
1	103K		
2	6 473	6 4153	€ 102 KAC
3,4	(M224 K4C)	© 104 K7C	(4333 KAC
5,U	474 K4C	(M 474 M7C	(M) 104 KAC

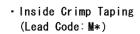


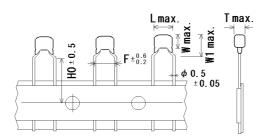




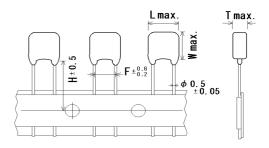
Init	٠	m	m

											L	init : i	<u>mm</u>
Overland Bert Novelon	Museum Deut Nusselan	Τ.Ο	DC Rated	0	0		Di	mensi	on (mr	n)		Size	Pack
Customer Part Number	Murata Part Number	VOIT.	r.C. volt. Cap.	Сар.	Cap. tol.	L	W	W1	F	Т	НО	Lead Code	qty. (pcs)
	RCER72E102K1M1H03A	X7R	250	1000pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E152K1M1H03A	X7R	250	1500pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E222K1M1H03A	X7R	250	2200pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E332K1M1H03A	X7R	250	3300pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E472K1M1H03A	X7R	250	4700pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E682K1M1H03A	X7R	250	6800pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E103K1M1H03A	X7R	250	10000pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E153K1M1H03A	X7R	250	15000pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E223K1M1H03A	X7R	250	22000pF	±10%	4.0	3.5	5.0	5.0	3.15	16.0	1M1	2000
	RCER72E333K2M1H03A	X7R	250	33000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72E473K2M1H03A	X7R	250	47000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72E683K2M1H03A	X7R	250	68000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72E104K2M1H03A	X7R	250	0.10µF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72E154K3M1H03A	X7R	250	0.15µF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RCER72E224K3M1H03A	X7R	250	0.22µF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RCER72E334K4M1H03A	X7R	250	0.33µF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RCER72E474K4M1H03A	X7R	250	0.47µF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RCER72E684K5E1H03A	X7R	250	0.68µF	±10%	7.5	7.5	-	5.0	4.0	17.5	5E1	1500
	RCER72E105K5E1H03A	X7R	250	1.0µF	±10%	7.5	7.5	-	5.0	4.0	17.5	5E1	1500
	RCER72E225MUE1H03A	X7R	250	2.2µF	±20%	7.7	12.5	-	5.0	4.0	17.5	UE1	1500
	RCER72J102K2M1H03A	X7R	630	1000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J152K2M1H03A	X7R	630	1500pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J222K2M1H03A	X7R	630	2200pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J332K2M1H03A	X7R	630	3300pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J472K2M1H03A	X7R	630	4700pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J682K2M1H03A	X7R	630	6800pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J103K2M1H03A	X7R	630	10000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J153K2M1H03A	X7R	630	15000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J223K2M1H03A	X7R	630	22000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER72J333K3M1H03A	X7R	630	33000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RCER72J473K3M1H03A	X7R	630	47000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RCER72J683K4M1H03A	X7R	630	68000pF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RCER72J104K4M1H03A	X7R	630	0.10µF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RCER72J154K5E1H03A	X7R	630	0.15µF	±10%	7.5	8.0	-	5.0	4.0	17.5	5E1	1500
	RCER72J224K5E1H03A	X7R	630	0.22µF	±10%	7.5	8.0	-	5.0	4.0	17.5	5E1	1500
	RCER72J474MUE1H03A	X7R	630	0.47µF	±20%	7.7	13.0	-	5.0	4.0	17.5	UE1	1500





Staight Taping (Lead Code:E*)



Unit: mm

Customer Part Number	Murata Part Number	T.C.	DC Rated	Con	Con tol		Di	mensi	on (mr	n)		Size	Pack
Customer Part Number	Murata Part Number	1.0.	volt. (V)	Cap.	Cap. tol.	L	W	W1	F	Т	НО	Lead Code	qty. (pcs)
	RCER73A102K2M1H03A	X7R	1000	1000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER73A152K2M1H03A	X7R	1000	1500pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER73A222K2M1H03A	X7R	1000	2200pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER73A332K2M1H03A	X7R	1000	3300pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER73A472K2M1H03A	X7R	1000	4700pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER73A682K2M1H03A	X7R	1000	6800pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER73A103K2M1H03A	X7R	1000	10000pF	±10%	5.5	4.0	6.0	5.0	3.15	16.0	2M1	2000
	RCER73A153K3M1H03A	X7R	1000	15000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RCER73A223K3M1H03A	X7R	1000	22000pF	±10%	5.5	5.0	7.5	5.0	4.0	16.0	3M1	2000
	RCER73A333K4M1H03A	X7R	1000	33000pF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RCER73A473K4M1H03A	X7R	1000	47000pF	±10%	7.5	5.5	8.0	5.0	4.0	16.0	4M1	1500
	RCER73A683K5E1H03A	X7R	1000	68000pF	±10%	7.5	8.0	-	5.0	4.0	17.5	5E1	1500
	RCER73A104K5E1H03A	X7R	1000	0.10µF	±10%	7.5	8.0	-	5.0	4.0	17.5	5E1	1500
	RCER73A224MUE1H03A	X7R	1000	0.22µF	±20%	7.7	13.0	-	5.0	4.0	17.5	UE1	1500

).	AEC-Q200 Test Item		Specification	AEC-Q200 Test Method				
Pre-and Post-Stress			-					
!	Electrical Tes High Temperature Exposure (Storage)	Appearance	No defects or abnormalities within ±12.5% 0.04 max.	Sit the capacitor for 1,000±12h at 150±3°C. Let sit for 24±2h *room condition then measure. •Pretreatment Perform the heat treatment at 150+0/-10°C for 60±5 min and				
3	Temperature	I.R. Appearance	More than 1,000M Ω or 50 M Ω · μ F (Whichever is smaller) No defects or abnormalities	then let sit for 24±2 h at *room condition. Perform the 1,000 cycles according to the four heat treatment				
	Cycling	Capacitance Change	within ±12.5%	listed in the following table. Let sit for 24±2 h at *room condit then measure.				
		D.F. I.R.	0.05 max. 1,000M Ω or 50M Ω ·μF min.	Step 1 2 3 4 Temp. 55.0/2 Room 425.3/0 Room				
			(Whichever is smaller)	(°C) -55+0/-3 ROUTH Temp. 125+3/-0 ROUTH Temp. Time (min.) 15±3 1 15±3 1				
then let sit for 24±2 h a			 Pretreatment Perform the heat treatment at 150+0/-10°C for 60±5 min and then let sit for 24±2 h at *room condition. Apply the 24h heat (25 to 65°C) and humidity (80 to 98%) 					
	Resistance	11	within ±12.5%	treatment shown below, 10 consecutive times. Let sit for 24±2 h at *room condition, then measure.				
		D.F.	0.05 max. 500MΩ or 25MΩ·μF min. (Whichever is smaller)	Temperature Humidity 90-98% 90-98% Humidity 90-98%				
	Biased Humidity	Capacitance Change	No defects or abnormalities within ±12.5%	Perform the heat treatment at 150+0/-10°C for 60±5 min and then let sit for 24±2 h at *room condition. Apply the rated voltage and DC1.3+0.2/-0 V (add 100kΩ resi at 85±3°C and 80 to 85% humidity for 1,000±12h. Remove and let sit for 24±2 h at *room condition, then meas				
		D.F. I.R.	0.05 max. 500MΩ or 25MΩ·μF min. (Whichever is smaller)	The charge/discharge current is less than 50mA. •Pretreatment Perform the heat treatment at 150+0/-10°C for 60±5 min and then let sit for 24±2 h at *room condition.				
	Operational Life	Capacitance Change D.F.	No defects or abnormalities within ±12.5% 0.04 max.	Apply voltage in Table for 1,000±12h at 125±3°C. Let sit for 24±2 h at *room condition, then measure. The charge/discharge current is less than 50mA. •Pretreatment Apply test voltage for 60±5 min at test temperature.				
		I.R.	1,000M Ω or 50M Ω ·μF min. (Whichever is smaller)	Remove and let sit for 24±2 h at *room condition. Rated Voltage Test Voltage				
				DC250V 150% of the rated voltage				
				DC630V 120% of the rated voltage DC1000V 110% of the rated voltage				
_			No defects or abnormalities	Visual inspection				
_	Physical Dimension Marking		Within the specified dimensions To be easily legible.	Using calipers and micrometers. Visual inspection				

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INO		Q200	Specification	AEC-Q200 Test Method				
		Item	·	D 1411 OTD 20014 # 1045				
10	Resistance to Solvents	Appearance Capacitance	No defects or abnormalities Within the specified tolerance	Per MIL-STD-202 Method 215 Solvent 1 : 1 part (by volume) of isopropyl alcohol				
	Corverns	D.F.	0.025 max.	3 parts (by volume) of mineral spirits				
		I.R.	More than 10,000MΩ or 500 MΩ·μF	Solvent 2 : Terpene defluxer Solvent 3 : 42 parts (by volume) of water 1part (by volume) of propylene glycol				
			(Whichever is smaller)					
				monomethyl ether				
				1 part (by volume) of monoethanolamine				
11	Mechanical	Appearance	No defects or abnormalities	Three shocks in each direction should be applied along 3				
	Shock	Capacitance	Within the specified tolerance	mutually perpendicular axes of the test specimen (18 shocks). The specified test pulse should be Half-sine and should have a				
		D.F.	0.025 max.	duration :0.5ms, peak value:1,500G and velocity change: 4.7m				
12	Vibration	Appearance	No defects or abnormalities	The capacitor should be subjected to a simple harmonic motion				
		Capacitance	Within the specified tolerance	having a total amplitude of 1.5mm, the frequency being varied				
		D.F.	0.025 max.	uniformly between the approximate limits of 10 and 2,000Hz. The frequency range, from 10 to 2,000Hz and return to 10Hz, should be traversed in approximately 20 min. This motion should be applied for 12 items in each 3 mutually perpendicula				
				directions (total of 36 times).				
13-1	Resistance	Appearance	No defects or abnormalities	The lead wires should be immersed in the melted solder 1.5 to				
	to Soldering	Capacitance	Within ±7.5%	2.0mm from the root of terminal at 260±5°C for 10±1 seconds				
	Heat	Change						
	(Non- Preheat)	Dielectric	No defects	Pre-treatment Capacitor should be stored at 150+0/-10°C for one				
	Fielleat)	Strength (Between		hour, then place at *room condition for 24±2 hours before initi				
		terminals)		measurement.				
		,		Post-treatment Capacitor should be stored for 24±2 hours at *room condition				
10.0	5		N. I.C. I. III					
13-2	Resistance to Soldering	Appearance	No defects or abnormalities	First the capacitor should be stored at 120+0/-5°C for 60+0/-5 seconds.				
	Heat	Capacitance	Within ±7.5%	Then, the lead wires should be immersed in the melted solder				
	(On- Preheat)	Change		1.5 to 2.0mm from the root of terminal at 260±5°C for 7.5+0/-				
		Dielectric	No defects	seconds.				
		Strength (Between		Pre-treatment				
		terminals)		Capacitor should be stored at 150+0/-10°C for one				
		,		hour, then place at *room condition for 24±2 hours before initial				
				measurement.				
				Post-treatment				
13-3	Resistance	Appearance	No defects or abnormalities	Capacitor should be stored for 24±2 hours at *room condition Test condition				
10-0	to Soldering	Capacitance	Within ±7.5%	Termperature of iron-tip: 350±10°C Soldering time: 3.5±0.5 seconds				
	Heat	Change	Within ±7.5%					
	(soldering	Dielectric	No defects	Soldering position				
	iron method)	Strength		Straight Lead:1.5 to 2.0mm from the root of terminal.				
		(Between		Crimp Lead:1.5 to 2.0mm from the end of lead bend.				
		terminals)		Pre-treatment				
				Capacitor should be stored at 150+0/-10°C for one				
				hour, then place at *room condition for 24±2 hours before initial				
				measurement.				
				 Post-treatment Capacitor should be stored for 24±2 hours at *room condition 				
14	Thermal Shock	Appearance	No defects or abnormalities	Perform the 300 cycles according to the two heat treatments list				
		Capacitance	within ±12.5%	in the following table(Maximum transfer time is 20s.). Let sit fo				
		Change		24±2 h at *room condition, then measure.				
		D.F.	0.05 max.	Step 1 2				
		I.R.	1,000M Ω or 50M Ω ·μF min.	Temp55+0/-3 125+3/-0				
			(Whichever is smaller)	(°C) 3310/3 12313/3				
				(min.) 15±3 15±3				
				•Pretreatment				
				Perform the heat treatment at 150+0/-10°C for 60±5 min and				
15	ESD	Appearance	No defects or abnormalities	then let sit for 24±2 h at *room condition.				
15	ESD	Appearance Capacitance	Within the specified tolerance	Per AEC-Q200-002				
		D.F.	0.025 max.					
		I.R.	More than 10,000MΩ or 100 MΩ·μF	 				
		1.17.	INDIE triari 10,000 Nis 2 Or 100 Nis 2 tri					

				Reference onl	<u>y</u>					
No.	AEC-Q200 Test Item Specifications			AEC-Q200 Test Method						
16		Lead wire should be soldered with uniform coating on the axial direction over 95% of the circumferential direction.			The terminal of capacitor is dipped into a solution of ethanol (JIS K 8101) and rosin (JIS K 5902) (25% rosin in weight propotion).Immerse in solder solution for 2±0.5 seconds. In both cases the depth of dipping is up to about 1.5 to 2mm fror the terminal body. Temp. of solder: 245±5°C Lead Free Solder(Sn-3.0Ag-0.5Cu) 235±5°C H60A or H63A Eutectic Solder					
17	Electrical Characte-	Apperance Capacitance			Visual inspection. The capacitance/D.F. should be measured at 25°C at the					
	rization	D.F.	·			and voltage sh			at the	
						Frequenc	су	Voltage		
						1±0.1kH	z	1±0.2V(rms)		
		I.R.	Between Terminals	10,000MΩ or 100MΩ·μF min. (Whichever is smaller)		/ in case of rated		d be measured with age : DC250V) at 25		
		Dielectric Strength	Between Terminals	No defects or abnormalities	The capa applied b	citor should not	inatio	maged when voltagens for 1 to 5 seconds		e is
						Rated Voltage		Test Voltage		
						DC250V	20	00% of the rated vo	ltage	
						DC630V	15	50% of the rated vo	ltage	
						DC1000V	12	20% of the rated vo	ltage	
			Body Insulation	No defects or abnormalities	diameter 2mm from in case of 5 second	The capacitor is placed in a container with metal balls of 1 diameter so that each terminal, short-circuit is kept approximate from the balls, and 200% of the rated DC voltage(Doin case of rated voltage: DC630V,DC1000V) is impressed seconds between capacitor terminals and metal balls. (Charge/Discharge current ≤ 50mA.)		approxi age(DC ressed f	matel 1300\	
18	Terminal Strength	Tensile Strength	Termination	Termination not to be broken or loosened		figure, fix the ca	pacito directi	or body, apply the for ion of the capacitor upplied for 10±1 second	until rea	
						↓ ↓ F				
		Bending Strength	Termination	Termination not to be broken or loosened		Each lead wire should be subjected to a force of 2.5N and then be bent 90° at the point of egress in one direction. Each wire is then returned to the original position and bent 90° in the opposit direction at the rate of one bend per 2 to 3 seconds.				
19	Capacitano Temperatu		Within ±15%			citance change cified temperatu		d be measured after	5min. a	t
	Characteri				each spe	Step		nperature(°C)		
						1	1011	25±2		
						2		-55±3		
						3		25±2		
						5		125±3 25±2		
					25°C valushould be Pretreati Perform then let s	es of capacitance within the speciment	eratur ified rant nt at 1 room	nge compared with tre ranges shown in tanges. 150+0/-10°C for 60±4 condition.	he table	•
* "roon	n condition"	Temperature:	:15 to 35°C, R	elative humidity:45 to 75%, Atmosphere p	ressure:86	to 106kPa				

6. Packing specification

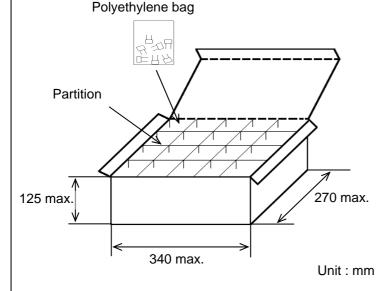
•Bulk type (Packing style code : B)

The size of packing case and packing way

The number of packing = *1 Packing quantity *2 n

*1 : Please refer to [Part number list].

*2 : Standard n = 20 (bag)

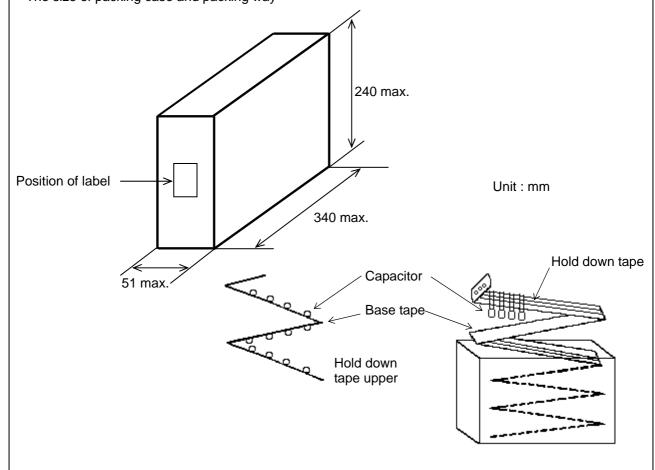


Note)

The outer package and the number of outer packing be changed by the order getting amount.

- •Ammo pack taping type (Packing style code : A)
 - \cdot A crease is made every 25 pitches, and the tape with capacitors is packed zigzag into a case.
 - · When body of the capacitor is piled on other body under it.

The size of packing case and packing way



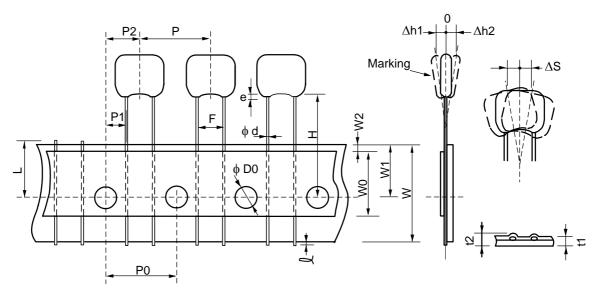
EKBCRPE01

7. Taping specification

7-1. Dimension of capacitors on tape

Straight taping type < Lead code : E1 >

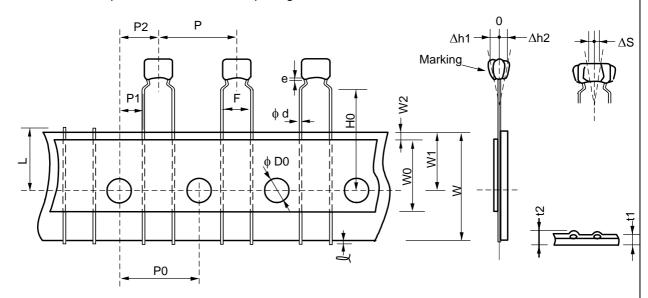
Pitch of component 12.7mm / Lead spacing 5.0mm



Unit : mm

Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7+/-1.0	
Pitch of sprocket hole	P0	12.7+/-0.2	
Lead spacing	F	5.0+0.6/-0.2	
Length from hole center to component center	P2	6.35+/-1.3	Deviation of programs discation
Length from hole center to lead	P1	3.85+/-0.7	Deviation of progress direction
Deviation along tape, left or right defect	ΔS	0+/-2.0	They include deviation by lead bend .
Carrier tape width	W	18.0+/-0.5	
Position of sprocket hole	W1	9.0+0/-0.5	Deviation of tape width direction
For straight lead type	Н	17.5+/-0.5	
Protrusion length	l	0.5 max.	
Diameter of sprocket hole	D0	4.0+/-0.1	
Lead diameter	φd	0.50+/-0.05	
Total tape thickness	t1	0.6+/-0.3	
Total thickness of tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
	∆h1	2.0 max. (Dimension code : U)	
Deviation across tape	∆h2	1.0 max. (exce	pt as above)
Portion to cut in case of defect	L	11.0+0/-1.0	
Hold down tape width	W0	9.5 min.	
Hold down tape position	W2	1.5+/-1.5	
Coating extension on lead	е	2.0 max. (Dimension code : U) 1.5 max. (except as above)	

Inside crimp taping type < Lead code : M1 > Pitch of component 12.7mm / Lead spacing 5.0mm

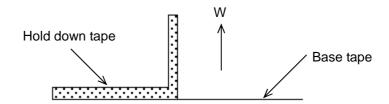


Unit: mm

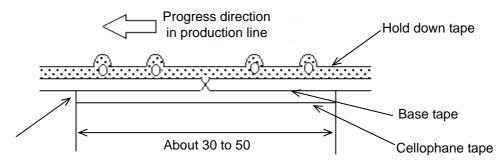
Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7+/-1.0	
Pitch of sprocket hole	P0	12.7+/-0.2	
Lead spacing	F	5.0+0.6/-0.2	
Length from hole center to component center		6.35+/-1.3	Deviation of management discording
Length from hole center to lead	P1	3.85+/-0.7	Deviation of progress direction
Deviation along tape, left or right defect	ΔS	0+/-2.0	They include deviation by lead bend .
Carrier tape width	W	18.0+/-0.5	
Position of sprocket hole	W1	9.0+0/-0.5	Deviation of tape width direction
Lead distance between reference and bottom plane	НО	16.0+/-0.5	
Protrusion length	l	0.5 max.	
Diameter of sprocket hole	D0	4.0+/-0.1	
Lead diameter	φd	0.50+/-0.05	
Total tape thickness	t1	0.6+/-0.3	
Total thickness of tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
	∆h1	2.0 max. (Dimension code : W)	
Deviation across tape	∆h2	1.0 max. (except as above)	
Portion to cut in case of defect	L	11.0+0/-1.0	
Hold down tape width	W0	9.5 min.	
Hold down tape position	W2	1.5+/-1.5	
Coating extension on lead	е	Up to the end of o	rimp

7-2. Splicing way of tape

1) Adhesive force of tape is over 3N at test condition as below.



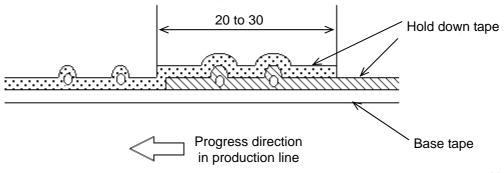
- 2) Splicing of tape
 - a) When base tape is spliced
 - •Base tape shall be spliced by cellophane tape. (Total tape thickness shall be less than 1.05mm.)



No lifting for the direction of progressing

Unit: mm

- b) When hold down tape is spliced
 - •Hold down tape shall be spliced with overlapping. (Total tape thickness shall be less than 1.05mm.)



Unit: mm

- c) When both tape are spliced
 - •Base tape and hold down tape shall be spliced with splicing tape.

EU RoHS and Halogen Free

This products of the following crresponds to EU RoHS and Halogen Free

(1) RoHS

EU RoHs 2011/65/EC compliance

maximum concentration values tolerated by weight in homogeneous materials

- •1000 ppm maximum Lead
- •1000 ppm maximum Mercury
- •100 ppm maximum Cadmium
- •1000 ppm maximum Hexavalent chromium
- •1000 ppm maximum Polybrominated biphenyls (PBB)
- •1000 ppm maximum Polybrominated diphenyl ethers (PBDE)

(2) Halogen-Free

The International Electrochemical Commission's (IEC) Definition of Halogen-Free (IEC 61249-2-21) compliance

- •900 ppm maximum chlorine
- •900 ppm maximum bromine
- •1500 ppm maximum total chlorine and bromine