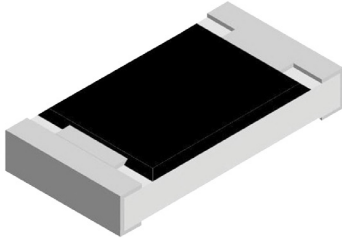


# Thick Film Low Ohm Chip Resistors

**RoHS  
Compliant**



## Description:

The resistors are constructed in a high grade ceramic body (aluminium oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Lead-tin alloy.

## Features:

- High Power Rating and Compact Size
- High Reliability and Stability
- Lead Free and Halogen Free

## Application:

- Power Supply
- PDA Digital Meter Computer
- Automotive Battery Charger
- DC-DC Power Converter

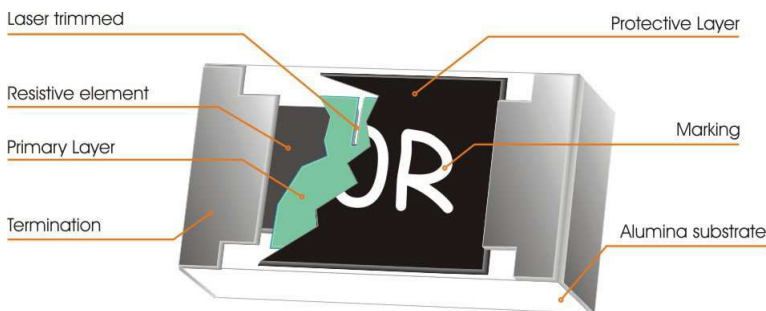


Fig 1. Construction of 2512, 2010 Chip-R

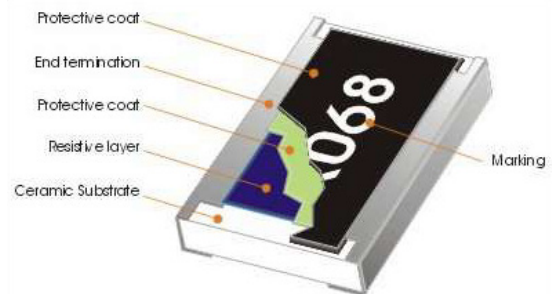


Fig 2. Construction of a 1218 Chip-R

## Quick Reference Data:

Item	General Specification					
	MCWW25X		MCWW20X		MCWW18X	
Part No.	MCWW25X		MCWW20X		MCWW18X	
Size Code	2512 ( 6432 )		2010(5025)		1218(3248)	
Resistance Tolerance	±5% (E24)	±1%	±5% (E24)	±1%	±5% (E24)	±1%
Resistance Range	0.01Ω to 0.91Ω	0.01Ω to 0.976Ω	0.01Ω to 0.91Ω	0.01Ω to 0.976Ω	0.02Ω to 0.91Ω	0.02Ω to 0.976Ω
TCR (ppm/°C)						
0.01Ω ≤ Rn < 0.05Ω	≤ 2100 ppm/°C			≤ 1500 ppm/°C		
0.05Ω ≤ Rn < 0.1Ω	≤ 1000 ppm/°C			≤ 1000 ppm/°C		
0.1Ω ≤ Rn < 0.5Ω	≤ 500 ppm/°C			≤ 500 ppm/°C		
0.5Ω ≤ Rn < 1Ω	≤ 400 ppm/°C			≤ 300 ppm/°C		

# Thick Film Low Ohm Chip Resistors

Item	General Specification		
Max.dissipation at $T_{amb} = 70^{\circ}C$	1 W	0.5 W	1 W
Climatic Category (IEC 60068)	55/155/56	55/155/56	55/155/56

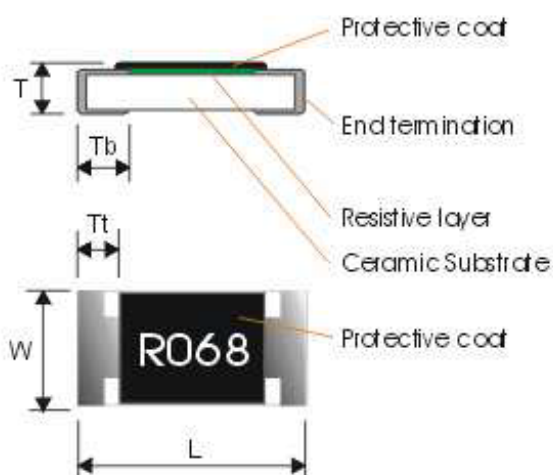
## Note :

1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}} \text{ or Max. RCWV listed above, whichever is lower.}$$

3. Runner items are included in product range defined as 0.025, 0.04, 0.05, 0.06, 0.07, 0.08, 0.09ohm

## Mechanical Data(unit : mm)



Size	2512	2010	1218
L	6.4 ±0.2	5 ±0.2	3.05 ±0.15
W	3.2 ±0.2	2.5 ±0.2	4.6 ±0.2
T	0.6 ±0.1	0.55 ±0.1	0.55 ±0.1
Tt	0.65 ±0.25	0.65 ±0.25	0.45 ±0.25
Tb	0.9 ±0.25	0.6 ±0.25	0.5 ±0.25

## Marking

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value.

### Example:

R010 = 0.01Ω

R510 = 0.51Ω

## Functional Description

### Product characterization

Standard values of nominal resistance are taken from the E96 & E24 series for resistors with a tolerance of 5% & 1%. The values of the E24/E96 series are in accordance with "IEC publication 60063".

## Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.3

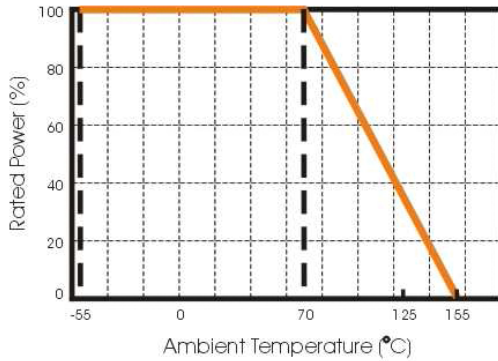


Fig 3. Max. dissipation in percentage of rated power as a function of the ambient temperature

## Mounting

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

## Soldering Condition

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 4.

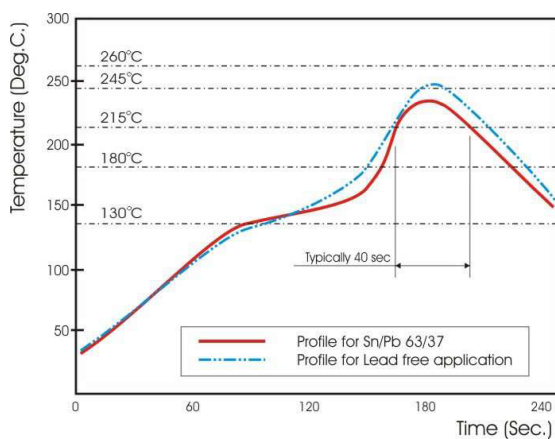


Fig 4. Infrared soldering profile for Chip Resistors

# Thick Film Low Ohm Chip Resistors

## Test and Requirements (JIS C 5201-1 : 1998)

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category **LCT/UCT/56**(rated temperature range : Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, sub clause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

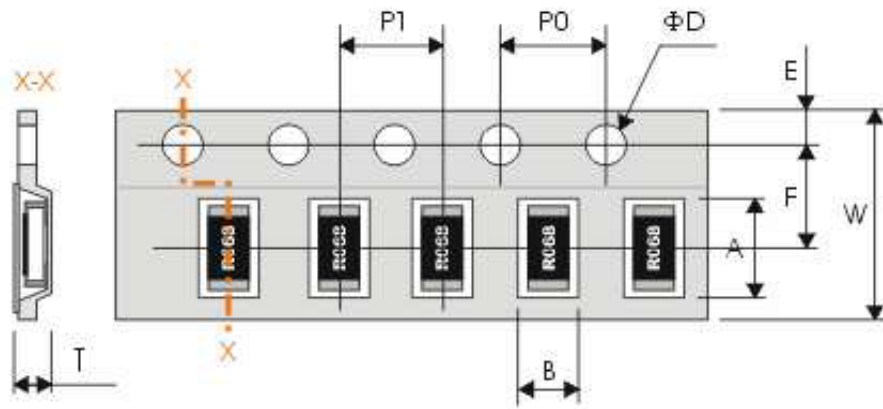
All soldering tests are performed with mildly activated flux.

TEST	PROCEDURE	REQUIREMENT
Temperature Coefficient of Resistance(T.C.R) <b>Clause 4.8</b>	Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_1 - t_2)} \times 10^6(\text{ppm}/^\circ\text{C})$ $t_1 : 20^\circ\text{C} +5^\circ\text{C} -1^\circ\text{C}$ $R_1$ : Resistance at reference temperature $R_2$ : Resistance at test temperature	Refer to "QUICK REFERENCE DATA"
Short time overload (S.T.O.L) <b>Clause 4.13</b>	Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	$\Delta R/R$ max. $\pm(2\%+0.005\Omega)$
Resistance to soldering heat(R.S.H) <b>Clause 4.18</b>	Un-mounted chips completely immersed for $10 \pm 1$ second in a SAC solder bath at $260^\circ\text{C} \pm 5^\circ\text{C}$	no visible damage $\Delta R/R$ max. $\pm(1\%+0.005\Omega)$
Solderability <b>Clause 4.17</b>	Un-mounted chips completely immersed for $2 \pm 0.5$ second in a SAC solder bath at $235^\circ\text{C} \pm 5^\circ\text{C}$	good tinning (>95% covered) no visible damage
Temperature cycling <b>Clause 4.19</b>	30 minutes at $-55^\circ\text{C} \pm 3^\circ\text{C}$ , 2~3 minutes at $20^\circ\text{C} +5^\circ\text{C} -1^\circ\text{C}$ , 30 minutes at $+155^\circ\text{C} \pm 3^\circ\text{C}$ , 2~3 minutes at $20^\circ\text{C} +5^\circ\text{C} -1^\circ\text{C}$ , total 5 continuous cycles	no visible damage $\Delta R/R$ max. $\pm(1\%+0.005\Omega)$
Load life (endurance) <b>Clause 4.25</b>	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller $70 \pm 2^\circ\text{C}$ , 1.5 hours on and 0.5 hours off	$\Delta R/R$ max. $\pm(3\%+0.005\Omega)$
Load life in Humidity <b>Clause 4.24</b>	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at $40^\circ\text{C} \pm 2^\circ\text{C}$ and 90~95% relative humidity, 1.5 hours on and 0.5 hours off	$\Delta R/R$ max. $\pm(3\%+0.005\Omega)$
Bending strength <b>Clause 4.33</b>	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 2 mm, once for 10 seconds	$\Delta R/R$ max. $\pm(1\%+0.005\Omega)$
Adhesion <b>Clause 4.32</b>	Pressurizing force: 5N, Test time: $10 \pm 1$ sec.	No remarkable damage or removal of the terminations
Insulation Resistance <b>Clause 4.6</b>	Apply the maximum overload voltage (DC) for 1minute	$R \geq 10G\Omega$
Dielectric Withstand Voltage <b>Clause 4.7</b>	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover

# Thick Film Low Ohm Chip Resistors

## Packaging

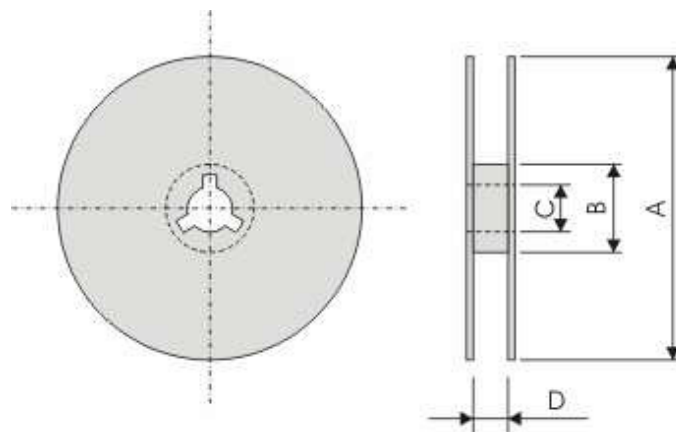
Plastic Tape specifications (unit :mm)



Symbol	B	W	F	E
2512	3.6 ±0.2	12 ±0.3	5.5 ±0.2	1.75 ±0.1
2010	2.8 ±0.2			
1218	3.55 ±0.3			

Symbol	P1	P0	D	T
2512	4 ±0.1	4 ±0.1	Φ1.5 <sup>+0.1</sup> <sub>-0.0</sub>	Max. 1.2
2010				Max. 1.2
1218	8 ±0.1	4 ±0.1		1.3 ±0.2

## Reel dimensions



(Unit :mm)	A	B	C	D
7" reel	Φ178 ±2	Φ60 ±1	13 ±0.2	14 ±0.2
10" reel	Φ254 ±2	Φ100 ±1	13 ±0.2	14 ±0.2

# Thick Film Low Ohm Chip Resistors

## Taping quantity

- MCWW20, MCWW25 by plastic tape taping 4,000 pcs per 7" reel/8,000pcs per 10" reel!
- MCWW18 by plastic tape taping 3,000 pcs per 10" reel

## Ordering Information

The resistors have a catalogue number starting with.

MCWW25	X	R020	J	T	L
<b>Size Code</b>	<b>Type Code</b>	<b>Resistance Code</b>	<b>Tolerance</b>	<b>Packaging Code</b>	<b>Termination Code</b>
MCWW25 : 2512	X : Normal	E96 +E24:	J : ±5%	T : 7" Reeled	L = Sn base(lead free)
MCWW20 : 2010		R is first digit followed by 3 significant digits.	F : ±1%	Q : 10" Reeled	
MCWW18 : 1218		0.02Ω = R020	D : ±0.5%		
		0.51Ω = R510			
		0.025Ω = R025			

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