

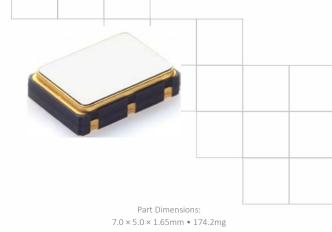
# Model 647H Very Low Jitter HCSL Clock

### **Features**

- High Speed Current Steering Logic [HCSL] Output
- Ceramic Surface Mount Package
- Low Phase Jitter Performance, 500fs Typical
- Fundamental or 3<sup>rd</sup> Overtone Crystal Design
- Frequency Range 13.5MHz 200MHz \*
- +2.5V or +3.3V Operation
- Output Enable Standard
- Tape and Reel Packaging, EIA-418

# **Applications**

- PCI Express [PCIe]
- Data Storage Systems
- Ethernet Line Cards
- Serial ATA Express [SATAe]
- Intel Chipsets
- Network Servers
- Switches and Routers
- Set-Top Boxes/DVRs



### Standard Frequencies

- 25MHz  **100MHz**
- 155.52MHz
- 27MHz
- 106.25MHz -
- 156.25MHz
- 50MHz 125MHz
- \* Check with factory for availability of frequencies not listed.

## Description

CTS Model 647H is a low cost, high performance clock oscillator supporting HCSL output. Employing the latest IC technology, M647H has excellent stability and low phase jitter performance.

# **Ordering Information**

Model		Output Type	F	requency Co [MHz]	de		Frequency Stability			erature nge		Supply Voltage		Packaging
647		Н		XXX or XXX	(		3			I		3		Т
							<b></b>					<u> </u>		
	Code	Output	_		C	ode	Stability	•			Code	Voltage	_	
	Н	HCSL - Pin 1 Enable	_		_	5	±25ppm	-			2	+2.5Vdc	_	
			_		_	4	±30ppm	-			3	+3.3Vdc	_	
						3	±50ppm							
						2	±100ppm							
									,					
			Code	Frequency				Code	Temp.	Range	-		Code	Packing
			-	duct Frequency Code <sup>1</sup>	. 1			С	-20°C t	o +70°C	-		Т	1k pcs./ree
			Product		de -			I	-40°C t	o +85°C	_			
								G	-40°C to	+105°C 2				

### Notes:

- 1] Refer to document 016-1454-0, Frequency Code Tables. 3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.
- 2] Check factory for availability. Stability codes 2 and 3 only.

Not all performance combinations and frequencies may be available.

Contact your local CTS Representative or CTS Customer Service for availability.

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.



### **Operating Conditions**

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT	
Maximum Supply Voltage	V <sub>CC</sub>	-	-0.3	-	4.0	V	
Cumple Valtage		150/	2.375	2.5	2.625	.,	
Supply Voltage	$V_{CC}$	±5%	3.135	3.3	3.465	V	
Supply Current	I <sub>cc</sub>	Maximum Load Maximum Current Value @ +3.3V	-	-	60	mA	
			-20		+70		
Operating Temperature	$T_A$	-	-40	+25	+85	°C	
			-40		+105		
Storage Temperature	T <sub>STG</sub>	-	-50	-	+125	°C	

### Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Frequency Range	f <sub>O</sub>	-		13.5 - 200		MHz
Frequency Stability [Note 1]	Δf/f <sub>O</sub>	-	25	5, 30, 50 or 1	00	±ppm
Aging	$\Delta f/f_{25}$	First Year @ +25°C, nominal V <sub>CC</sub>	-5	<u>±</u> 3	5	ppm

### **Output Parameters**

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Output Type	-	-		HCSL		-
Output Load	$R_L$	Terminated to ground	-	50	-	Ohms
Outrut Valtage Levels	V <sub>OH</sub>	110011	-580	-	850	\/
Output Voltage Levels	$V_{OL}$	HCSL LOAD	- HCSL inated to ground - 50 - HCSL Load -580 - 850 -150 - 150 O Ohms to ground 0.4	mV		
Output Duty Cycle	SYM	Differential Output, @ VCC - 1.3V	45	-	55	%
Differential Output Voltage	V <sub>OD</sub>	R <sub>L</sub> = 50 Ohms to ground	0.4	-	-	Vp-p
Rise and Fall Time	T <sub>R</sub> , T <sub>F</sub>	@ 20%/80% Levels, $R_L = 50$ Ohms to ground	-	0.50	0.70	ns

### **Output Parameters**

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Start Up Time	$T_S$	Application of $V_{CC}$	-	5	10	ms
Enable Function [Standby]						
Enable Input Voltage	$V_{IH}$	Pin 1 Logic '1', Output Enabled	$0.7V_{CC}$	-	-	V
Disable Input Voltage	$V_{IL}$	Pin 1 Logic '0', Output Disabled	-	-	$0.3V_{CC}$	V
Disable Current	$I_{\rm IL}$	Pin 1 Logic '0', Output Disabled	-	15	-	μΑ
Enable Time	$T_{PLZ}$	Pin 1 Logic '1', Output Enabled	-	-	2	ms
Phase Jitter, RMS	tjrms	Bandwidth 12 kHz - 20 MHz	-	500	-	fs

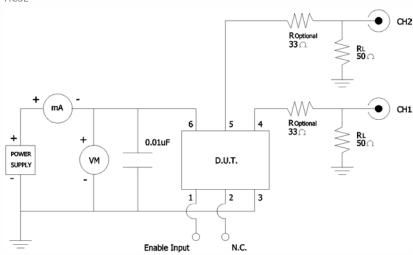


### **Enable Truth Table**

Pin 1	Pin 4 & Pin 5				
Logic '1'	Output Enabled				
Open	Output Enabled				
1: - (0)	Output Disabled,				
Logic '0'	High Impedance				

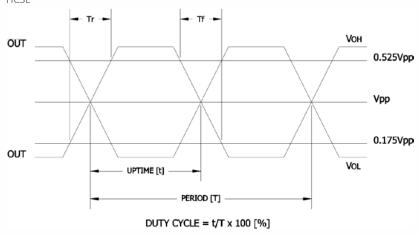
### **Test Circuit**

HCSL



### **Output Waveform**



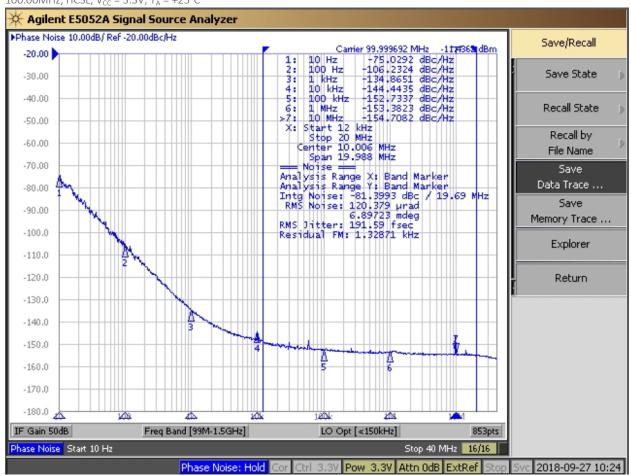




### Performance Data

Phase Noise [typical]

100.00MHz, HCSL,  $V_{CC} = 3.3V$ ,  $T_A = +25$ °C





### Performance Data

Phase Noise Tabulated

Typical, 100.00MHz, HCSL,  $V_{CC} = 3.3V$ ,  $T_A = +25^{\circ}C$ 

DARAMETER.	63/448.61	CONDITIONS	TVD	
PARAMETER	SYMBOL	CONDITIONS	TYP	UNIT
HCSL @ 100.00MHz				
Phase Noise		Single Side Band		
		@ 10Hz	-75.9328	
		@ 100Hz	-106.9929	
		@ 1kHz	-135.1951	dBc/Hz
	-	@ 10kHz	-144.2209	UBC/11Z
		@ 100kHz	-152.8159	
		@ 1MHz	-153.5793	
		@ 10MHz	-154.8219	
Phase Jitter, RMS	tjrms	Integration Bandwidth 12kHz - 20MHz	188.2315	fs



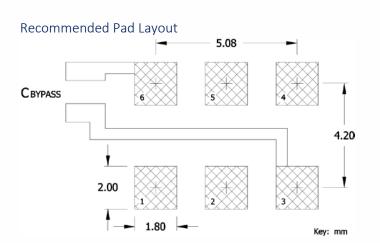
# **Mechanical Specifications**

# Package Drawing 7.00 ±0.20 1.40 1.22 647OSTV Dxxxx 1.65 Max Key: mm

### **Marking Information**

- 1. O Output Type; H = HCSL.
- 2. ST Frequency Stability/Temperature Code. [Refer to Ordering Information]
- 3. V Voltage Code; 3 = 3.3V, 2 = 2.5V.
- 4. D Date Code. See Table I for codes.
- 5. xxxx Frequency Code.
  - 3-digits, frequencies below 100MHz

4-digits, frequencies 100MHz or greater [See document 016-1454-0, Frequency Code Tables.]



### Notes

- 1. JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- 2. Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
- 3. MSL = 1.

### Pin Assignments

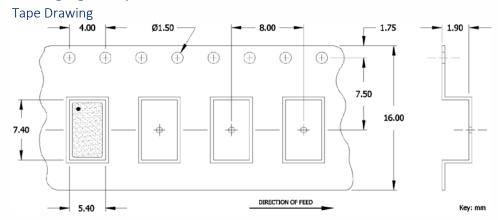
Pin	Symbol	Function						
1	EOH	Enable						
2	N.C.	No Connect						
3	GND	Circuit & Package Ground						
4	Output	RF Output						
5	Output	Complimentary RF Output						
6	$V_{CC}$	Supply Voltage						

### Table I - Date Code

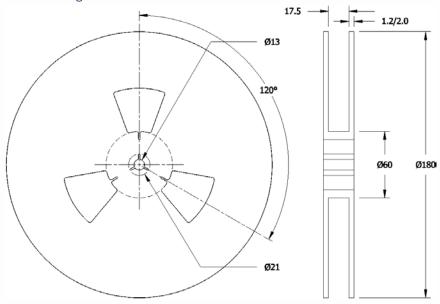
			MONTH		JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	ост	NOV	DEC
	YE	AR			JAN	FEB	IVIAR	APK	IVIAT	1014	JOL	AUG	SEP	UCI	NOV	DEC
2001	2005	2009	2013	2017	А	В	С	D	Ε	F	G	Н	J	K	L	М
2002	2006	2010	2014	2018	N	Р	Q	R	S	Т	U	V	W	Χ	Υ	Z
2003	2007	2011	2015	2019	а	b	С	d	е	f	g	h	j	k	I	m
2004	2008	2012	2016	2020	n	р	q	r	S	t	u	V	W	Х	У	Z



# Packaging - Tape and Reel



### **Reel Drawing**



### Notes

- 1. Device quantity is 1k pieces maximum per 180mm reel.
- 2. Complete CTS part number, frequency value and date code information must appear on reel and carton labels.