



Analog Semiconductor IC

VRD Series

High speed, Low dropout, $\pm 1\%$ High output accuracy
CMOS Voltage Regulator with Dual output

(IMPORTANT: Please check the last page for Genuine Product Labeling)

Rev. E13-01

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High speed, Low dropout, $\pm 1\%$ High output accuracy Dual CMOS Voltage Regulator

VRD Series

GENERAL DESCRIPTIONS

The VRD series are dual positive voltage regulators with high speed, low voltage dropout and high accuracy output achieved by low current consumption. Because each regulator is completely separated, the crosstalk between each regulator can be reduced. The output voltage is guaranteed within $\pm 1\%$ within the given temperature range by V_{ref} controlling the temperature characteristic. It corresponds to the low ESR capacitor as an output stabilization capacitor. The charged ESR capacitor can be discharged with an internal switch by making the $CE=V_{SS}$, as a result the V_{OUT} quickly returns to the V_{SS} level. To make the current capacity of the output transistor not exceeded, the over-current protection circuit is built in.



FEATURES

- Output voltage range 0.8V~5.0V (selectable with a step of 0.05V)
- Operating voltage range 1.6V~6.0V
- High accuracy output voltage $\pm 1\%$ ($V_{out}=1.95V\sim 5.0V$) / $\pm 20mV$ ($V_{out}=0.8V\sim 1.9V$)
- Maximum output current 250mA / Channel
- Dropout voltage Typ.90mV (output=3.0V, $I_{OUT}=100mA$)
- Low current consumption Typ. 25 μA / Channel
- High ripple rejection Typ. 75dB at 1KHz
- Low ESR capacitor 1.0 μF ceramic capacitor
- Operating temperature range $-40^{\circ}C \sim +85^{\circ}C$
- Built-in over-current protector Limit current : 300mA
- CL high-speed auto-discharge
- Built-in On/Off circuit
- Small package SOT-26 (2.9x2.8x1.1mm), LLP-6 (1.8x2.0x0.6mm)

APPLICATIONS

- Battery powered devices
- Cellular phone
- Digital / Video cameras
- Portable games
- Handheld instruments

PRODUCTS NUMBERING GUIDE



● **Function specification selection table**

Symbol	VR1	VR2	Function Specification
L	$\pm 20\text{mv}$	$\pm 20\text{mv}$	$V_{\text{out}1}=0.80\text{V}\sim 1.9\text{V}$ / $V_{\text{out}2}=0.80\text{V}\sim 1.9\text{V}$
M	$\pm 20\text{mv}$	$\pm 1\%$	$V_{\text{out}1}=0.80\text{V}\sim 1.9\text{V}$ / $V_{\text{out}2}=1.95\text{V}\sim 5.0\text{V}$
N	$\pm 1\%$	$\pm 20\text{mv}$	$V_{\text{out}1}=1.95\text{V}\sim 5.0\text{V}$ / $V_{\text{out}2}=0.80\text{V}\sim 1.9\text{V}$
P	$\pm 1\%$	$\pm 1\%$	$V_{\text{out}1}=1.95\text{V}\sim 5.0\text{V}$ / $V_{\text{out}2}=1.95\text{V}\sim 5.0\text{V}$

● **Output voltage selection table**

Symbol	Voltage	Symbol	Voltage	Symbol	Voltage	Symbol	Voltage	Symbol	Voltage
		1A	1.05	2A	2.05	3A	3.05	4A	4.05
		11	1.10	21	2.10	31	3.10	41	4.10
		1B	1.15	2B	2.15	3B	3.15	4B	4.15
		12	1.20	22	2.20	32	3.20	42	4.20
		1C	1.25	2C	2.25	3C	3.25	4C	4.25
		13	1.30	23	2.30	33	3.30	43	4.30
		1D	1.35	2D	2.35	3D	3.35	4D	4.35
		14	1.40	24	2.40	34	3.40	44	4.40
		1E	1.45	2E	2.45	3E	3.45	4E	4.45
		15	1.50	25	2.50	35	3.50	45	4.50
		1F	1.55	2F	2.55	3F	3.55	4F	4.55
		16	1.60	26	2.60	36	3.60	46	4.60
		1G	1.65	2G	2.65	3G	3.65	4G	4.65
		17	1.70	27	2.70	37	3.70	47	4.70
		1H	1.75	2H	2.75	3H	3.75	4H	4.75
08	0.80	18	1.80	28	2.80	38	3.80	48	4.80
0J	0.85	1J	1.85	2J	2.85	3J	3.85	4J	4.85
09	0.90	19	1.90	29	2.90	39	3.90	49	4.90
0K	0.95	1K	1.95	2K	2.95	3K	3.95	4K	4.95
10	1.00	20	2.00	30	3.00	40	4.00	50	5.00

PIN CONFIGURATION / MARKING SPECIFICATION (SOT-26) - 1



● **Pin Configuration**

No.	Symbol	Descriptions
1	EN1	1-Channel enable
2	VIN	Voltage input
3	EN2	2-Channel enable
4	VOUT2	2-Channel output
5	VSS	Power ground
6	VOUT1	1-Channel output

● **Marking Specification**

Code	Mark	Contents
A	D	Products series name
BC	08~50	1-Ch output voltage
DE	08~50	2-Ch output voltage
F	X	Discharge function
G	Internal rule	Lot number

PIN CONFIGURATION / MARKING SPECIFICATION (SOT-26) - 2



● **Pin Configuration**

No.	Symbol	Descriptions
1	EN1	1-Channel enable
2	VIN	Voltage input
3	EN2	2-Channel enable
4	VOUT2	2-Channel output
5	VSS	Power ground
6	VOUT1	1-Channel output

● **Marking Specification**

Code	Mark	Contents
A	D	Products series name
BC	08~50	1-Ch output voltage
DE	08~50	2-Ch output voltage
F	Internal rule	Lot number

PIN CONFIGURATION / MARKING SPECIFICATION (LLP-6)



(Top view)

● **Pin Configuration**

No.	Symbol	Descriptions
1	EN2	2-Channel enable
2	VIN	Voltage input
3	EN1	1-Channel enable
4	VSS	Power ground
5	VOUT1	1-Channel output
6	VOUT2	2-Channel output

● **Marking Specification**

Code	Mark	Contents
A	D	Products series name
BC	08~50	1-Ch output voltage
DE	08~50	2-Ch output voltage
F	X	Discharge function
G	Internal rule	Lot number

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Items	Symbol	Ratings	Unit	
Input voltage range	V _{IN}	-0.3 ~ +7.0	V	
Output current	I _{OUT}	500	mA	
Output voltage range	V _{OUT}	V _{SS} -0.3 ~ V _{IN} +0.3	V	
Power dissipation ※1)	SOT-26	PD	600	mW
	LLP-6	PD	1000	mW
Operating temperature range	T _{OPR}	-40 ~ +85	°C	
Storage temperature range	T _{STG}	-55 ~ +125	°C	

Note :

※1) Power dissipation depends on conditions of mounting on boards. PCB dimension is 50mm×50mm×1.6mm.

TYPICAL APPLICATION CIRCUITS



ELECTRICAL CHARACTERISTICS

(Ta=25°C unless otherwise specified)

Items	Symbol	Conditions	Min.	Typ.	Max.	Unit	Test circuit	
Output voltage	V _{OUT}	V _{OUT} +1.0V ≤ V _{IN} ≤ 6.0V I _{OUT} =30mA Ta=-40°C ~ +85°C	V _{OUT} ≥ 1.95V	V _{OUT} x0.99	V _{OUT}	V _{OUT} x1.01	V	1
			V _{OUT} ≤ 1.90V	-20	-	+20	mV	
Output current	I _{OUT}	V _{IN} ≥ V _{OUT} +1.0V	0.9V ≤ V _{OUT} ≤ 1.15V	-	250	-	mA	1
			1.2V ≤ V _{OUT} ≤ 1.65V	-	250	-	mA	
			1.7V ≤ V _{OUT} ≤ 2.25V	-	250	-	mA	
			2.3V ≤ V _{OUT} ≤ 2.85V	-	250	-	mA	
			2.9V ≤ V _{OUT} ≤ 3.45V	-	250	-	mA	
			3.5V ≤ V _{OUT} ≤ 4.05V	-	250	-	mA	
			4.0V ≤ V _{OUT} ≤ 5.00V	-	250	-	mA	
Dropout voltage	V _{DROP}	I _{OUT} =100mA	0.9V ≤ V _{OUT} ≤ 1.15V	-	580	910	mV	1
			1.2V ≤ V _{OUT} ≤ 1.65V	-	210	340	mV	
			1.7V ≤ V _{OUT} ≤ 2.25V	-	130	205	mV	
			2.3V ≤ V _{OUT} ≤ 2.85V	-	100	150	mV	
			2.9V ≤ V _{OUT} ≤ 3.45V	-	90	137	mV	
			3.5V ≤ V _{OUT} ≤ 4.05V	-	85	125	mV	
			4.1V ≤ V _{OUT} ≤ 5.00V	-	80	115	mV	

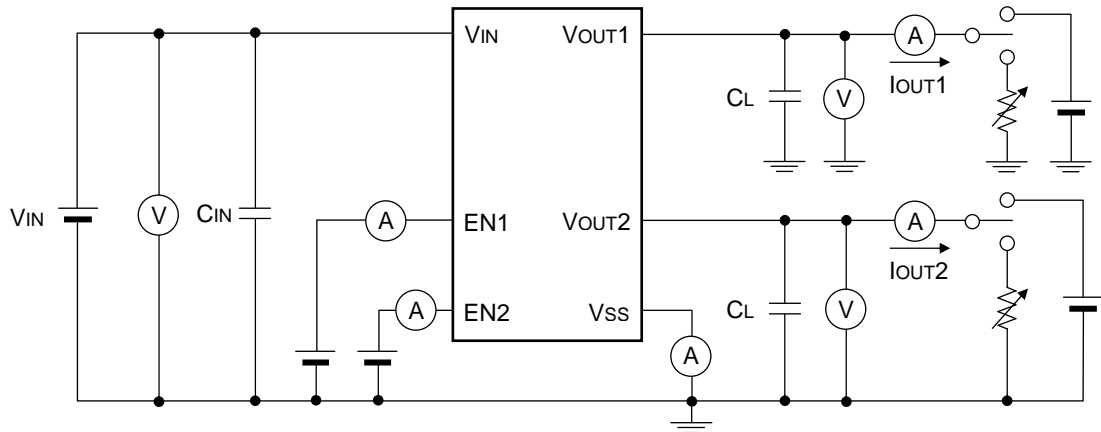
ELECTRICAL CHARACTERISTICS (CONTINUED)

(Ta=25°C unless otherwise specified)

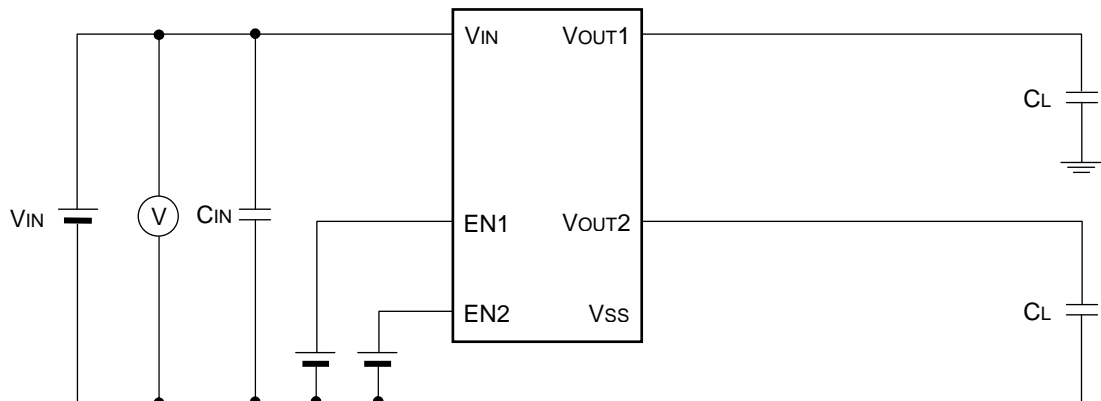
Items	Symbol	Conditions	Min.	Typ.	Max.	Unit	Test circuit
Current consumption	I _{DD}	V _{IN} =V _{OUT} +1.0V, V _{OUT} =Open, I _{OUT} =0mA	-	50	80	μA	2
Standby current	I _{STB}	EN=V _{SS}	-	0.01	0.1	μA	2
Input voltage	V _{IN}		1.6	-	6.0	V	1
Load regulation	ΔV _{OUT}	V _{IN} =V _{OUT} +1.0V, I _{OUT} =0.1mA ~100mA	-	10	40	mV	1
Line regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \cdot V_{OUT}}$	V _{OUT} +1.0V ≤ V _{IN} ≤ 6.0V, I _{OUT} =30mA	-	0.01	0.1	%/V	1
Ripple rejection	R _r	V _{IN} =V _{OUT} +1.0V, f=1KHz, ΔV _{RIP} =0.5VP-P, I _{OUT} =30mA	-	75	-	dB	3
Limit current	I _{LIMIT}	EN=V _{IN}	300	-	-	mA	1
Short circuit current	I _{SHORT}	V _{IN} =V _{OUT} +1.0V, V _{OUT} =0V	-	40	-	mA	1
Output voltage temperature coefficient	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V _{EN} =V _{IN} , I _{OUT} =300mA Ta=-40°C ~ +85°C		±20		ppm/°C	1
EN high level voltage	V _{ENH}		1.2	-	6.0	V	1
EN low level voltage	V _{ENL}		-	-	0.3	V	1
EN high level current	I _{ENH}	V _{EN} = V _{IN}	-0.1	-	0.1	μA	1
EN low level current	I _{ENL}	V _{EN} = V _{SS}	-0.1	-	0.1	μA	1
CL auto-discharge resistance	R _{DIS}	V _{IN} =6.0V, V _{OUT} =4.0V, V _{EN} = V _{SS}	-	160	-	Ω	1

TEST CIRCUITS

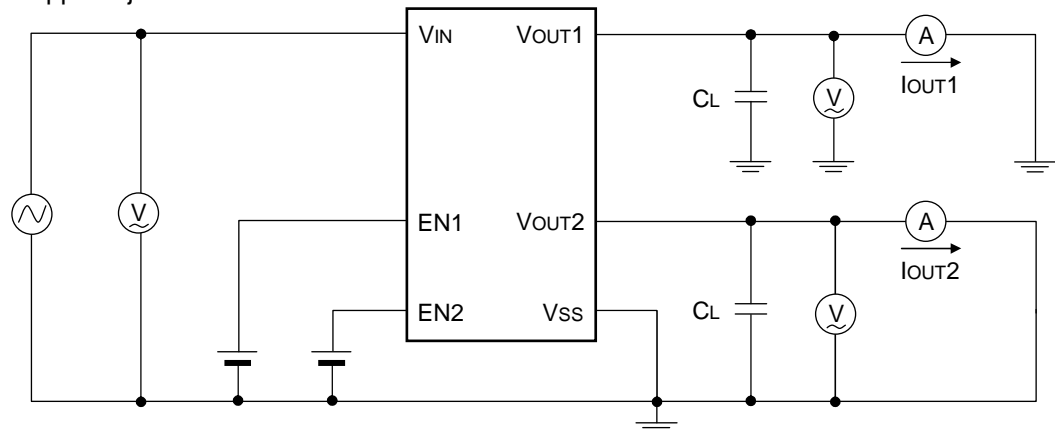
- Circuit (1)** - Output voltage, Output current, Dropout voltage, Input voltage, Load regulation, Line regulation, Limit current, Short circuit current, Output voltage temperature coefficient, CL auto-discharge resistance, EN high level voltage, EN low level voltage, EN high level current, EN low level current



- Circuit (2)** - Current consumption, Standby current



- Circuit (3)** - Ripple rejection



DESCRIPTION OF OPERATION

● General operation

In reference to following a part of the block diagram of output voltage control of the VRD series ;



By the error amplifier, the reference voltage (V_{REF}) is compared with $V_{FEEDBACK}$ which is divided by feedback resistors R_1 and R_2 . It supplies the output transistor (see note *1) to keep a stabilized output voltage against with any fluctuation of input voltage by negative feedback system.

Note *1)

The VRD series has P-channel MOSFET which is connected to the VOUT terminal as the output transistor. To prevent the VRD series from being damaged due to inverse current from VOUT terminal to VIN terminal through a parasitic diode, VOUT should not be exceeded $V_{IN} + 0.3V$.

● EN terminal (shutdown function) & CL auto-discharge

The VRD series can be shutdown through EN terminal. The P-channel MOSFET is turned off and operation of all internal circuits stops to reduce the current consumption when the signal of EN terminal is set to the shutdown level. In shutdown mode, the VRD series enables the electric charge at the CL to be discharged via the auto-discharge resistance (R_{DIS} ; 160Ω), and the VOUT terminal can be rapidly returned to the VSS level as a result.

● Output capacitor (CL)

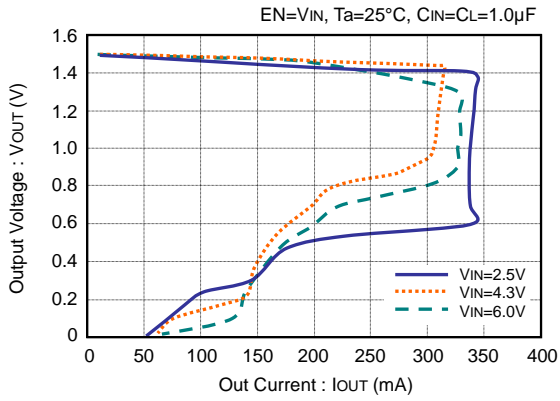
For phase compensation, an output capacitor (C_L) is required to connected between VOUT terminal and VSS terminal as close as possible. A ceramic capacitor with capacitance value of at least $1.0\mu F$ is recommended. Of course, the suitable capacitance value should be different by the condition of each application. In addition, an input capacitor (C_{IN}) with capacitance value of at least $1.0\mu F$ is also recommended between VIN terminal and VSS terminal for stable input power.

● Over current protection and short circuit protection

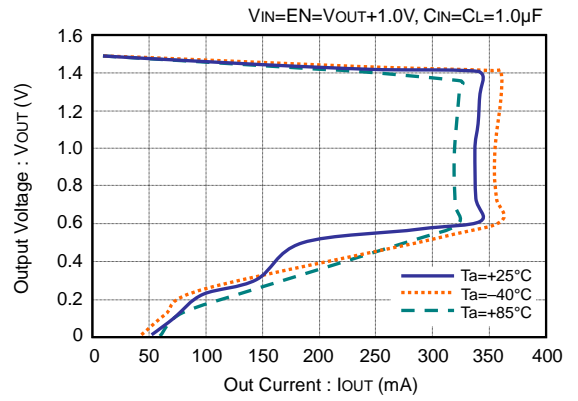
Built-in over current protection circuit performs circuit protection from over current as a current limiter. When the load current reaches the limit level, the output voltage drops and output current also decreases accordingly. A current of typ. $40mA$ will flow when output terminal is shorted.

TYPICAL CHARACTERISTICS – Output Voltage vs. Output Current

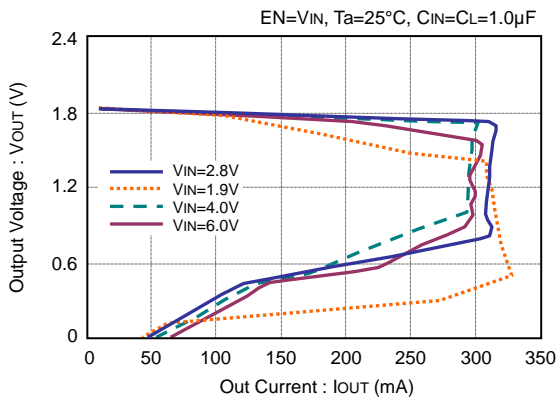
● **VRD (V_{OUT}=1.5V)**



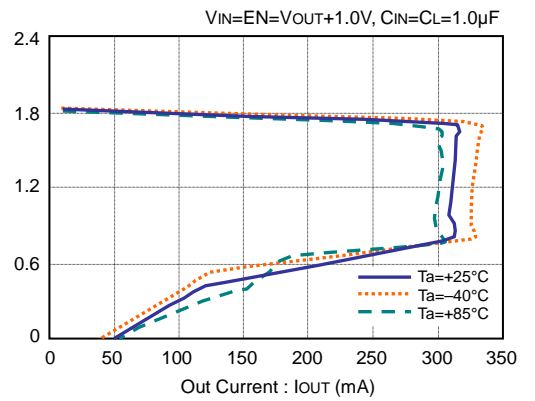
● **VRD (V_{OUT}=1.5V)**



● **VRD (V_{OUT}=1.8V)**



● **VRD (V_{OUT}=1.8V)**



TYPICAL CHARACTERISTICS – Output Voltage vs. Output Current (continued)

● **VRD (V_{OUT}=2.8V)**



● **VRD (V_{OUT}=2.8V)**



● **VRD (V_{OUT}=3.3V)**

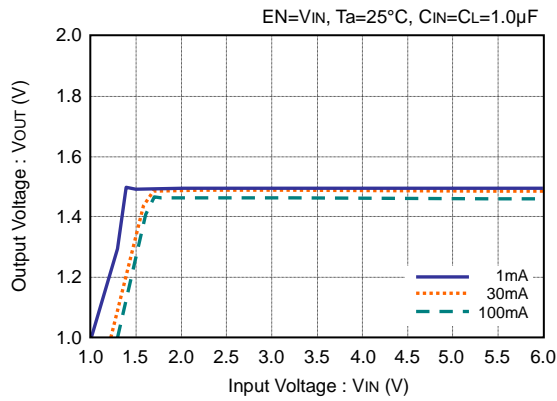


● **VRD (V_{OUT}=3.3V)**

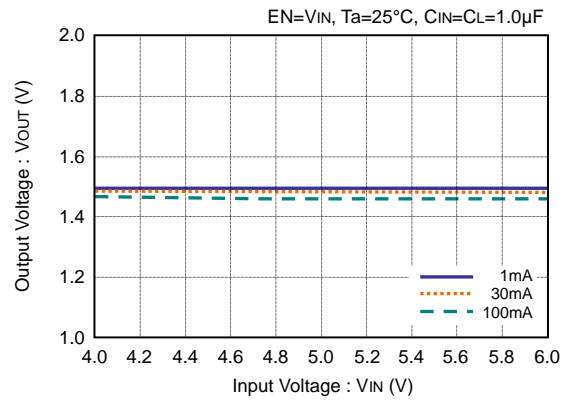


TYPICAL CHARACTERISTICS – Output Voltage vs. Input Voltage

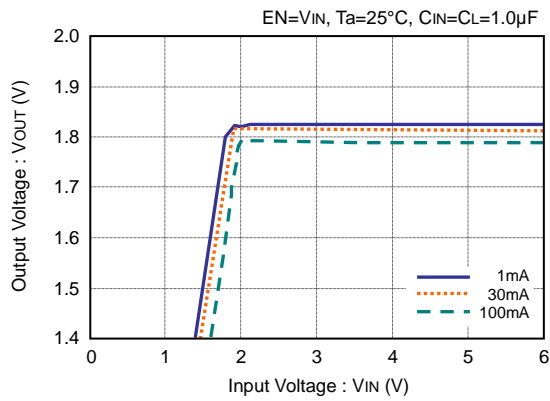
● **VRD (V_{OUT}=1.5V)**



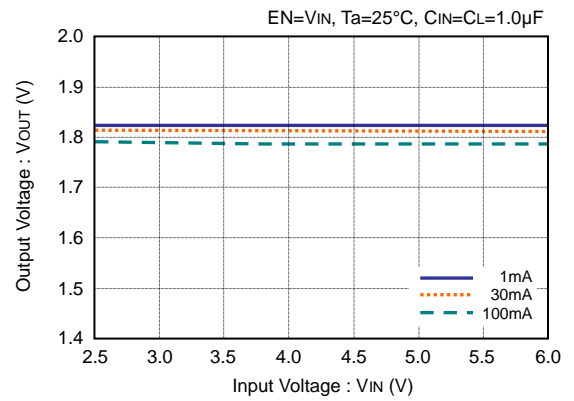
● **VRD (V_{OUT}=1.5V)**



● **VRD (V_{OUT}=1.8V)**



● **VRD (V_{OUT}=1.8V)**



TYPICAL CHARACTERISTICS – Output Voltage vs. Input Voltage (continued)

● **VRD (V_{OUT}=2.8V)**



● **VRD (V_{OUT}=2.8V)**



● **VRD (V_{OUT}=3.3V)**

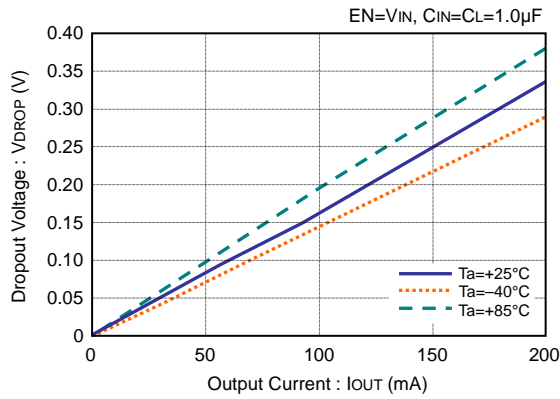


● **VRD (V_{OUT}=3.3V)**

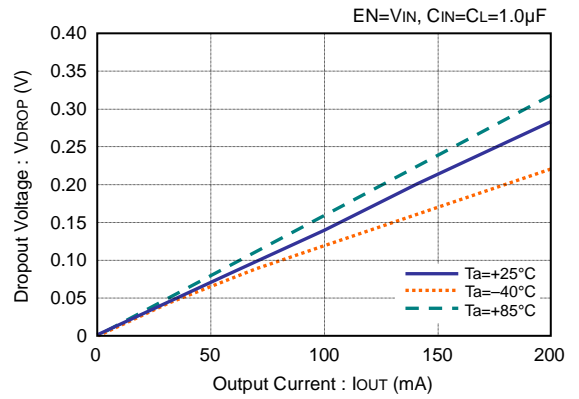


TYPICAL CHARACTERISTICS – Dropout Voltage vs. Output Current

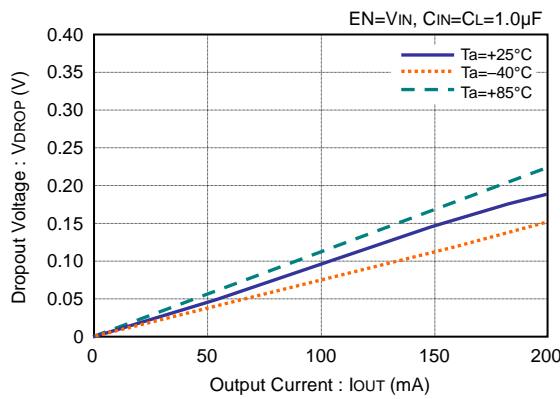
● **VRD (V_{OUT}=1.5V)**



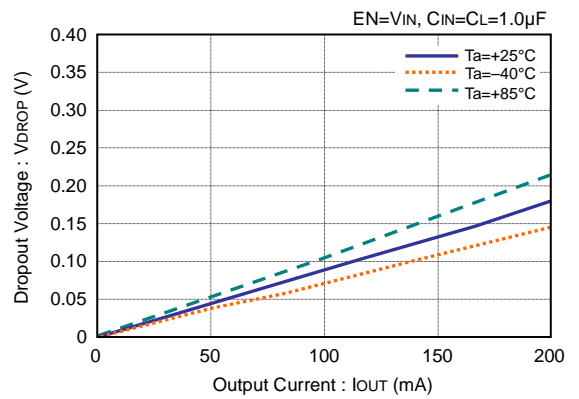
● **VRD (V_{OUT}=1.8V)**



● **VRD (V_{OUT}=2.8V)**



● **VRD (V_{OUT}=3.3V)**

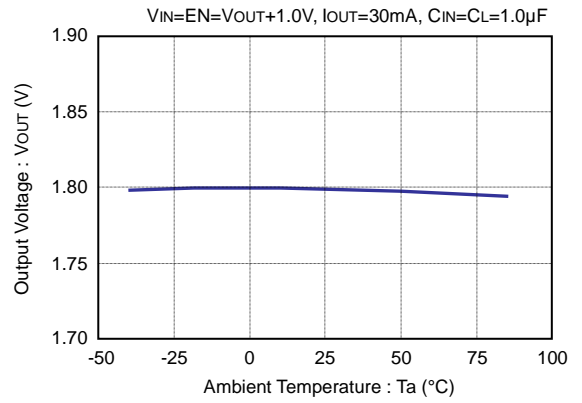


TYPICAL CHARACTERISTICS – Output Voltage vs. Ambient Temperature

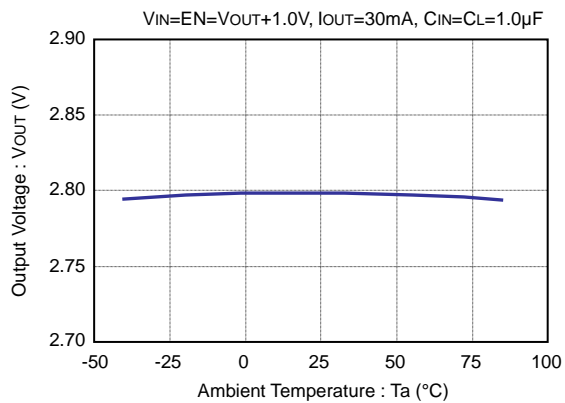
● **VRD (V_{OUT}=1.5V)**



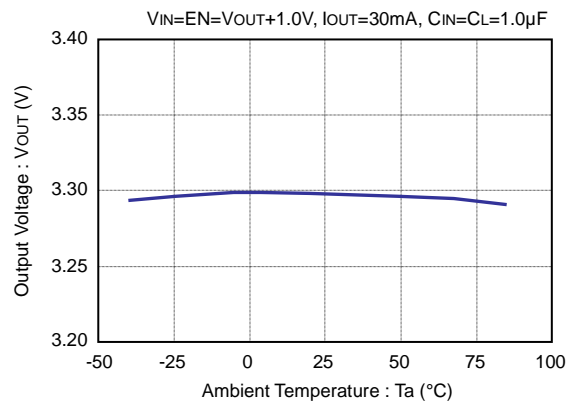
● **VRD (V_{OUT}=1.8V)**



● **VRD (V_{OUT}=2.8V)**



● **VRD (V_{OUT}=3.3V)**



TYPICAL CHARACTERISTICS – Supply Current vs. Ambient Temperature



TYPICAL CHARACTERISTICS – Cross Talk

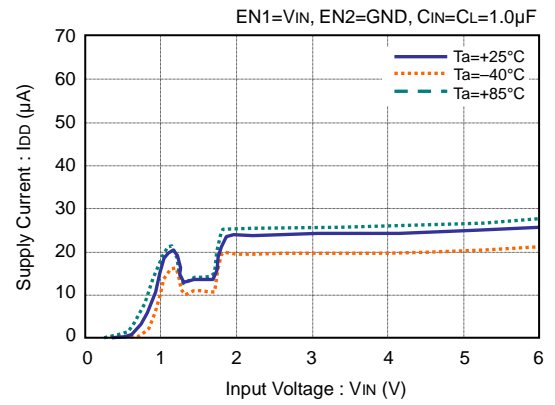


TYPICAL CHARACTERISTICS – Supply Current vs. Input Voltage

● **VRD (V_{OUT}=1.5V)**



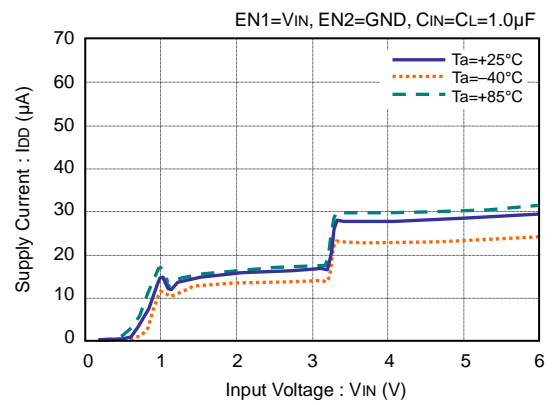
● **VRD (V_{OUT}=1.8V)**



● **VRD (V_{OUT}=2.8V)**

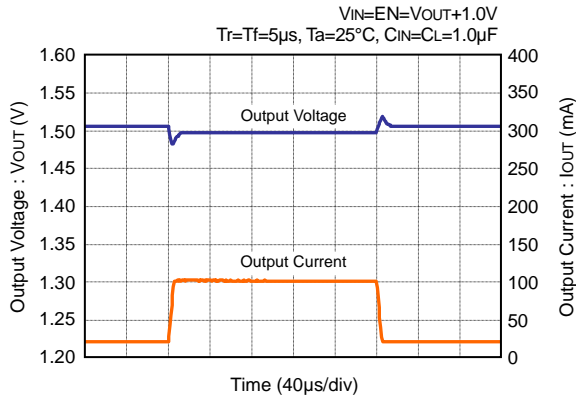


● **VRD (V_{OUT}=3.3V)**

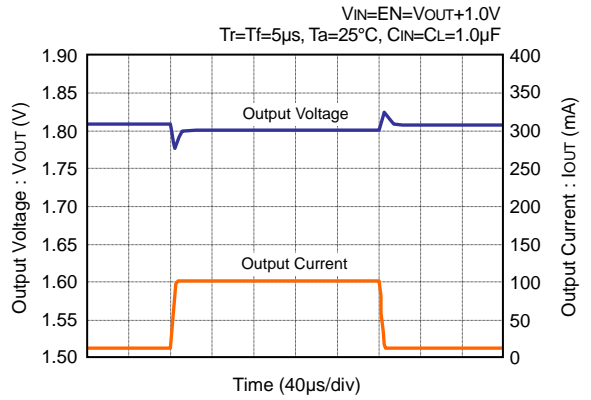


TYPICAL CHARACTERISTICS – Load Transient Response

● **VRD (VOUT=1.5V)**



● **VRD (VOUT=1.8V)**



● **VRD (VOUT=2.8V)**

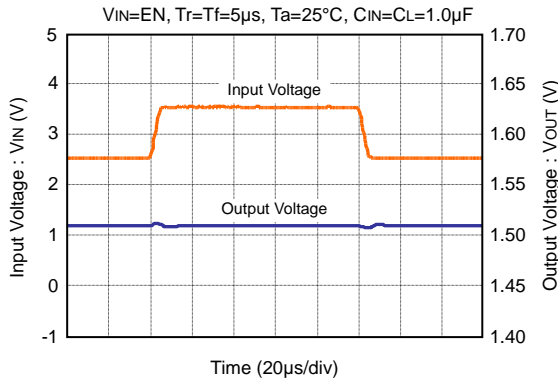


● **VRD (VOUT=3.3V)**

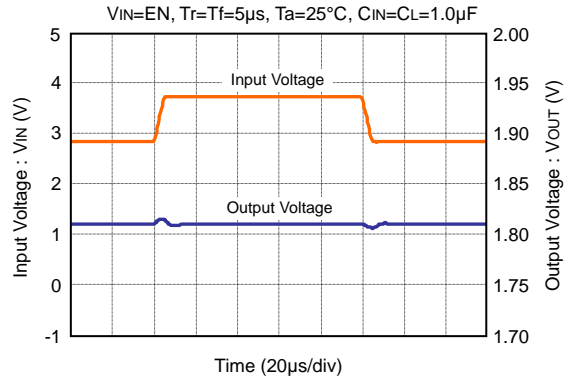


TYPICAL CHARACTERISTICS – Input Transient Response ($I_{out}=1mA$)

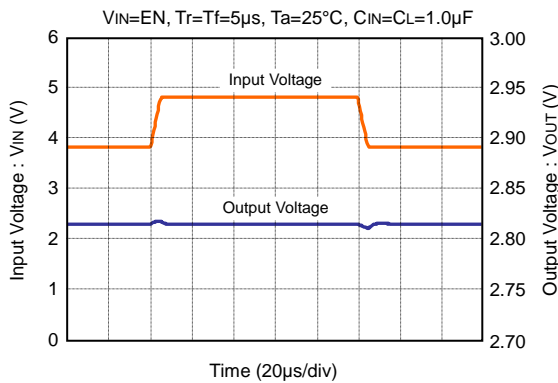
● **VRD ($V_{OUT}=1.5V$)**



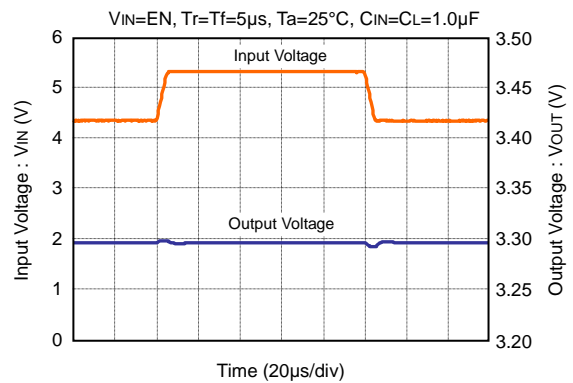
● **VRD ($V_{OUT}=1.8V$)**



● **VRD ($V_{OUT}=2.8V$)**

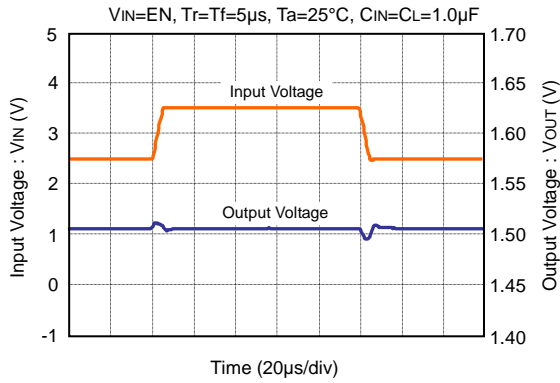


● **VRD ($V_{OUT}=3.3V$)**



TYPICAL CHARACTERISTICS – Input Transient Response (Iout=30mA)

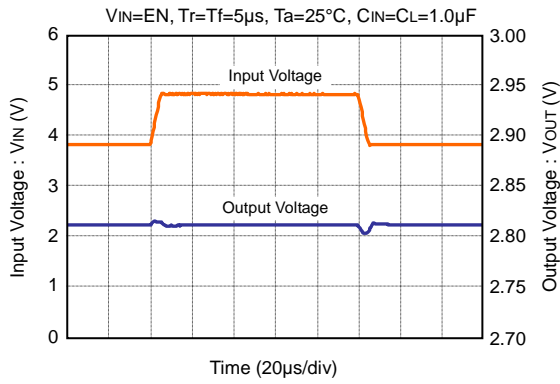
● **VRD (Vout=1.5V)**



● **VRD (Vout=1.8V)**



● **VRD (Vout=2.8V)**

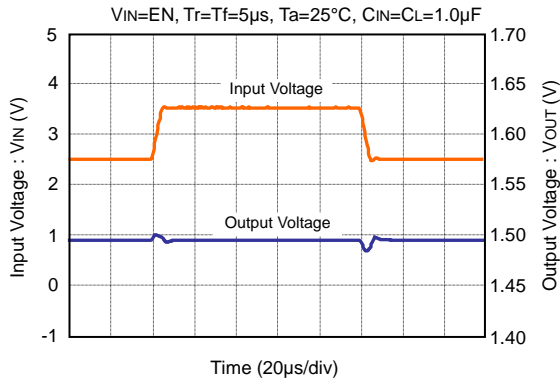


● **VRD (Vout=3.3V)**

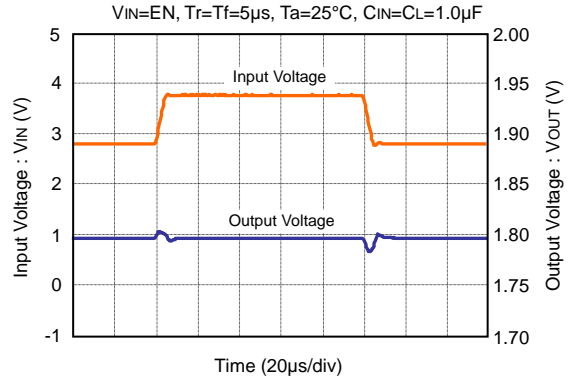


TYPICAL CHARACTERISTICS – Input Transient Response ($I_{out}=100mA$)

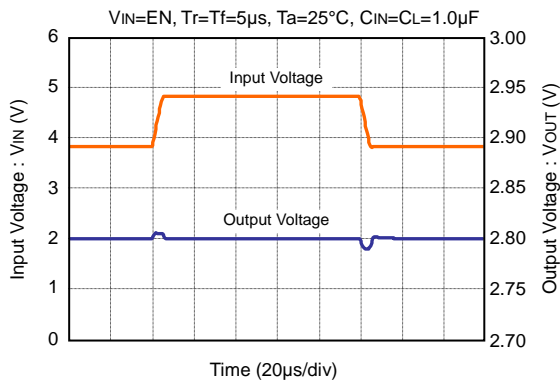
● **VRD ($V_{OUT}=1.5V$)**



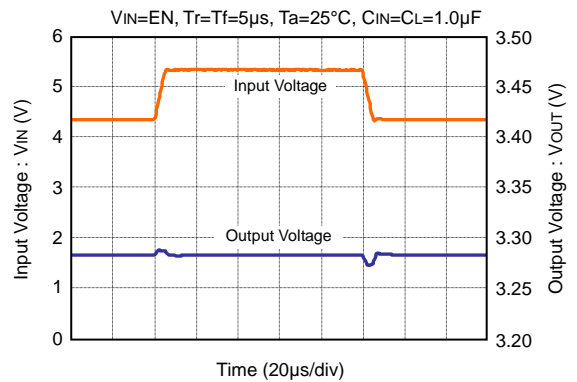
● **VRD ($V_{OUT}=1.8V$)**



● **VRD ($V_{OUT}=2.8V$)**

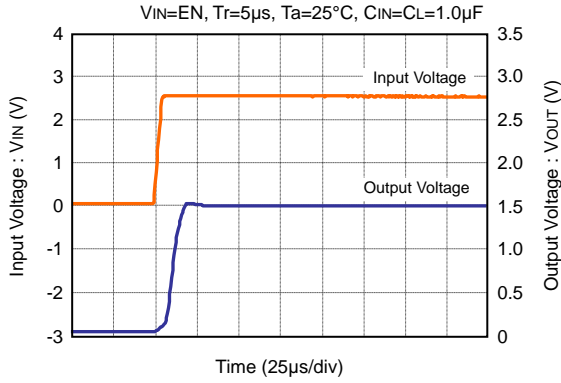


● **VRD ($V_{OUT}=3.3V$)**

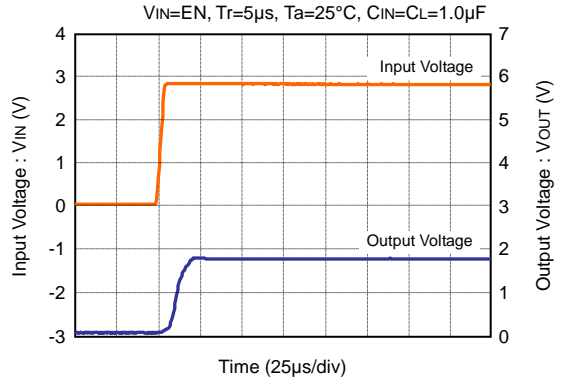


TYPICAL CHARACTERISTICS – Input Rise Time (Iout=1mA)

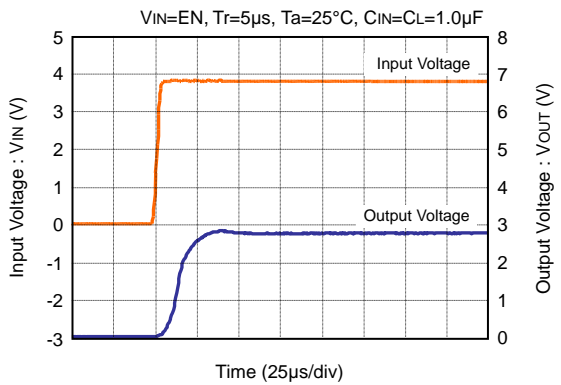
● **VRD (VOUT=1.5V)**



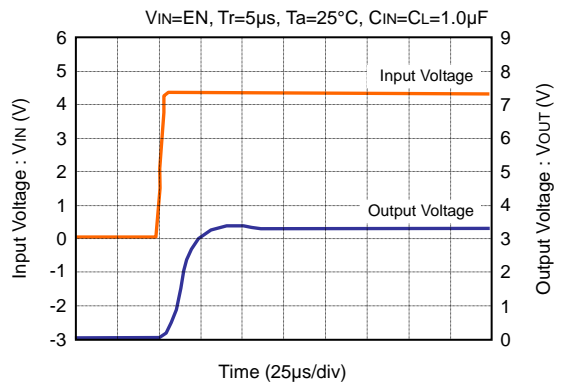
● **VRD (VOUT=1.8V)**



● **VRD (VOUT=2.8V)**



● **VRD (VOUT=3.3V)**



TYPICAL CHARACTERISTICS – Input Rise Time (I_{out}=30mA)

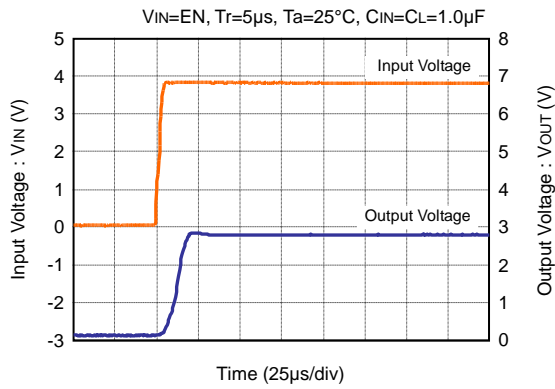
● **VRD (V_{OUT}=1.5V)**



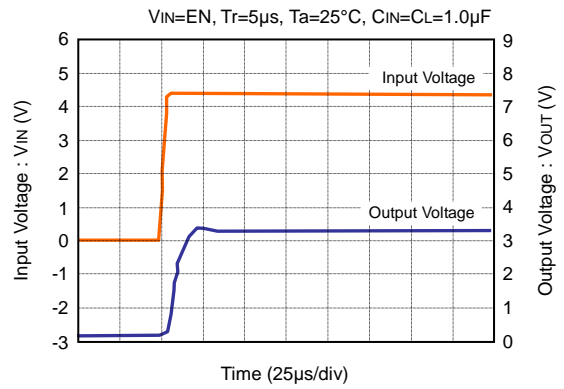
● **VRD (V_{OUT}=1.8V)**



● **VRD (V_{OUT}=2.8V)**

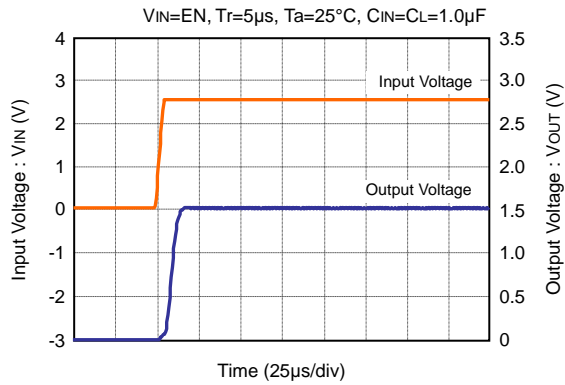


● **VRD (V_{OUT}=3.3V)**

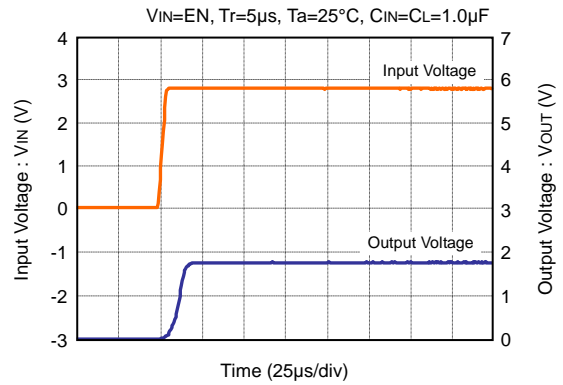


TYPICAL CHARACTERISTICS – Input Rise Time (Iout=100mA)

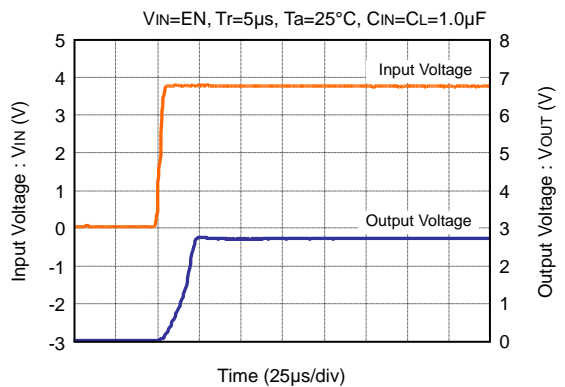
● **VRD (Vout=1.5V)**



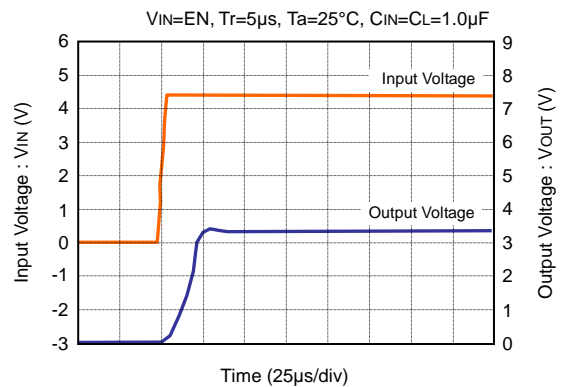
● **VRD (Vout=1.8V)**



● **VRD (Vout=2.8V)**

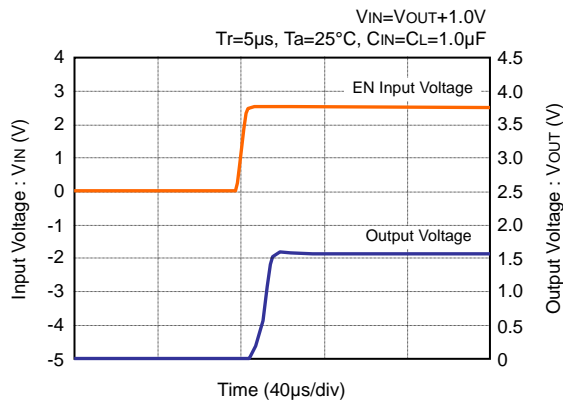


● **VRD (Vout=3.3V)**

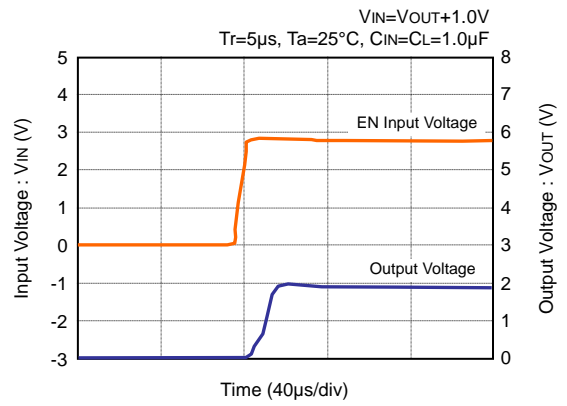


TYPICAL CHARACTERISTICS – EN Rise Time ($I_{out}=1mA$)

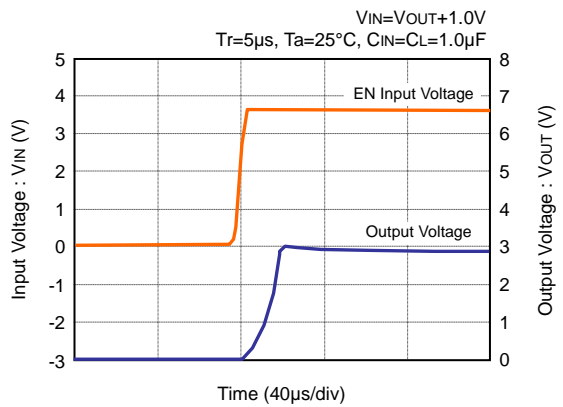
● **VRD ($V_{out}=1.5V$)**



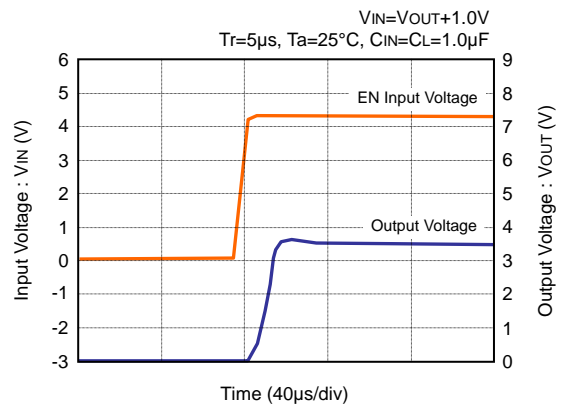
● **VRD ($V_{out}=1.8V$)**



● **VRD ($V_{out}=2.8V$)**



● **VRD ($V_{out}=3.3V$)**

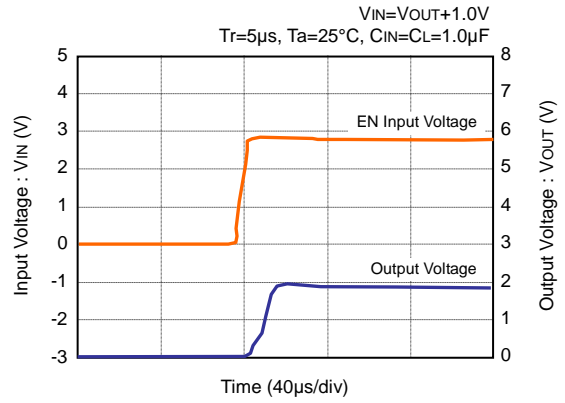


TYPICAL CHARACTERISTICS – EN Rise Time (Iout=30mA)

● **VRD (VOUT=1.5V)**



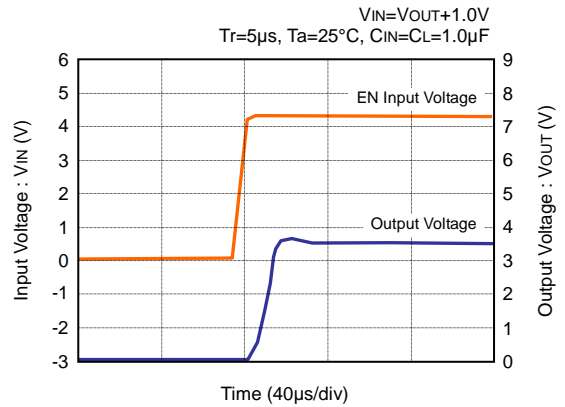
● **VRD (VOUT=1.8V)**



● **VRD (VOUT=2.8V)**

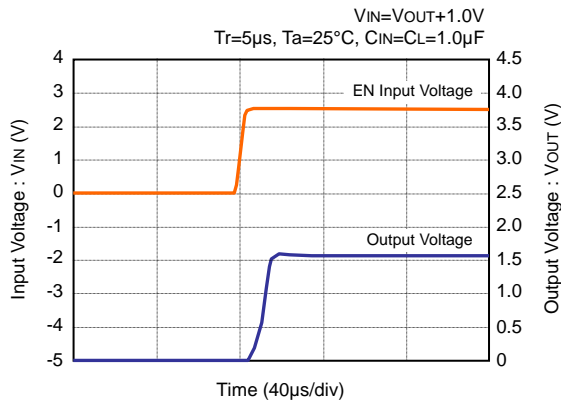


● **VRD (VOUT=3.3V)**

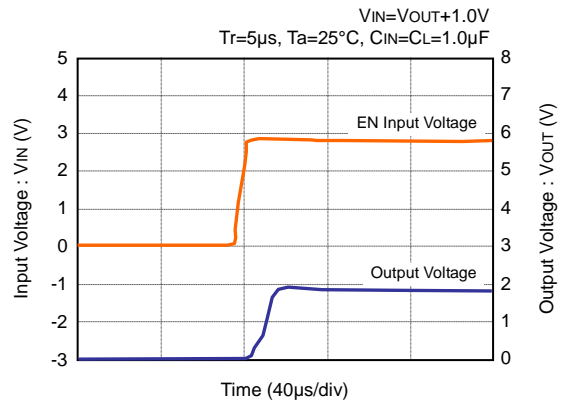


TYPICAL CHARACTERISTICS – EN Rise Time (I_{out}=100mA)

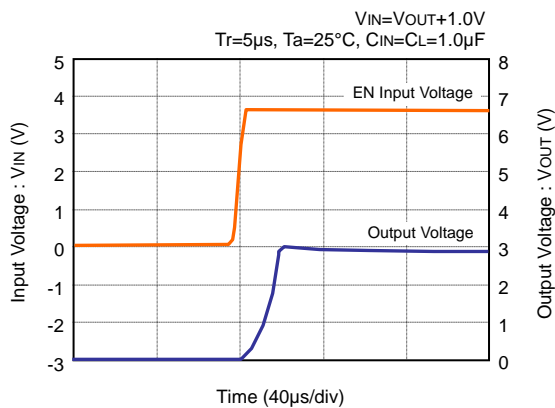
● **VRD (V_{OUT}=1.5V)**



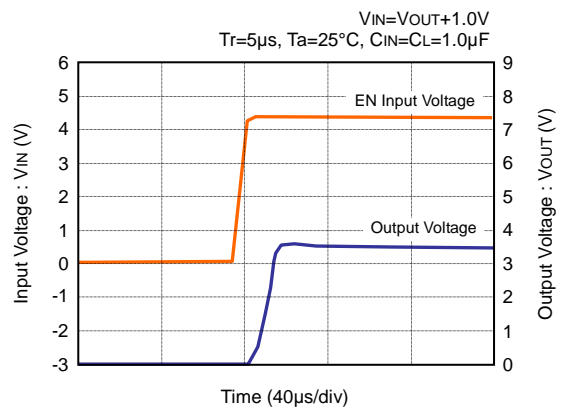
● **VRD (V_{OUT}=1.8V)**



● **VRD (V_{OUT}=2.8V)**

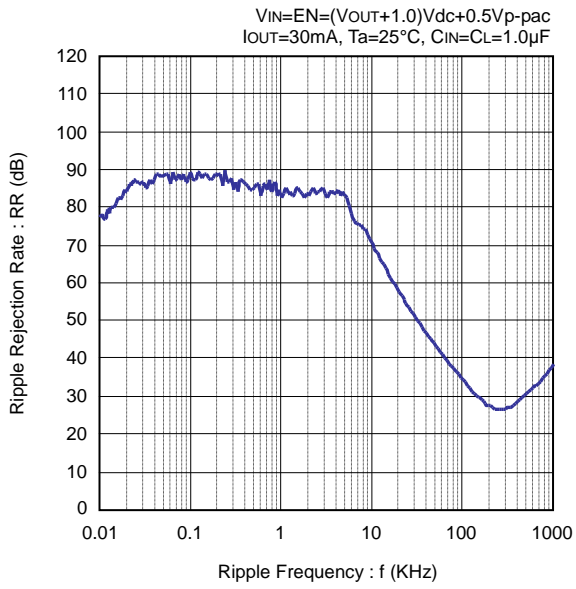


● **VRD (V_{OUT}=3.3V)**

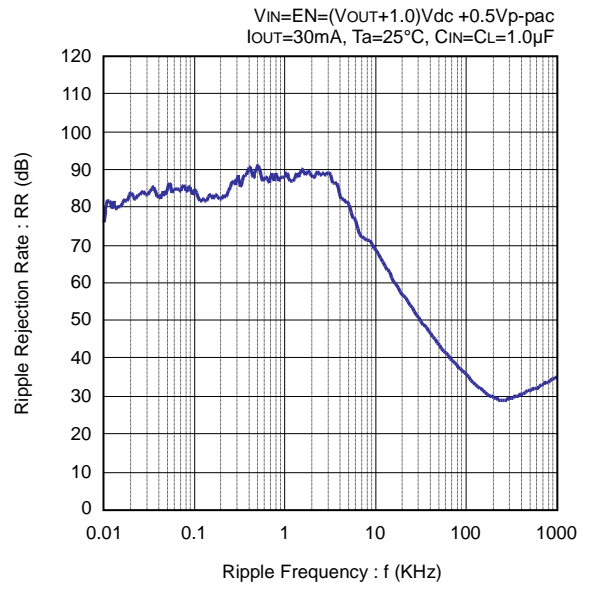


TYPICAL CHARACTERISTICS – Ripple Rejection Rate

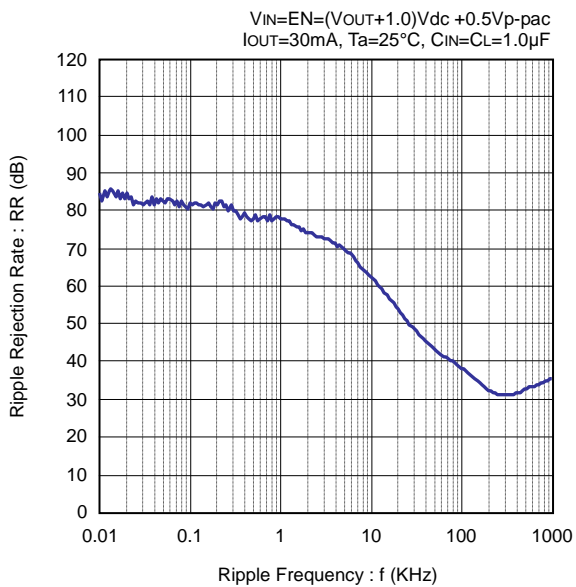
● **VRD (VOUT=1.5V)**



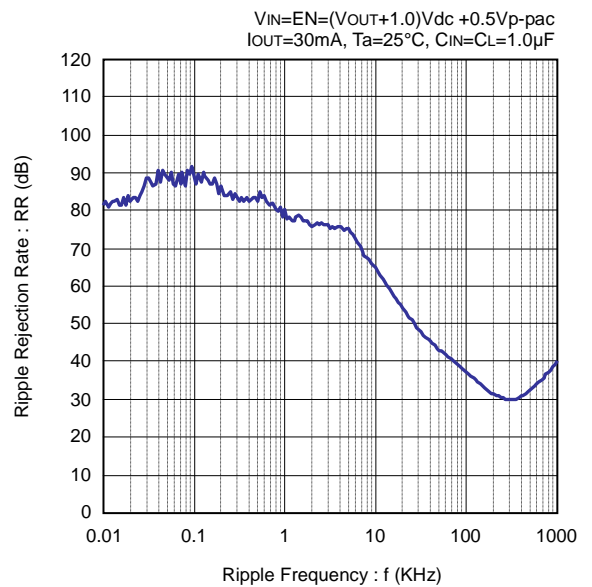
● **VRD (VOUT=1.8V)**



● **VRD (VOUT=2.8V)**



● **VRD (VOUT=3.3V)**



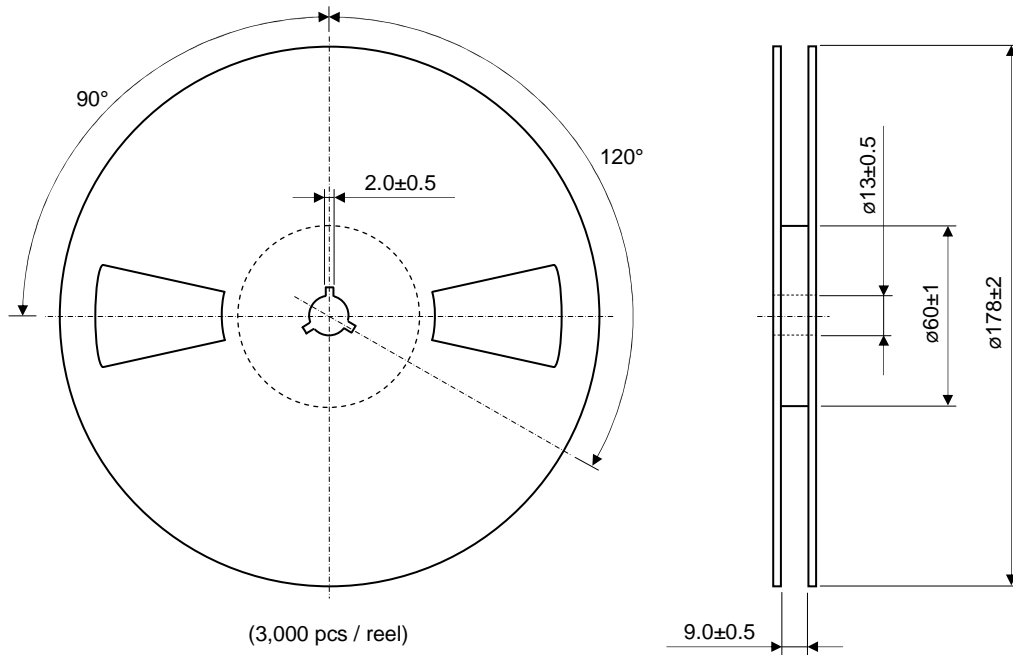
TAPING AND LOADING SPECIFICATIONS (SOT-26)

(Unit : mm)



REEL DIMENSIONS (SOT-26)

(Unit : mm)



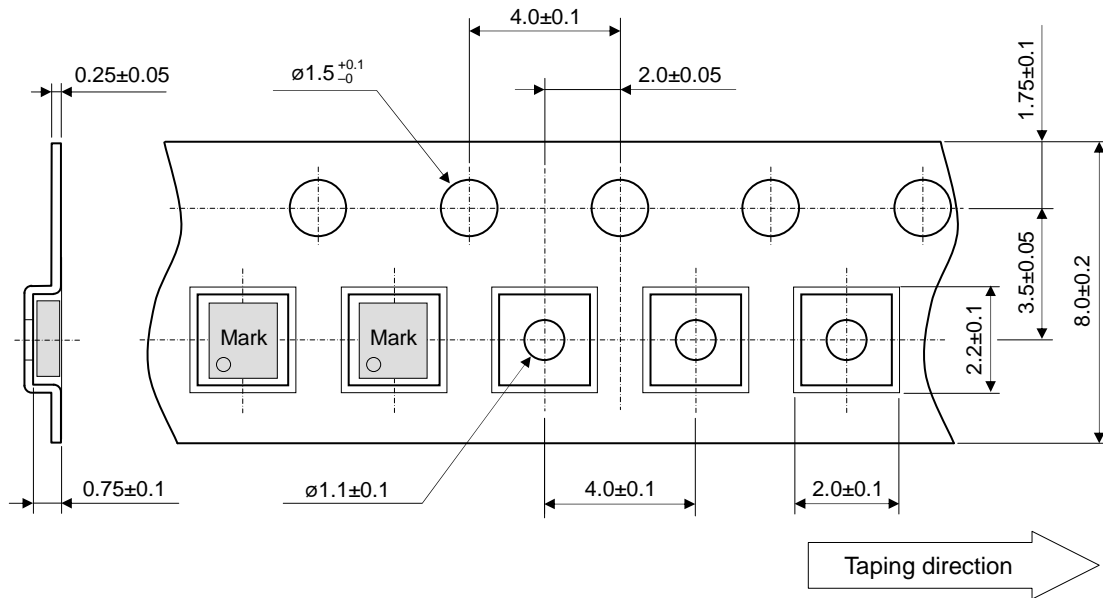
PACKAGE DIMENSIONS (LLP-6)

(Unit : mm)



TAPING AND LOADING SPECIFICATIONS (LLP-6)

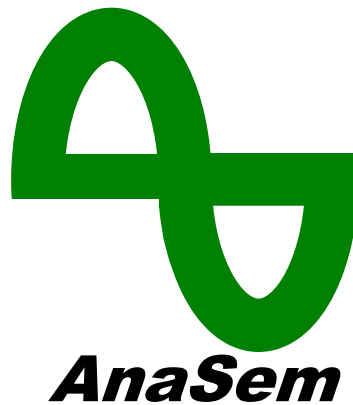
(Unit : mm)



REEL DIMENSIONS (LLP-6)

(Unit : mm)





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