

Automotive-Grade, Galvanically Isolated Current Sensor IC with Common-Mode Field Rejection in a Small-Footprint SOIC8 Package

FEATURES AND BENEFITS

- AEC-Q100 qualified
- Differential Hall sensing rejects common-mode fields
- 1.2 m Ω primary conductor resistance for low power loss and high inrush current withstand capability
- Integrated shield virtually eliminates capacitive coupling from current conductor to die, greatly suppressing output noise due to high dv/dt transients
- Industry-leading noise performance with greatly improved bandwidth through proprietary amplifier and filter design techniques
- High-bandwidth 120 kHz analog output for faster response times in control applications
- Filter pin allows user to filter the output for improved resolution at lower bandwidth
- Patented integrated digital temperature compensation circuitry allows for near closed loop accuracy over temperature in an open loop sensor
- Small-footprint, low-profile SOIC8 package suitable for space-constrained applications
- Filter pin simplifies bandwidth limiting for better resolution at lower frequencies

Continued on the next page





PACKAGE: 8-Pin SOIC (suffix LC)



Not to scale

DESCRIPTION

The Allegro[™] ACS724 current sensor IC is an economical and precise solution for AC or DC current sensing in industrial, automotive, commercial, and communications systems. The small package is ideal for space-constrained applications while also saving costs due to reduced board area. Typical applications include motor control, load detection and management, switched-mode power supplies, and overcurrent fault protection.

The device consists of a precise, low-offset, linear Hall sensor circuit with a copper conduction path located near the surface of the die. Applied current flowing through this copper conduction path generates a magnetic field which is sensed by the integrated Hall IC and converted into a proportional voltage. The current is sensed differentially in order to reject common-mode fields, improving accuracy in magnetically noisy environments. The inherent device accuracy is optimized through the close proximity of the magnetic field to the Hall transducer. A precise, proportional voltage is provided by the low-offset, chopper-stabilized BiCMOS Hall IC, which is programmed for accuracy after packaging. The output of the device has a positive slope when an increasing current flows through the primary copper conduction path (from pins 1 and 2, to pins 3 and 4), which is the path used for current sensing. The internal resistance of this conductive path is $1.2 \text{ m}\Omega$ typical, providing low power loss.

The terminals of the conductive path are electrically isolated from the sensor leads (pins 5 through 8). This allows the ACS724 current sensor IC to be used in high-side current sense applications without the use of high-side differential amplifiers or other costly isolation techniques.

Continued on the next page ...

1 8 VCC IP+ ACS724 2 VIOUT IP+ CBYPASS 6 3 0.1 µF FILTER IP-CLOAD C_{F} 5 4 1 nF IP-GND Typical Application

The ACS724 outputs an analog signal, V_{IOUT} , that changes proportionally with the bidirectional AC or DC primary sensed current, I_P , within the specified measurement range. The FILTER pin can be used to decrease the bandwidth in order to optimize the noise performance.

FEATURES AND BENEFITS (continued)

- 5 V, single supply operation
- Output voltage proportional to AC or DC current
- Factory-trimmed sensitivity and quiescent output voltage for improved accuracy
- Chopper stabilization results in extremely stable quiescent output voltage
- Nearly zero magnetic hysteresis

SELECTION GUIDE

Ratiometric output from supply voltage

DESCRIPTION (continued)

The ACS724 is provided in a small, low-profile surface-mount SOIC8 package. The leadframe is plated with 100% matte tin, which is compatible with standard lead (Pb) free printed circuit board assembly processes. Internally, the device is Pb-free, except for flip-chip high-temperature Pb-based solder balls, currently exempt from RoHS. The device is fully calibrated prior to shipment from the factory.

Part Number	I _{PR} (A)	Sens(Typ) at V _{CC} = 5 V (mV/A)	T _A (°C)	Packing*
ACS724LLCTR-2P5AB-T	±2.5	800		
ACS724LLCTR-05AB-T	±5	400		
ACS724LLCTR-10AU-T	10	400		
ACS724LLCTR-10AB-T	±10	200		
ACS724LLCTR-20AU-T	20	200	10 to 150	Tana and Baal, 2000 pieces per real
ACS724LLCTR-20AB-T	±20	100	-40 to 150	Tape and Reel, 3000 pieces per reel
ACS724LLCTR-30AU-T	30	133		
ACS724LLCTR-30AB-T	±30	66		
ACS724LLCTR-40AU-T	40	100		
ACS724LLCTR-50AB-T	±50	40		

*Contact Allegro for additional packing options.



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SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Notes	Rating	Units
Supply Voltage	V _{CC}		6	V
Reverse Supply Voltage	V _{RCC}		-0.1	V
Output Voltage	V _{IOUT}		V _{CC} + 0.5	V
Reverse Output Voltage	V _{RIOUT}		-0.1	V
Maximum Continuous Current	I _{CMAX}	T _A = 25°C	55	А
Operating Ambient Temperature	T _A	Range L	-40 to 150	°C
Junction Temperature	T _J (max)		165	°C
Storage Temperature	T _{stg}		-65 to 165	°C

ESD RATINGS

Characteristic	Symbol	Notes	Rating	Units
Human Body Model	V _{HBM}	Per AEC-Q100	±3.5	kV
Charged Device Model	V _{CDM}	Per AEC-Q100	±1	kV

ISOLATION CHARACTERISTICS

Characteristic	Symbol	Notes	Rating	Unit
Dielectric Surge Strength Test Voltage	ctric Surge Strength Test Voltage V _{SURGE} Tested ±5 pulses at 2/minute in compliance to IEC 61000-4-5 1.2 μs (rise) / 50 μs (width).		6000	V
Dielectric Strength Test Voltage	Strength Test Voltage V _{ISO} Agency type-tested for 60 seconds per UL standard 60950- 1 (edition 2); production-tested at V _{ISO} for 1 second, in accordance with UL 60950-1 (edition 2).		2400	V _{RMS}
Working Voltage for Basic Isolation	V _{WVBI}	Maximum approved working voltage for basic (single)	420	V_{pk} or VDC
Working voltage for Dasic Isolation	V WVBI	isolation according to UL 60950-1 (edition 2)	297	V _{rms}
Clearance	D _{cl}	Minimum distance through air from IP leads to signal leads.	4.2	mm
Creepage	D _{cr} Minimum distance along package body from IP leads to signal leads.		4.2	mm
Comparative Tracking Index	СТІ	Material Group II	400 to 599	V

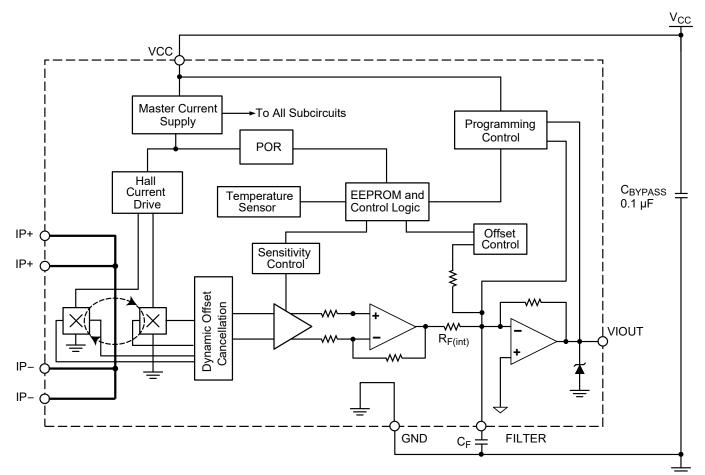
THERMAL CHARACTERISTICS

Characteristic	Symbol	Test Conditions*	Value	Units
Package Thermal Resistance (Junction to Ambient)	R _{θJA}	Mounted on the Allegro 85-0740 evaluation board with 1500 mm ² of 4 oz. copper on each side, connected to pins 1 and 2, and to pins 3 and 4, with thermal vias connecting the layers. Performance values include the power consumed by the PCB.	23	°C/W
Package Thermal Resistance (Junction to Lead)	R _{θJL}	Mounted on the Allegro ASEK724 evaluation board.	5	°C/W

*Additional thermal information available on the Allegro website.

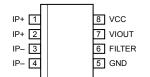


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Functional Block Diagram

PINOUT DIAGRAM AND TERMINAL LIST TABLE



Package LC, 8-Pin SOICN Pinout Diagram

Terminal List Table

Number	Name	Description
1, 2	IP+	Terminals for current being sensed; fused internally
3, 4	IP-	Terminals for current being sensed; fused internally
5	GND	Signal ground terminal
6	FILTER	Terminal for external capacitor that sets bandwidth
7	VIOUT	Analog output signal
8	VCC	Device power supply terminal



Automotive-Grade, Galvanically Isolated Current Sensor IC with Common-Mode Field Rejection in a Small-Footprint SOIC8 Package

COMMON ELECTRICAL CHARACTERISTICS ^[1]: Valid through the full range of T_A , V_{CC} = 5 V, C_F = 0, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Supply Voltage	V _{cc}		4.5	-	5.5	V
Supply Current	I _{CC}	V _{CC} = 5 V, output open	_	10	14	mA
Output Capacitance Load	CL	VIOUT to GND	_	_	10	nF
Output Resistive Load	RL	VIOUT to GND	4.7	-	_	kΩ
Primary Conductor Resistance	R _{IP}	T _A = 25°C	_	1.2	_	mΩ
Primary Conductor Inductance	L _{IP}	T _A = 25°C	_	2	_	nH
Internal Filter Resistance [2]	R _{F(int)}		_	1.8	_	kΩ
Common Mode Field Rejection Ratio	CMFRR	Uniform external magnetic field	-	40	_	dB
Primary Hall Coupling Factor	G1	T _A = 25°C	_	11	_	G/A
Secondary Hall Coupling Factor	G2	T _A = 25°C	_	2.8	_	G/A
Hall Plate Sensitivity Matching	Sens _{match}	T _A = 25°C	_	±1	_	%
Rise Time	t _r	T _A = 25°C, C _L = 1 nF	_	3	_	μs
Propagation Delay	t _{pd}	$T_A = 25^{\circ}C, C_L = 1 \text{ nF}$	_	2	_	μs
Response Time	t _{RESPONSE}	T _A = 25°C, C _L = 1 nF	-	4	_	μs
Output Slew Rate	SR	$T_{A} = 25^{\circ}C, C_{L} = 1 \text{ nF}$	-	0.53	_	V/µs
Bandwidth	BW	Small signal –3 dB; C _L = 1 nF	-	120	_	kHz
Noise Density	I _{ND}	Input-referenced noise density; $T_A = 25^{\circ}C, C_L = 1 \text{ nF}$	-	150	_	µA _(rms) / √Hz
Noise	I _N	Input-referenced noise: $C_F = 4.7 \text{ nF}$, $C_L = 1 \text{ nF}$, BW = 18 kHz, $T_A = 25^{\circ}C$	_	20	_	mA _(rms)
Nonlinearity	E _{LIN}	Through full range of I _P	-1.5	-	+1.5	%
Sensitivity Ratiometry Coefficient	SENS_RAT_ COEF	V_{CC} = 4.5 to 5.5 V, T_A = 25°C	_	1.3	_	_
Zero-Current Output Ratiometry Coefficient	QVO_RAT_ COEF	$V_{CC} = 4.5$ to 5.5 V, $T_A = 25^{\circ}C$	-	1	_	_
O - transform) (c. the me. [2]	V _{OH}	R _L = 4.7 kΩ	_	V _{CC} - 0.3	_	V
Saturation Voltage [3]	V _{OL}	R _L = 4.7 kΩ	_	0.3	_	V
Power-On Time	t _{PO}	T _A = 25°C	-	80	_	μs
Shorted Output-to-Ground Current	I _{SC(GND)}	T _A = 25°C	_	3.3	_	mA
Shorted Output-to-V _{CC} Current	I _{SC(VCC)}	T _A = 25°C	_	45		mA

^[1] Device may be operated at higher primary current levels, I_P, ambient temperatures, T_A, and internal leadframe temperatures, provided the Maximum Junction Temperature, T_J(max), is not exceeded.

 $^{[2]}$ R_{F(int)} forms an RC circuit via the FILTER pin.

[3] The sensor IC will continue to respond to current beyond the range of I_P until the high or low saturation voltage; however, the nonlinearity in this region will be worse than through the rest of the measurement range.



xLLCTR-2P5AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE						
Current Sensing Range	I _{PR}		-2.5	-	2.5	A
Sensitivity	Sens	I _{PR(min)} < I _P < I _{PR(max)}	-	800	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V
ACCURACY PERFORMANC	E			· · · · · ·		
Total Quite ut Erman ^[2]	E _{TOT}	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±1.5	2.5	%
Total Output Error ^[2]		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6.5	±4.5	6.5	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sonaitivity Error	-	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±1	2	%
Sensitivity Error	E _{sens}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4.5	6	%
Valtage Offect Error	M	I _P = 0 A, T _A = 25°C to 150°C	-20	±7	20	mV
Voltage Offset Error	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-40	±13	40	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-05AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE			·	· · · · · · · · · · · · · · · · · · ·		<u>^</u>
Current Sensing Range	I _{PR}		-5		5	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	400	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V
ACCURACY PERFORMANC	E			· · · · · · · · · · · · · · · · · · ·		<u>`</u>
Total Output Error ^[2]	E _{TOT}	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±1.5	2.5	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4.5	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Consitivity Error	_	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±1	2	%
Sensitivity Error	E _{sens}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±4.5	5.5	%
Valtage Offeet Error	V	I _P = 0 A, T _A = 25°C to 150°C	-15	±7	15	mV
Voltage Offset Error	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±13	30	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-10AU PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE				· · · · ·		
Current-Sensing Range	I _{PR}		0	-	10	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	400	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Unidirectional, I _P = 0 A	-	V _{CC} × 0.1	_	V
ACCURACY PERFORMANC	E			· · · · ·		
Total Output Error [2]	E _{TOT}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2.5	±1.5	2.5	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4.5	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Consitivity Error	_	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±1	2	%
Sensitivity Error	E _{sens}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±4.5	5.5	%
Voltage Offeet Error	<i>\</i> /	I _P = 0 A, T _A = 25°C to 150°C	-15	±7	15	mV
Voltage Offset Error	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±13	30	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-10AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE				· · · ·		
Current-Sensing Range	I _{PR}		-10	-	10	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	200	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V
ACCURACY PERFORMANC	E			· · · · ·		
Total Output Error ^[2]	E _{TOT}	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±1	2	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4.5	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Consitivity Error	_	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-1.5	±1	1.5	%
Sensitivity Error	E _{sens}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±4.5	5.5	%
Voltage Offeet Error	<i>\</i> /	I _P = 0 A, T _A = 25°C to 150°C	-10	±6	10	mV
Voltage Offset Error	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±8	30	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-20AU PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.[1]	Max.	Unit
NOMINAL PERFORMANCE				· · · · ·		
Current-Sensing Range	I _{PR}		0	-	20	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	200	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Unidirectional, I _P = 0 A	-	V _{CC} × 0.1	_	V
ACCURACY PERFORMANC	E			· · · · ·		
Tatal Quiter it Ennen [2]	E _{TOT}	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±0.7	2	%
Total Output Error ^[2]		$I_P = I_{PR(max)}$, $T_A = -40^{\circ}C$ to 25°C	-6	±4	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sonoitivity Error	-	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-1.5	±0.7	1.5	%
Sensitivity Error	E _{sens}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±4	5.5	%
Valtana Offact Erran		I _P = 0 A, T _A = 25°C to 150°C	-10	±6	10	mV
Voltage Offset Error	V _{OE}	$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±8	30	mV
LIFETIME DRIFT CHARACT	ERISTICS			`		
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-20AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE				· · · · ·		<u>.</u>
Current-Sensing Range	I _{PR}		-20	-	20	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	100	-	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V
ACCURACY PERFORMANC	E					
Total Output Error ^[2]	E _{TOT}	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±0.8	2	%
		$I_P = I_{PR(max)}$, $T_A = -40^{\circ}$ C to 25°C	-6	±4	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	E _{sens}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-1.5	±0.6	1.5	%
		$I_P = I_{PR(max)}$, $T_A = -40^{\circ}C$ to 25°C	-5.5	±4	5.5	%
Voltage Offeet Error	V _{OE}	I _P = 0 A, T _A = 25°C to 150°C	-10	±5	10	mV
Voltage Offset Error		$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±6	30	mV
LIFETIME DRIFT CHARACT	ERISTICS			· · · · · ·		-
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-30AU PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE				· · · · · · · · · · · · · · · · · · ·		
Current-Sensing Range	I _{PR}		0		30	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	133	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Unidirectional, I _P = 0 A	-	V _{CC} × 0.1	_	V
ACCURACY PERFORMANC	E			· · · · · · · · · · · · · · · · · · ·		
Total Output Error ^[2]	E _{TOT}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±0.7	2	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	-	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-1.5	±0.7	1.5	%
	E _{sens}	$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±4	5.5	%
Voltage Offect Error	V _{OE}	I _P = 0 A, T _A = 25°C to 150°C	-10	±6	10	mV
Voltage Offset Error		$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±7	30	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-30AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE				· · · · · · · · · · · · · · · · · · ·		
Current-Sensing Range	I _{PR}		-30	-	30	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	66	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V
ACCURACY PERFORMANC	E					
Total Output Error ^[2]	E _{TOT}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±0.8	2	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	E _{sens}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-1.5	±0.8	1.5	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±4	5.5	%
Voltage Offect Error	V _{OE}	I _P = 0 A, T _A = 25°C to 150°C	-10	±6	10	mV
Voltage Offset Error		$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±6	30	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.



xLLCTR-40AU PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE			·	· · · · · · · · · · · · · · · · · · ·		<u>^</u>
Current-Sensing Range	I _{PR}		0	-	40	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	100	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Unidirectional, I _P = 0 A	-	V _{CC} × 0.1	_	V
ACCURACY PERFORMANC	E		·	· · · · · · · · · · · · · · · · · · ·		<u>`</u>
Total Output Error ^[2]	E _{TOT}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±0.7	2	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	E _{sens}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-1.5	±0.7	1.5	%
		$I_P = I_{PR(max)}$, $T_A = -40^{\circ}C$ to 25°C	-5.5	±4	5.5	%
Valtage Offeet Error	V _{OE}	I _P = 0 A, T _A = 25°C to 150°C	-10	±6	10	mV
Voltage Offset Error		$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±7	30	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.

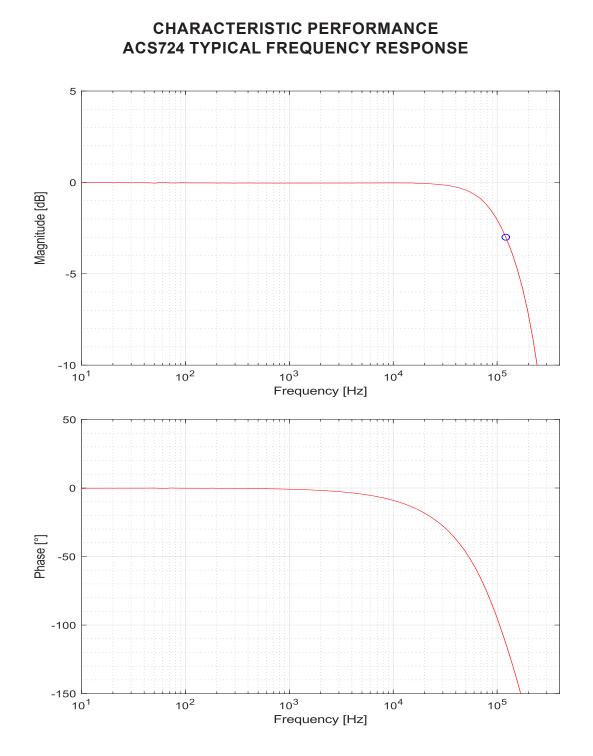


xLLCTR-50AB PERFORMANCE CHARACTERISTICS: T_A Range L, valid at $T_A = -40$ °C to 150°C, $V_{CC} = 5$ V, $C_F = 0$, unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ. ^[1]	Max.	Unit
NOMINAL PERFORMANCE				· · · · · · · · · · · · · · · · · · ·		
Current-Sensing Range	I _{PR}		-50	-	50	A
Sensitivity	Sens	$I_{PR(min)} < I_P < I_{PR(max)}$	-	40	_	mV/A
Zero-Current Output Voltage	V _{IOUT(Q)}	Bidirectional, I _P = 0 A	-	V _{CC} × 0.5	_	V
ACCURACY PERFORMANC	E			· · · · · · · · · · · · · · · · · · ·		
Total Output Error ^[2]	E _{TOT}	$I_P = I_{PR(max)}, T_A = 25^{\circ}C \text{ to } 150^{\circ}C$	-2	±0.8	2	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-6	±4	6	%
TOTAL OUTPUT ERROR CO	MPONENT	S ^[3] E _{TOT} = E _{SENS} + 100 × V _{OE} /(Sens × I _P)				
Sensitivity Error	E _{sens}	$I_{P} = I_{PR(max)}, T_{A} = 25^{\circ}C \text{ to } 150^{\circ}C$	-1.5	±0.8	1.5	%
		$I_P = I_{PR(max)}, T_A = -40^{\circ}C \text{ to } 25^{\circ}C$	-5.5	±4	5.5	%
Voltage Offect Error	V _{OE}	I _P = 0 A, T _A = 25°C to 150°C	-10	±6	10	mV
Voltage Offset Error		$I_{P} = 0 \text{ A}, T_{A} = -40^{\circ}\text{C} \text{ to } 25^{\circ}\text{C}$	-30	±6	30	mV
LIFETIME DRIFT CHARACT	ERISTICS					
Sensitivity Error Lifetime Drift	E _{sens_drift}		-3	±1	3	%
Total Output Error Lifetime Drift	E _{tot_drift}		-3	±1	3	%

^[1] Typical values with +/- are 3 sigma values.





For information regarding bandwidth characterization methods used for the ACS724, see the "Characterizing System Bandwidth" application note (https://allegromicro.com/en/insights-and-innovations/technical-documents/hall-effect-sensor-ic-publications/an-effective-method-for-characterizing-system-bandwidth-an296169) on the Allegro website.



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RESPONSE CHARACTERISTICS DEFINITIONS AND PERFORMANCE DATA

Response Time (t_{RESPONSE})

The time interval between a) when the sensed input current reaches 90% of its final value, and b) when the sensor output reaches 90% of its full-scale value.

Propagation Delay (tpd)

The time interval between a) when the sensed input current reaches 20% of its full-scale value, and b) when the sensor output reaches 20% of its full-scale value.

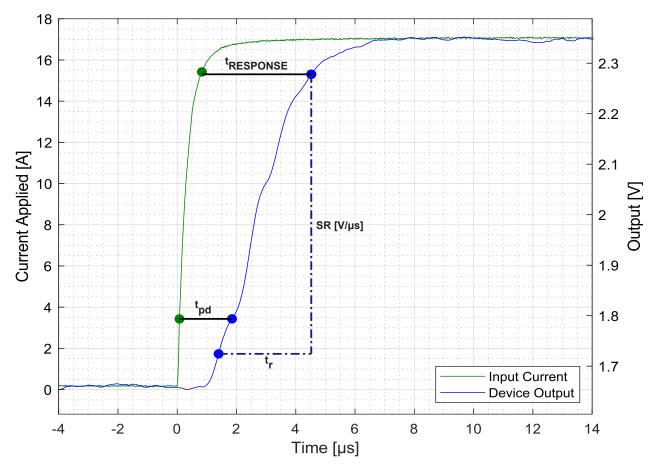
Rise Time (t_r)

The time interval between a) when the sensor reaches 10% of its full-scale value, and b) when it reaches 90% of its full-scale value.

Output Slew Rate (SR)

The rate of change $[V/\mu s]$ in the output voltage from a) when the sensor reaches 10% of its full-scale value, and b) when it reaches 90% of its full-scale value.

Response Time, Propagation Delay, Rise Time, and Output Slew Rate Applied current step with 10%-90% rise time = 1 μ s Test Conditions: T_A = 25°C, C_{BYPASS} = 0.1 μ F, C_L = 0 F





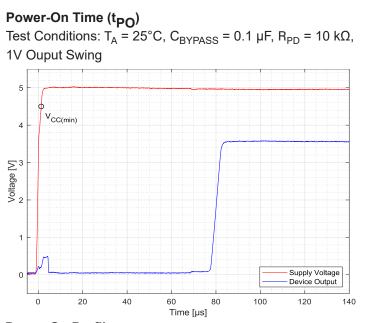
POWER ON FUNCTIONAL DESCRIPTION AND PERFORMANCE DATA

Power-On Time (tpO)

When the supply is ramped to its operating voltage, the device requires a finite amount of time to power its internal components before responding to an input magnetic field. Power-On Time (t_{PO}) is defined as the time interval between a) when the power supply has reached its minimum specified operating voltage $(V_{CC(min)})$, and b) when the sensor output has settled within $\pm 10\%$ of its steady-state value under an applied magnetic field.

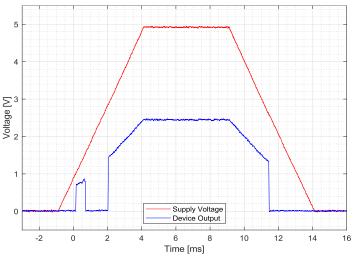
Power-On Profile

After applying power, the part remains off in a known state referred to as Power-on Reset, or POR. The device stays in this state until the voltage reaches a point at which the device will remain powered. The power-on profile below illustrates the intended power on/off. A pull-down resistor was used on the output of the tested device.



Power-On Profile

Supply voltage ramp rate = 1V/ms Test Conditions: T_A = 25°C, C_{BYPASS} = 0.1 μ F, R_{PD} = 10 k Ω





APPLICATION INFORMATION

Estimating Total Error vs. Sensed Current

The Performance Characteristics tables give distribution (±3 sigma) values for Total Error at $I_{PR(max)}$; however, one often wants to know what error to expect at a particular current. This can be estimated by using the distribution data for the components of Total Error, Sensitivity Error, and Voltage Offset Error. The ±3 sigma value for Total Error (E_{TOT}) as a function of the sensed current (I_p) is estimated as:

$$E_{TOT}(I_p) = \sqrt{E_{SENS}^2 + \left(\frac{100 \times V_{OE}}{Sens \times I_p}\right)^2}$$

Here, E_{SENS} and V_{OE} are the ± 3 sigma values for those error terms. If there is an average sensitivity error or average offset voltage, then the average Total Error is estimated as:

$$E_{\text{TOT}_{AVG}}(I_p) = E_{\text{SENS}_{AVG}} + \frac{100 \times V_{\text{OE}_{AVG}}}{\text{Sens} \times I_p}$$

The resulting total error will be a sum of E_{TOT} and E_{TOT}_{AVG} . Using these equations and the 3 sigma distributions for Sensitivity Error and Voltage Offset Error, the Total Error versus sensed current (I_p) is below for the ACS724LLCTR-20AB. As expected, as one goes towards zero current, the error in percent goes towards infinity due to division by zero (refer to Figure 1).

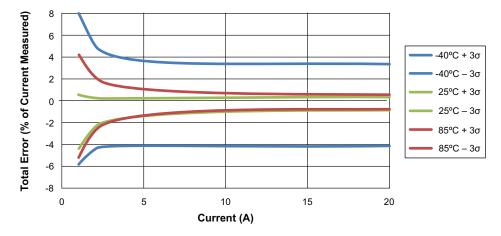


Figure 1: Predicted Total Error as a Function of the Sensed Current for the ACS724LLCTR-20AB

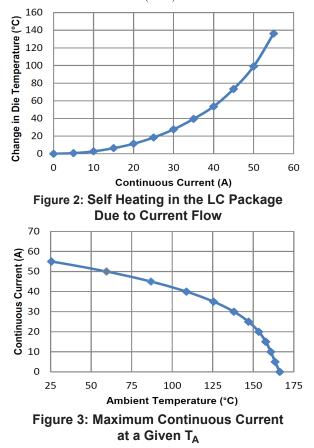


Thermal Rise vs. Primary Current

Self-heating due to the flow of current should be considered during the design of any current sensing system. The sensor, printed circuit board (PCB), and contacts to the PCB will generate heat as current moves through the system.

The thermal response is highly dependent on PCB layout, copper thickness, cooling techniques, and the profile of the injected current. The current profile includes peak current, current "on-time", and duty cycle. While the data presented in this section was collected with Direct-Current (DC), these numbers may be used to approximate thermal response for both AC signals and current pulses.

The plot in Figure 2 shows the measured rise in steady-state die temperature of the ACS724 versus continuous current at an ambient temperature, T_A , of 25 °C. The thermal offset curves may be directly applied to other values of T_A . Conversely, Figure 3 shows the maximum continuous current at a given T_A . Surges beyond the maximum current listed in Figure 3 are allowed given the maximum junction temperature, $T_{J(MAX)}$ (165°C), is not exceeded.



The thermal capacity of the ACS724 should be verified by the end user in the application's specific conditions. The maximum junction temperature, $T_{J(MAX)}$ (165°C), should not be exceeded. Further information on this application testing is available in the <u>DC</u> and <u>Transient Current Capability</u> application note on our website.

ASEK724 Evaluation Board Layout

Thermal data shown in Figure 2 and Figure 3 was collected using the ASEK724 Evaluation Board (TED-85-0740-003). This board includes 1500 mm² of 4 oz. copper (0.1388 mm) connected to pins 1 and 2, and to pins 3 and 4, with thermal vias connecting the layers. Top and Bottom layers of the PCB are shown below in Figure 4.

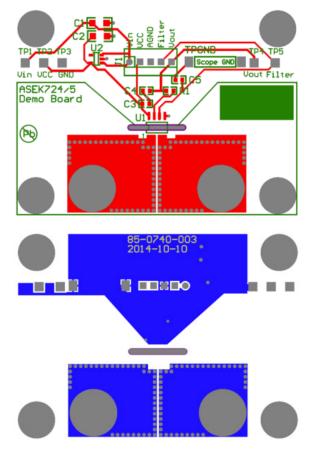


Figure 4: Top and Bottom Layers for ASEK724 Evaluation Board

Gerber files for the ASEK724 evaluation board are available for download from the Allegro website. Please see the technical documents section of the <u>ACS724</u> device webpage.



DEFINITIONS OF ACCURACY CHARACTERISTICS

Sensitivity (Sens). The change in sensor IC output in response to a 1A change through the primary conductor. The sensitivity is the product of the magnetic circuit sensitivity (G/A) (1 G = 0.1 mT) and the linear IC amplifier gain (mV/G). The linear IC amplifier gain is programmed at the factory to optimize the sensitivity (mV/A) for the full-scale current of the device.

Nonlinearity (E_{LIN}). The nonlinearity is a measure of how linear the output of the sensor IC is over the full current measurement range. The nonlinearity is calculated as:

$$E_{LIN} = \left\{ I - \left[\frac{V_{IOUT}(I_{PR(max)}) - V_{IOUT(Q)}}{2 \cdot V_{IOUT}(I_{PR(max)}/2) - V_{IOUT(Q)}} \right] \right\} \cdot 100(\%)$$

where $V_{IOUT}(I_{PR}(max))$ is the output of the sensor IC with the maximum measurement current flowing through it and $V_{IOUT}(I_{PR}(max)/2)$ is the output of the sensor IC with half of the maximum measurement current flowing through it.

Zero-Current Output Voltage (V_{IOUT(Q)}). The output of the sensor when the primary current is zero. For a unipolar supply voltage, it nominally remains at $0.5 \times V_{CC}$ for a bidirectional device and $0.1 \times V_{CC}$ for a unidirectional device. For example, in the case of a bidirectional output device, $V_{CC} = 5$ V translates into $V_{IOUT(Q)} = 2.5$ V. Variation in $V_{IOUT(Q)}$ can be attributed to the resolution of the Allegro linear IC quiescent voltage trim and thermal drift.

Voltage Offset Error (V_{OE}). The deviation of the device output from its ideal quiescent value of $0.5 \times V_{CC}$ (bidirectional) or 0.1 $\times V_{CC}$ (unidirectional) due to nonmagnetic causes. To convert this voltage to amperes, divide by the device sensitivity, Sens.

Total Output Error (E_{TOT}). The difference between the current measurement from the sensor IC and the actual current (I_p), relative to the actual current. This is equivalent to the difference between the ideal output voltage and the actual output voltage, divided by the ideal sensitivity, relative to the current flowing through the primary conduction path:

$$E_{\text{TOT}}(I_p) = \frac{V_{\text{IOUT_ideal}}(I_p) - V_{\text{IOUT}}(I_p)}{Sens_{ideal}(I_p) \bullet I_p} \bullet 100 \ (\%)$$

The Total Output Error incorporates all sources of error and is a function of I_P. At relatively high currents, E_{TOT} will be mostly due to sensitivity error, and at relatively low currents, E_{TOT} will be mostly due to Voltage Offset Error (V_{OE}). In fact, at I_P = 0, E_{TOT} approaches infinity due to the offset. This is illustrated in Figure 5 and Figure 6. Figure 5 shows a distribution of output voltages versus I_P at 25°C and across temperature. Figure 6 shows the corresponding E_{TOT} versus I_P.

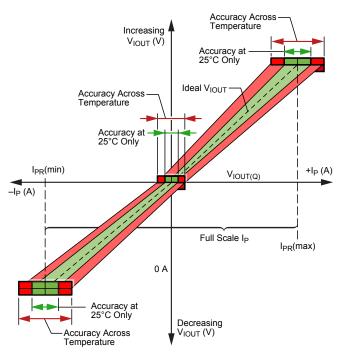


Figure 5: Output Voltage versus Sensed Current

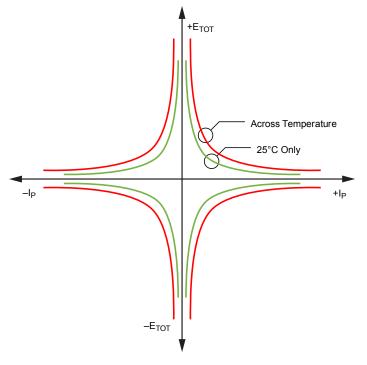


Figure 6: Total Output Error versus Sensed Current



Sensitivity Ratiometry Coefficient (SENS_RAT_COEF). The coefficient defining how the sensitivity scales with V_{CC} . The ideal coefficient is 1, meaning the sensitivity scales proportionally with V_{CC} . A 10% increase in V_{CC} results in a 10% increase in sensitivity. A coefficient of 1.1 means that the sensitivity increases by 10% more than the ideal proportionality case. This means that a 10% increase in V_{CC} results in an 11% increase in sensitivity. This relationship is described by the following equation:

$$Sens(V_{cc}) = Sens(5 V) \left[1 + \frac{(V_{cc} - 5 V) \cdot SENS_RAT_COEF}{5 V} \right]$$

This can be rearranged to define the sensitivity ratiometry coefficient as:

$$SENS_RAT_COEF = \left[\frac{Sens(V_{cc})}{Sens(5 V)} - 1\right] \bullet \frac{5 V}{(V_{cc} - 5 V)}$$

Zero-Current Output Ratiometry Coefficient (QVO_RAT_

COEF). The coefficient defining how the zero-current output voltage scales with V_{CC} . The ideal coefficient is 1, meaning the output voltage scales proportionally with V_{CC} , always being equal to $V_{CC}/2$. A coefficient of 1.1 means that the zero-current output voltage increases by 10% more than the ideal proportionality case. This means that a 10% increase in V_{CC} results in an 11% increase in the zero-current output voltage. This relationship is described by the following equation:

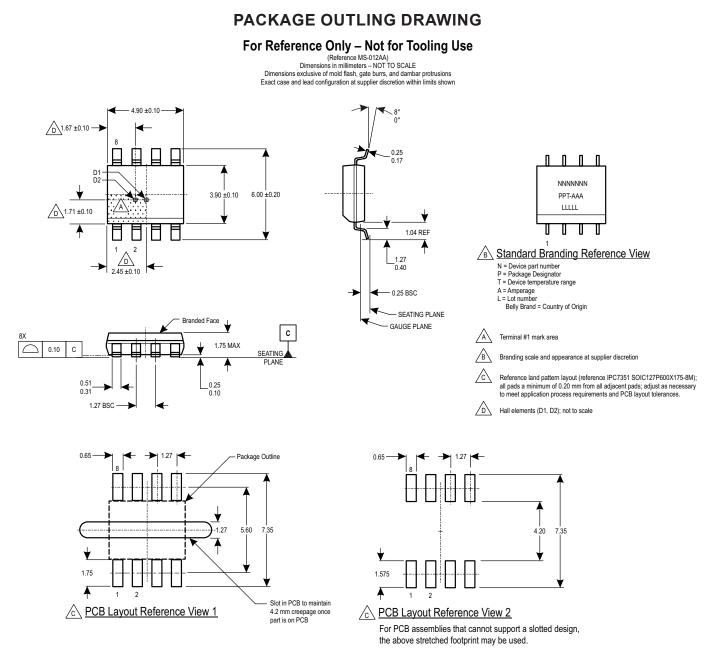
$$VIOUTQ(V_{cc}) = VIOUTQ(5 V) \left[1 + \frac{(V_{cc} - 5 V) \cdot QVO_RAT_COEF}{5 V} \right]$$

This can be rearranged to define the zero-current output ratiometry coefficient as:

$$QVO_RAT_COEF = \left[\frac{VIOUTQ(V_{cc})}{VIOUTQ(5 V)} - 1\right] \bullet \frac{5 V}{(V_{cc} - 5 V)}$$



Automotive-Grade, Galvanically Isolated Current Sensor IC with Common-Mode Field Rejection in a Small-Footprint SOIC8 Package







Automotive-Grade, Galvanically Isolated Current Sensor IC with Common-Mode Field Rejection in a Small-Footprint SOIC8 Package

Revision History

Number	Descriptioon	Pages	Responsible	Date	
_	Added Characteristic Performance graphs and Application Information to Preliminary draft to create Final draft	All	A. Latham	January 16, 2015	
1	Corrected Features and Benefits	2	A. Latham	June 19, 2015	
2	Added ACS724LLCTR-50AB-T variant with electrical characteristics	2, 9	A. Latham	June 23, 2015	
3	Corrected Characteristic Performance graph legends; updated Lifetime Drift Characteristics and added Error Over Lifetime electrical characteristics	6-18	A. Latham, S. Milano	August 12, 2015	
4	Added ACS724LLCTR-05AB-T variant with electrical characteristics	2, 6	W. Bussing	August 8, 2016	
5	Added AEC-Q100 qualified status	1	W. Bussing	June 28, 2017	
6	Added ACS724LLCTR-05AB-T and ACS724LLCTR-50AB-T Characteristic Performance graphs	14, 21	W. Bussing	August 3, 2017	
7	Updated Clearance and Creepage rating values	3	W. Bussing	January 10, 2018	
8	Added Dielectric Surge Strength Test Voltage characteristic	2	W/ Duccing	January 22, 2019	
8	Added Common Mode Field Rejection Ratio characteristic	5	W. Bussing	January 23, 2018	
9	Added ACS724LLCTR-2P5AB-T variant with electrical characteristics	2, 6	W/ Duccing	April 12, 2019	
9	Updated PCB Layout References in Package Outline Drawing	27	W. Bussing	April 13, 2018	
	Added Hall dimensions in Package Outline Drawing	27		May 14, 2018	
10	Added ACS724LLCTR-40AU-T variant with electrical characteristics and performance graphs	2, 14, 23	W. Bussing		
11	Added ACS724LLCTR-2P5AB-T performance graphs	16	M. McNally	luno 22, 2019	
11	Added Typical Frequency Response plots	26	W. Bussing	– June 22, 2018	
12	Added "Thermal Rise vs. Primary Current" and "ASEK724/5 Evaluation Board Layout" to the Applications Information section	28	W. Bussing	July 3, 2018	
13	Corrected ACS724LLCTR-40AU-T Total Output Error and Sensitivity Error values	14	M. McNally	November 15, 2018	
14	Updated certificate numbers	1	V. Mach	December 13, 2018	
15	Updated TUV certificate mark	1	M. McNally	June 3, 2019	
16	Added Maximum Current value to Absolute Maximum Ratings table; added ESD Ratings Table; updated Isolation Characteristics Table; updated Rise Time, Response Time, Propagation Delay, and Output Slew Rate test conditions; added Primary Conductor Inductance and Output Slew Rate values; added Typical Frequency Response application page; added Response Characteristics Definitions and Performance Data; added Power On Functional Description and Performance Data; added thermal data section; corrected Voltage Offset to Voltage Offset Error	All	K. Hampton	April 3, 2020	
17	Updated Functional Block Diagram	4	K. Hampton	February 1, 2021	



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