General Description

The AGM3400EL combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

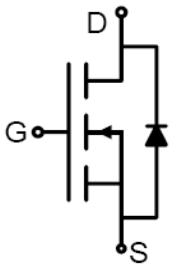
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
30V	20mΩ	5.6A

SOT-23 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3400EL	AGM3400EL	SOT-23	----	----	3000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	30	V
VGS	Gate-Source Voltage (VDS=0V)	±12	V
ID	Drain Current-Continuous(TA=25℃) **(Note 1)**	5.6	A
	Drain Current-Continuous(TA=70℃)	4.5	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed **(Note 2)**	23	A
PD	Maximum Power Dissipation(TA=25℃)	1.2	w
	Maximum Power Dissipation(TA=70℃)	0.8	w
EAS	Avalanche energy **(Note 3)**	--	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	℃

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State)[1]	---	104	℃/W
RθJC	Thermal Resistance Junction-Case[1]	---	--	℃/W

Table 3. Electrical Characteristics (TA=25℃unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	30	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=30V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±12V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	0.65	0.9	1.5	V
gFS	Forward Transconductance	VDS=5V,ID=4.2A	--	--	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=5.6A	--	20	25	mΩ
		VGS=4.5V, ID=5.0A	--	23	31	mΩ
		VGS=2.5V, ID=3A	--	27	45	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=15V,VGS=0V, F=1MHZ	--	630	--	pF
Coss	Output Capacitance		--	55	--	pF
Crss	Reverse Transfer Capacitance		--	71	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	--	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=15V, RGEN=3Ω,ID=5.6A	--	4.4	--	nS
tr	Turn-on Rise Time		--	28	--	nS
td(off)	Turn-Off Delay Time		--	16	--	nS
tf	Turn-Off Fall Time		--	26	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=15V, ID=5.6A	--	17	--	nC
Qgs	Gate-Source Charge		--	2.1	--	nC
Qgd	Gate-Drain Charge		--	2.0	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	5.6	A
VSD	Forward on Voltage	VGS=0V,IS=5.6A	--	--	1.2	V
trr	Reverse Recovery Time	IF=5.6A , dI/dt=100A/μs , TJ=25℃	--	1.1	--	ns
Qrr	Reverse Recovery Charge		--	13.1	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25℃

■ Typical Performance Characteristics

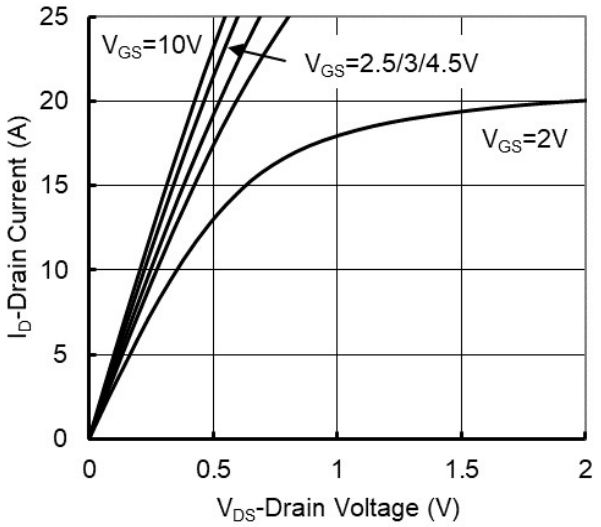


Figure1. Output Characteristics

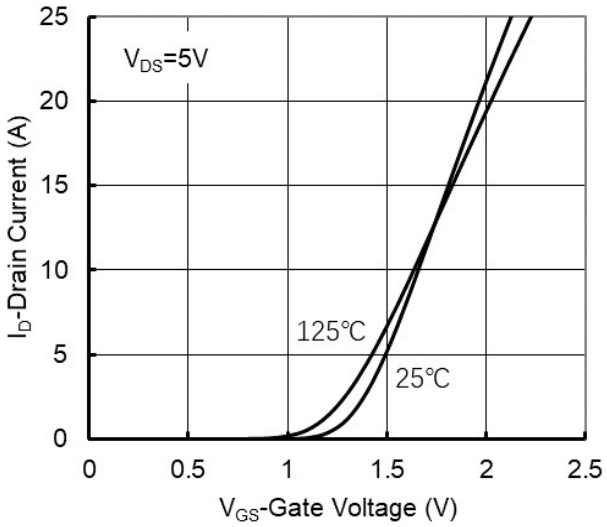


Figure2. Transfer Characteristics

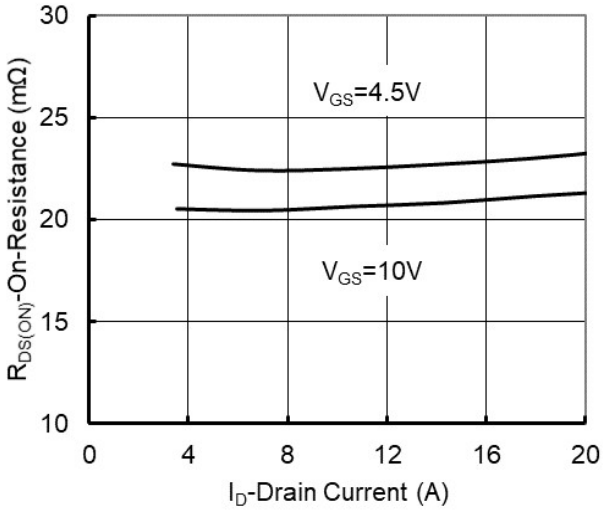


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

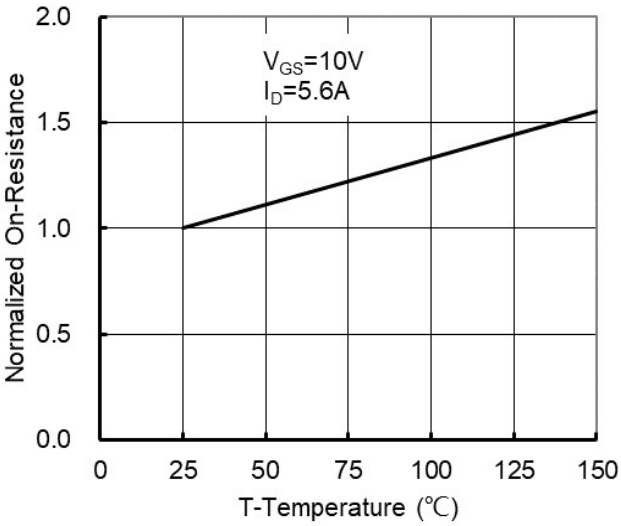


Figure 4: On-Resistance vs. Junction Temperature

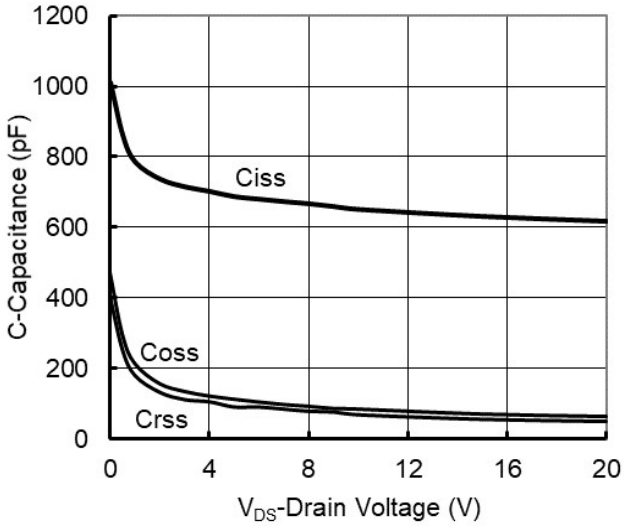


Figure5. Capacitance Characteristics

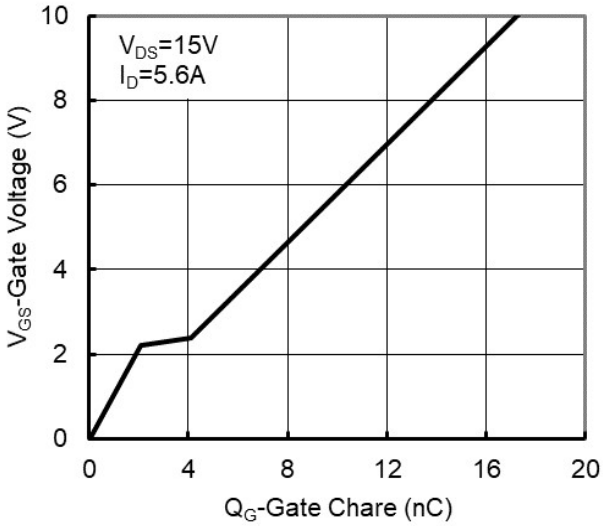


Figure6. Gate Charge

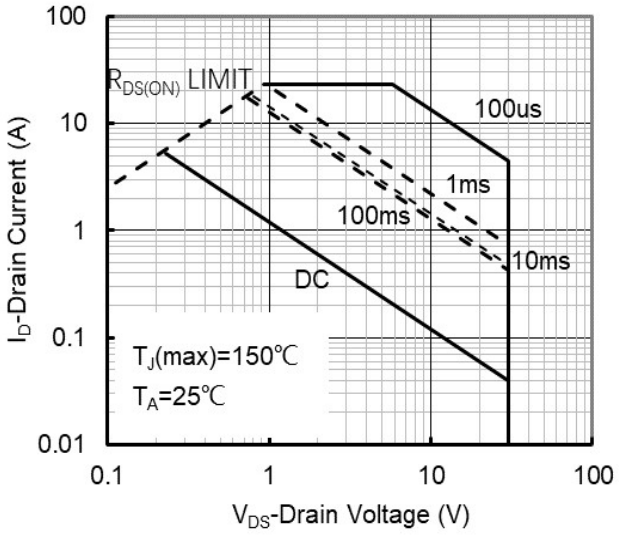


Figure7. Safe Operation Area

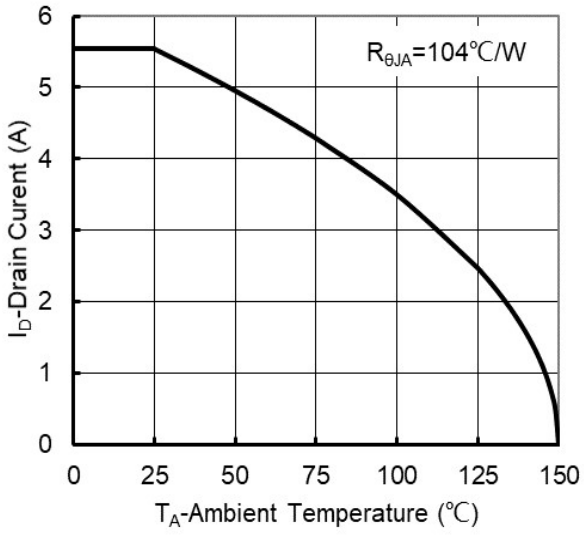


Figure8. Maximum Continuous Drain Current vs Ambient Temperature

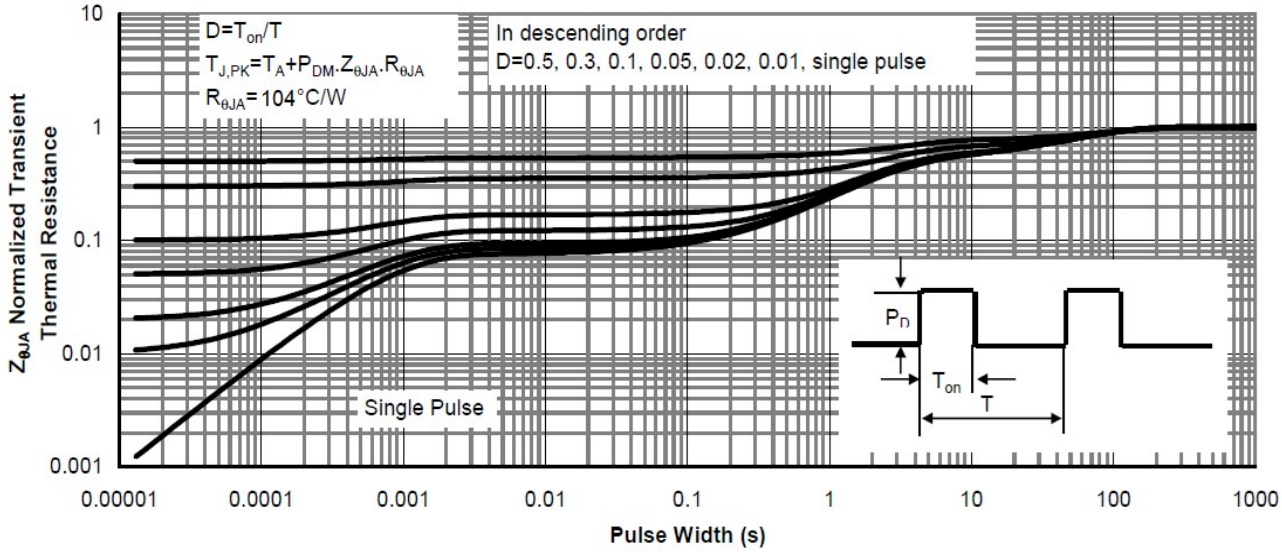
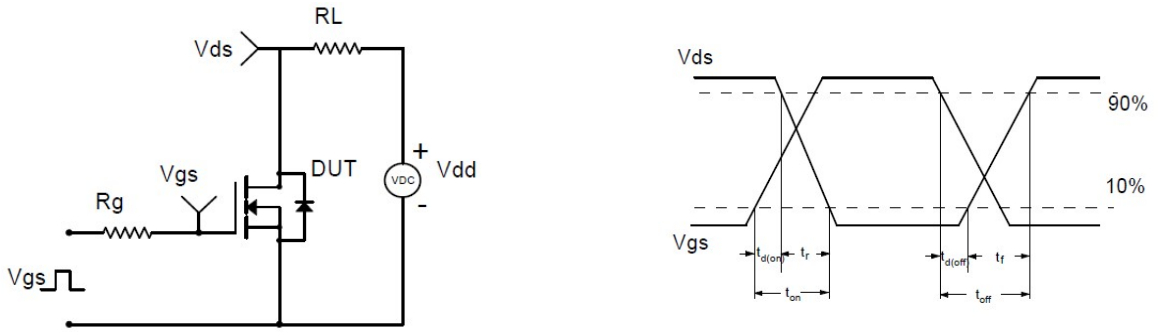
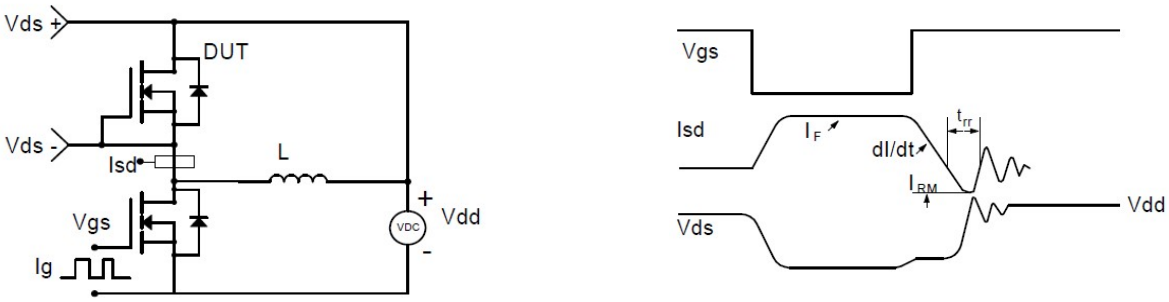


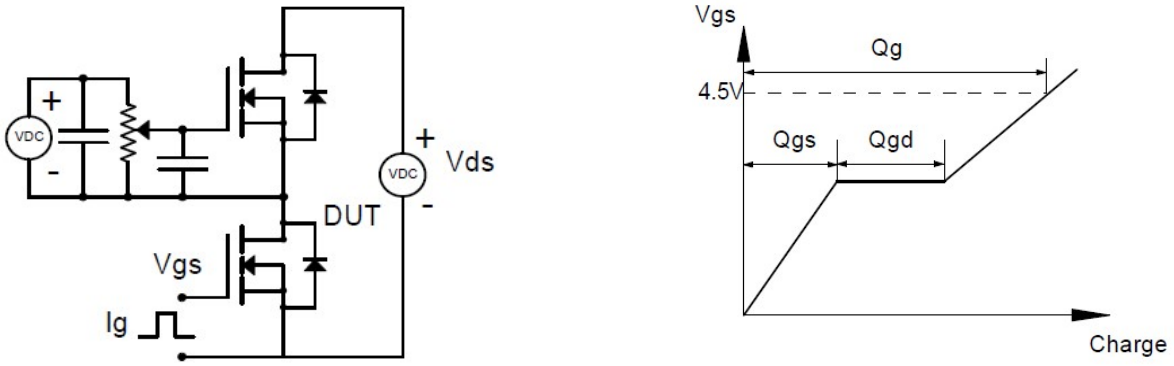
Figure9. Normalized Maximum Transient Thermal Impedance



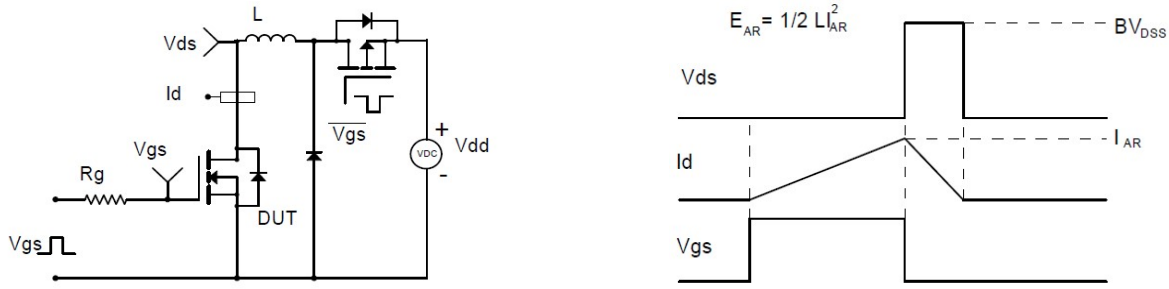
Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

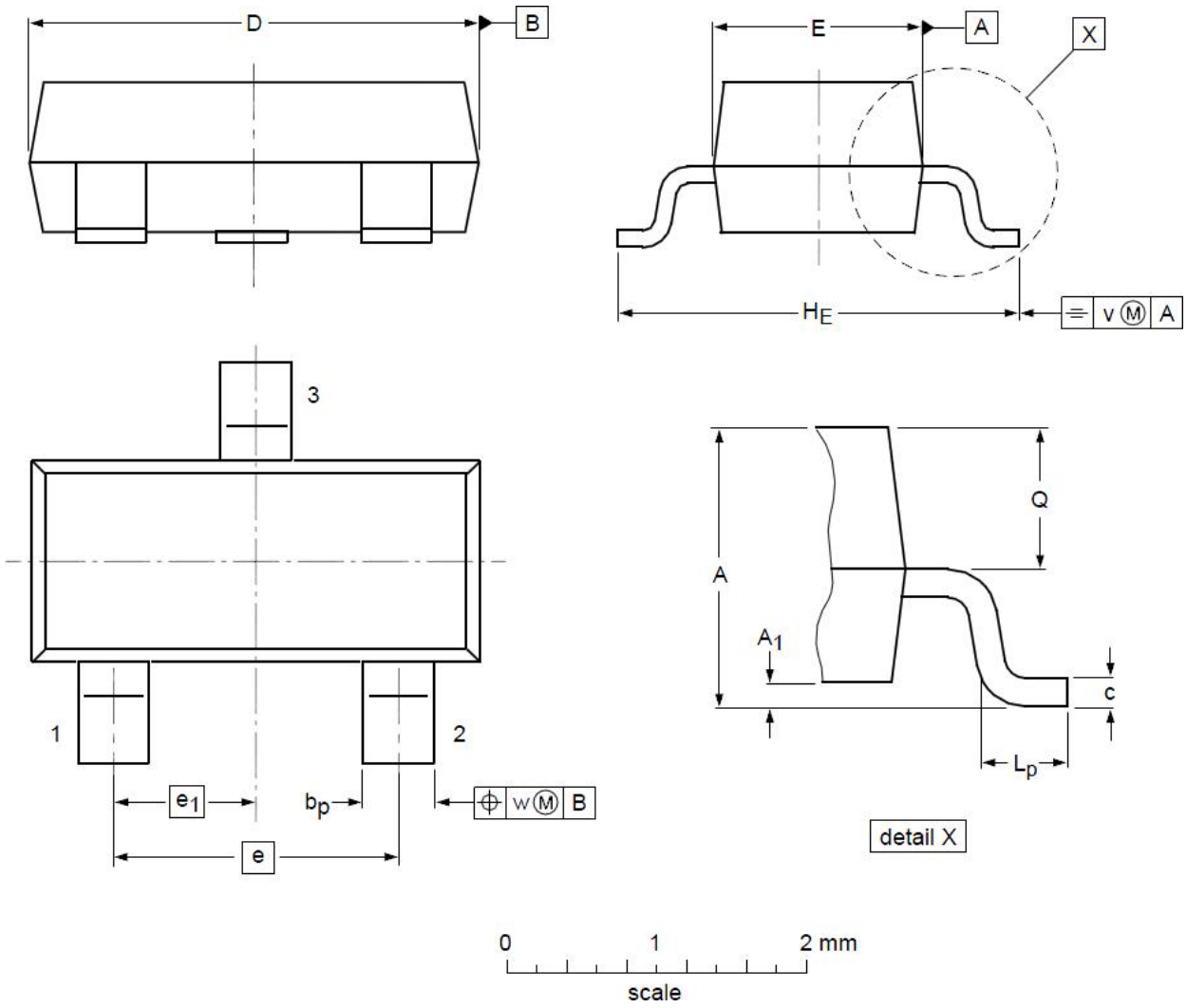


Gate Charge Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

Package Mechanical Data-SOT-23



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	**A_1**	0.01	0.05	0.10
b_p	0.30	0.42	0.50	**c**	0.08	0.13	0.15
D	2.80	2.92	3.00	**E**	1.20	1.33	1.40
e	--	1.90	--	**e_1**	--	0.95	--
H_E	2.25	2.40	2.55	**L_p**	0.30	0.42	0.50
Q	0.45	0.49	0.55	**v**	--	0.20	--
w	--	0.10	--				

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This document is the second version issued on October 10, 2022. This document replaces andReplace all previously provided information.