

SEA & LAND ELECTRONIC CORP.

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ALPHA-TOP TECHNOLOGY CORP.

## APPROVAL SHEET

MODEL NO.:	mSMD030-60V	
CUSTOMER:		
CUSTOMER'S APPROVA	AL:	
AUTHORIZED SIGNATU	DE/STAMD	
AUTHORIZED SIGNATOR	NE/STAWF:	
DATE		

MANUFACTURER:

HEAD OFFICE:

 $13F., No. 120\text{-}10, Sec. 3, Zhongshan\ Rd., Zhonghe\ Dist., New\ Taipei\ City\ 23544, Taiwan$ 

Tel: 886-2-8221-2567 Fax:882-2-2225-7268

E-mail:service@chipfast.com.tw

China Branch:

Factory Building B)Shuangpeng,Weibu Village, Qiuchang Town, Huiyang District, Huizhou City, Guangdong Province, P.R.C.)

Tel: 86-752-3562001 Fax:86-752-3558696 E-mail:service@atpptc.com

Submitted by: Chung Cheng Approved by: YC Lin DATE: 2-Mar-22

SEA & LAND ELECTRONIC CORP.



## mSMD030-60V

Ensturne

■ Surface Mount Devices

■ Lead free device

Size 4.5\*3.2 mm/0.18\*0.12 inchSurface Mount packaging

for automated assembly

Applications

Almost anywhere there is a low voltage power supply, up to 60V and a load to be

protected, including:

- Computer mother board, Modem. USB hub■ PDAs & Charger, Analog & digital line card
- Digital cameras, Disk drivers, CD-ROMs,

Alpha-Top (Sea & Land Alliance)

**Performance Specification** 

						Maxi	mum	Resis	tance		
Model	$V_{max}$	I <sub>max</sub>	I <sub>hold</sub>	I <sub>trip</sub>	$P_d$	Time 1	To Trip			Agency A	Approval
Wodel			@25°C	@25°C	Тур.	Current	Time	Ri <sub>min</sub>	R1 <sub>max</sub>	UL	TUV
	(Vdc)	(A)	(A)	(A)	(W)	(A)	(Sec)	$(\Omega)$	(Ω)	OL	100
mSMD030-60V	60	100	0.30	0.60	8.0	8.0	0.10	0.250	3.000		√

Ihold = Hold Current. Maximum current device will not trip in 25°C still air.

Itrip = Trip Current. Minimum current at which the device will always trip in 25°C still air.

Vmax = Maximum operating voltage device can withstand without damage at rated current (Imax).

Imax = Maximum fault current device can withstand without damage at rated voltage (Vmax).

Pd = Power dissipation when device is in the tripped state in 25°C still air environment at rated voltage.

Rimin/max = Minimum/Maximum device resistance prior to tripping at 25°C.

R1<sub>max</sub> = Maximum device resistance is measured one hour post reflow.

**CAUTION**: Operation beyond the specified ratings may result in damage and possible arcing and flame.

**Environmental Specifications** 

Test	Conditions	Resistance change					
Passive aging	+85°C, 1000 hrs.	±5% typical					
Humidity aging	+85°C, 85% R.H., 168 hours	±5% typical					
Thermal shock	+85°C to -40°C, 20 times	±33% typical					
Resistance to solvent	MIL-STD-202,Method 215	No change					
Vibration	MIL-STD-202,Method 201	No change					
Ambient operating conditions : - 40 °C to +85 °C							
Maximum surface temperature of the device in the tripped state is 125 °C							

## Agency Approvals :

Regulation/Standard:



R 50481056



2015/863/EU



EN14582

## I<sub>hold</sub> Versus Temperature

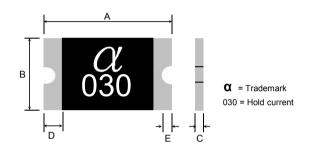
	noid reference									
Model Maximum ambient operating temperature ( $T_{mao}$ ) vs. hold current ( $I_{hold}$ )										
		-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
	mSMD030-60V	0.44	0.39	0.35	0.30	0.26	0.23	0.21	0.18	0.15

## mSMD030-60V

Construction And Dimension (Unit:mm)

Model		A		В	С В		E	
wodei	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Min.
mSMD030-60V	4.37	4.73	3.07	3.41	0.50	1.00	0.30	0.25

#### **Dimensions & Marking**



# Recommended Pad Layout (mm) 1.78 ±0.1 **1.78**±0.1

#### **Termination Pad Characteristics**

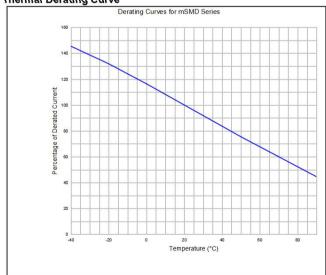
Terminal pad materials : Tin-plated Nickel-Copper

Terminal pad solderability: Meets EIA specification RS186-9E and ANSI/J-STD-002 Category 3.

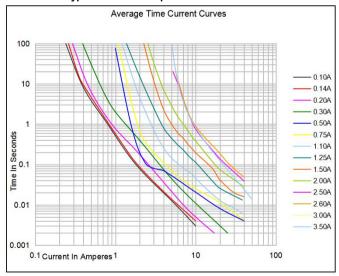
Rework

Use standard industry practices, the removal device must be replaced with a fresh one.

## Thermal Derating Curve



## Typical Time-To-Trip At 25°C

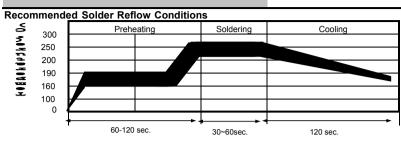


## WARNING:

- Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.

  PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are
- · Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
  · Use PPTC with a large inductance in circuit will generate a circuit voltage (L di/dt) above the rated voltage of the PPTC.
- Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.
- Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC SMD can be cleaned by standard methods.

## mSMD030-60V



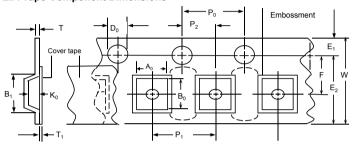
- Recommended reflow methods : IR, vapor phase oven, hot air oven.
- Devices are not designed to be wave soldered to the bottom side of the board.
- Recommended maximum paste thickness is 0.25 mm (0.010 inch).
- Devices can be cleaned using standard method and solvents

Note: If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

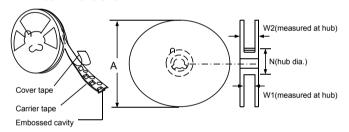
### Tape And Reel Specifications (mm)

0	F14 404 4
Governing Specifications	EIA 481-1
W	12 ± 0.3
P0	4.0 ± 0.10
P1	8.0 ± 0.10
P2	$2.0 \pm 0.05$
A0	$3.5 \pm 0.23$
B0	5.1 ± 0.15
B1max.	5.9
D0	1.5 + 0.1, -0
F	5.5 ± 0.05
E1	1.75 ± 0.10
E2min.	10.25
Tmax.	0.6
T1max.	0.1
K0	0.9 ± 0.15
Leader min.	390
Trailer min.	160
Reel Dimensions	
A max.	178
N min.	60
W1	12.4 + 2.0, -0.0
W2max.	18.4

#### **EIA Tape Component Dimensions**



#### **EIA Reel Dimensions**



## Storage And Handling

- Storage conditions : 40°C max, 70% R.H.
- Devices may not meet specified performance if storage conditions are exceeded.

#### Order Information Packaging

Oraci iiii	Officiation	i ackaging				
mSMD		030-60V Tape & Reel Quantity				
	Product name	Hold				
	Size 4532mm/1812 inch	Current	1,500 pcs/reel			
	SMD: surface mount device	0.30A				

Tape & reel packaging per EIA481-1

### Labeling Information

