

SEA & LAND ELECTRONIC CORP.

WWW.SEALAND-PPTC.COM

ALPHA-TOP TECHNOLOGY CORP.

APPROVAL SHEET

MODEL NO.:	SMD500L-24V
CUSTOMER:	
CUSTOMER'S APPRO	OVAL:
AUTHORIZED SIGNA	TURE/STAMP:
DATE	

MANUFACTURER:

HEAD OFFICE:

13F.,No.120-10,Sec.3,Zhongshan Rd.,Zhonghe Dist.,New Taipei City 23544,Taiwan

Tel: 886-2-8221-2567 Fax:882-2-2225-7268 E-mail:service@chipfast.com.tw

China Branch:

Factory Building B)Shuangpeng,Weibu Village, Qiuchang Town, Huiyang District, Huizhou City, Guangdong Province, P.R.C.)
Tel: 86-752-3562001
Fax:86-752-3558696

E-mail:service@atpptc.com

Submitted by: Chung Cheng YC Lin Approved by: 11-Apr-22 DATE:

SEA & LAND ELECTRONIC CORP.



SMD500L-24V

Features

■ Surface Mount Devices

■ Lead free device

■ Size 7.5*5.5 mm 0.29*0.20 inch

■ Surface Mount packaging for automated assembly

Applications

Almost anywhere there is a low voltage power supply, up to 60V and a load to be

protected, including:

■ Computer mother board, Modem.

■ Telecommunication equipments

Alpha-Top (Sea & Land Alliance)

Performance Specification

	Model	V_{max}	I_{max}	I _{max} I _{hold}	$I_{\rm trip}$	P_d	Maximum Time To Trip		Resistance		Agency Approval	
	Model			@25°C	@25°C	Тур.	Current	Time	Ri_{min}	R1 _{max}	UL	TUV
		(Vdc)	(A)	(A)	(A)	(W)	(A)	(Sec)	(Ω)	(Ω)	OL.	101
1	SMD500L-24V	24	100	5.00	10.00	1.8	20.0	10.0	0.005	0.031		

Ihold = Hold Current. Maximum current device will not trip in 25°C still air.

Itrip = Trip Current. Minimum current at which the device will always trip in 25°C still air.

Vmax = Maximum operating voltage device can withstand without damage at rated current (Imax).

Imax = Maximum fault current device can withstand without damage at rated voltage (Vmax).

Pd = Power dissipation when device is in the tripped state in 25°C still air environment at rated voltage.

Rimin/max = Minimum/Maximum device resistance prior to tripping at 25°C.

R1max = Maximum device resistance is measured one hour post reflow.

CAUTION: Operation beyond the specified ratings may result in damage and possible arcing and flame.

Environmental Specifications

Test	Conditions	Resistance change				
Passive aging	+85°C, 1000 hrs.	±5% typical				
Humidity aging	+85°C, 85% R.H., 168 hours	±5% typical				
Thermal shock	+85°C to -40°C, 20 times	±33% typical				
Resistance to solvent	MIL-STD-202,Method 215	No change				
Vibration	MIL-STD-202,Method 201	No change				
Ambient operating conditions : - 40 °C to +85 °C						
Maximum surface temperature of the device in the tripped state is 125 °C						

Agency Approvals :

Regulation/Standard:

Pb RoHS

2015/863/EU

HF

EN14582

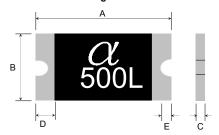
I_{hold} Versus Temperature

noid raidan raimbarar									
Model			Maximum ambient operating temperature (T _{mao) vs. hold current (Ihold)}						
	-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
SMD500L-24V	7.55	6.70	5.85	5.00	4.15	3.75	3.30	2.90	2.25

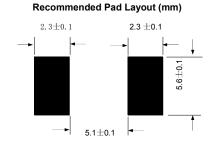
Construction And Dimension (Unit:mm)

Model		A		В С		C	D E	
Model	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Min.
SMD500L-24V	6.73	7.98	4.80	5.44	0.60	1.50	0.30	0.30

Dimensions & Marking



 α = Trademark 500 = Hold current



Termination Pad Characteristics

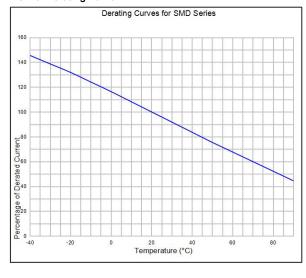
Tin-plated Nickel-Copper Terminal pad materials :

Terminal pad solderability: Meets EIA specification RS186-9E and ANSI/J-STD-002 Category 3.

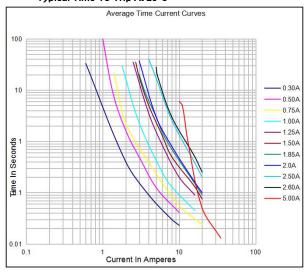
Rework

Use standard industry practices, the removal device must be replaced with a fresh one.

Thermal Derating Curve



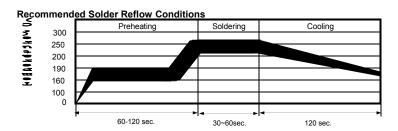
Typical Time-To-Trip At 25°C



🚺 WARNING:

- Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated. Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- · Use PPTC with a large inductance in circuit will generate a circuit voltage (L di/dt) above the rated voltage of the PPTC.
 · Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.
- Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC SMD can be cleaned by standard methods.

 Requests that customers comply with our recommended solder pad layouts and recommended reflow profile. Improper board layouts or reflow profile could negatively impact solderability performance of our devices.



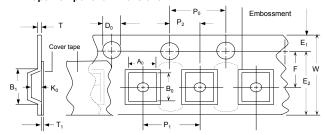
- Recommended reflow methods : IR, vapor phase oven, hot air oven.
- Devices are not designed to be wave soldered to the bottom side of the board.
- Recommended maximum paste thickness is 0.25 mm (0.010 inch).
- Devices can be cleaned using standard method and solvents.

Note: If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

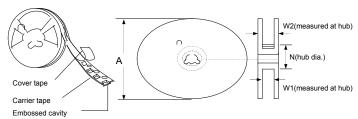
Tape And Reel Specifications (mm)

Governing Specifications	EIA 481-2
W	16.0 ± 0.3
P_0	4.0 ± 0.10
P ₁	8.0 ± 0.10
P ₂	2.0 ± 0.05
_A ₀	5.70 ± 0.10
B ₀	8.00 ± 0.10
B₁max.	12.1
D_{0}	1.5 + 0.1, -0
F	7.5 ± 0.05
E ₁	1.75 ± 0.10
E ₂ min.	14.25
Tmax.	0.6
T₁max.	0.1
_K ₀	0.80 ± 0.1
Leader min.	390
Trailer min.	160
Reel Dimensions	
A max.	178
N min.	60
W ₁	16.4 + 2.0, -0.0
W₂max.	22.4

EIA Tape Component Dimensions



EIA Reel Dimensions



- Storage And Handling
 Storage conditions: 40°C max, 70% R.H.
- · Devices may not meet specified performance if storage conditions are exceeded.

Order Information Packaging

SMD	500L-16V	Tape & Reel Quantity
Product name	Hold	
Size 7555 mm /2920 inch	Current	1500 pcs/reel
SMD : surface mount device	5.00A	

Tape & reel packaging per EIA481-1

Labeling Information

