

STU13005N

High voltage fast-switching NPN power transistor

Datasheet - production data



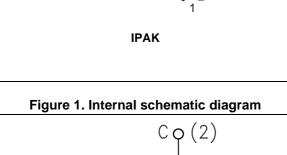
- High voltage capability
- Low spread of dynamic parameters
- Very high switching speed

Application

• Switch mode power supplies (AC-DC converters)

Description

This device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining a wide RBSOA.



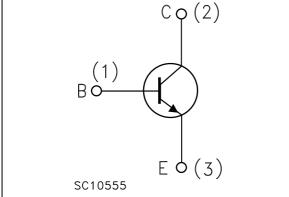


Table 1. Device summary

Order code	Marking	Package	Packaging
STU13005N	U13005N	IPAK	Tube

This is information on a product in full production.

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1 Electrical ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	700	V
V _{CEO}	Collector-emitter voltage ($I_B = 0$)	400	V
V_{EBO}	Emitter-base voltage ($I_C = 0$; $I_B = 1.5$ A; $t_p < 10$ ms)	V _{(BR)EBO}	V
۱ _C	Collector current	3	Α
I _{CM}	Collector peak current (t _P < 5 ms)	6	Α
Ι _Β	Base current	1.5	Α
I _{BM}	Base peak current (t _P < 5 ms)	3	Α
P _{TOT}	Total dissipation at $T_c = 25 \text{ °C}$	30	W
T _{STG}	Storage temperature	-65 to 150	°C
Τ _J	Max. operating junction temperature	150	°C

Table 2. Absolute maximum ratings

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case max	4.2	°C/W



2 Electrical characteristics

 $T_{case} = 25$ °C unless otherwise specified.

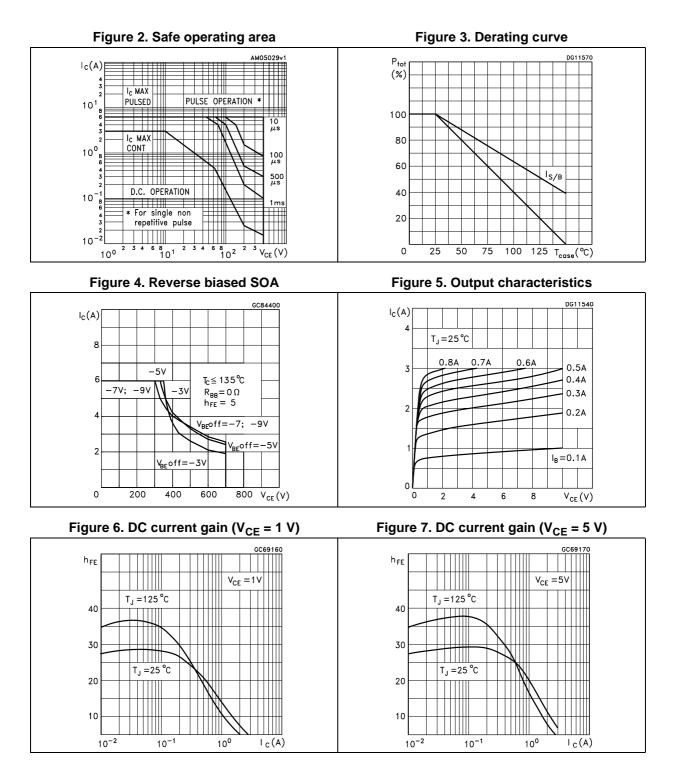
Table 4. Electrical characteristics						
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 700 V V _{CE} = 700 V T _C = 125 °C			1 5	mA mA
I _{CEO}	Collector-cut-off current $(I_B = 0)$	V _{CE} = 400 V			1	mA
V _{(BR)EBO}	Emitter base breakdown voltage $(I_{C} = 0)$	I _E = 10 mA	9		18	V
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage $(I_B = 0)$	I _C = 10 mA	400			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage				0.5 0.6 5	V V V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	$I_{C} = 1A \qquad I_{B} = 200 \text{ mA}$ $I_{C} = 2A \qquad I_{B} = 500 \text{ mA}$			1.2 1.6	V V
h _{FE} ⁽¹⁾	DC current gain	$ \begin{array}{ll} I_{C} = 500 \; \mu A & V_{CE} = 2 \; V \\ I_{C} = 425 \; m A & V_{CE} = 2 \; V \\ I_{C} = 1 \; A & V_{CE} = 5 \; V \\ I_{C} = 2 \; A & V_{CE} = 5 \; V \end{array} $	15 24 10 8		30 24	
t _s t _f	Resistive load Storage time Fall time	$I_{C} = 2 A$ $V_{CC} = 125 V$ $I_{B1} = -I_{B2} = 400 \text{ mA}$ $t_{p} = 30 \mu\text{s}$		1.65 260		µs ns
t _s t _f	Inductive load Storage time Fall time	$ I_C = 1 A \qquad V_{clamp} = 300 V \\ I_{B1} = 200 \text{ mA } V_{BE(off)} = -5 V \\ L = 50 \text{ mH} \qquad R_{BB} = 0 $		0.8 150		µs ns

Table 4.	Electrical	characteristics
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1. Pulse test: pulse duration \leq 300 µs, duty cycle \leq 2 %



2.1 Electrical characteristics (curves)



57

GC6919

 $I_{c}(A)$

Figure 8. Collector-emitter saturation voltage

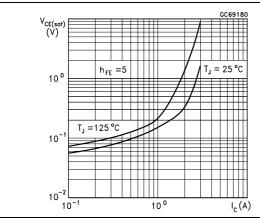


Figure 10. Inductive load fall time

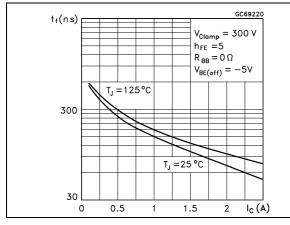
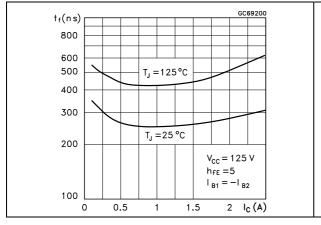


Figure 12. Resistive load fall time



0.4

Figure 9. Base-emitter saturation voltage

 $T_J = 25 °C$

T_J =125 °C

h_{FE}=5

V_{BE(sat)} (V)

1.0

0.8

0.6

0.2

 10^{-1}



10⁰

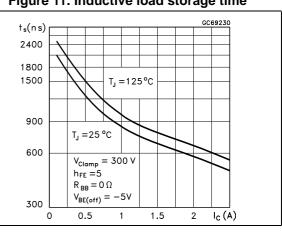
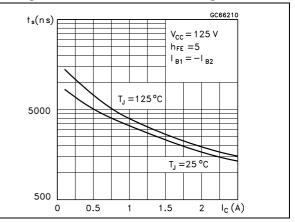


Figure 13. Resistive load storage time





3 Test circuits

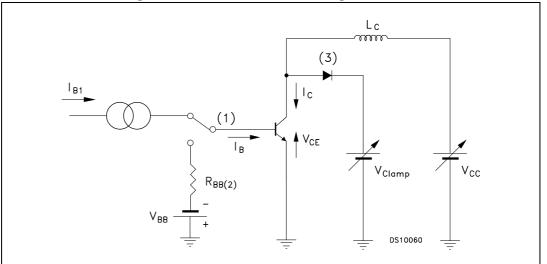


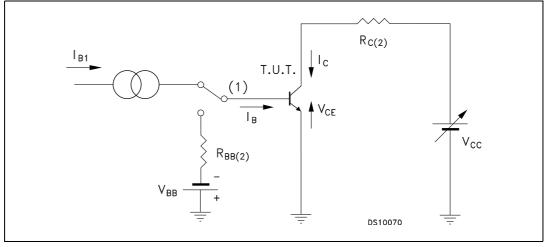
Figure 14. Inductive load switching test circuit

1) Fast electronic switch

2) Non-inductive resistor

3) Fast recovery rectifier

Figure 15. Resistive load switching test circuit



1) Fast electronic switch

2) Non-inductive resistor



4 Package mechanical data

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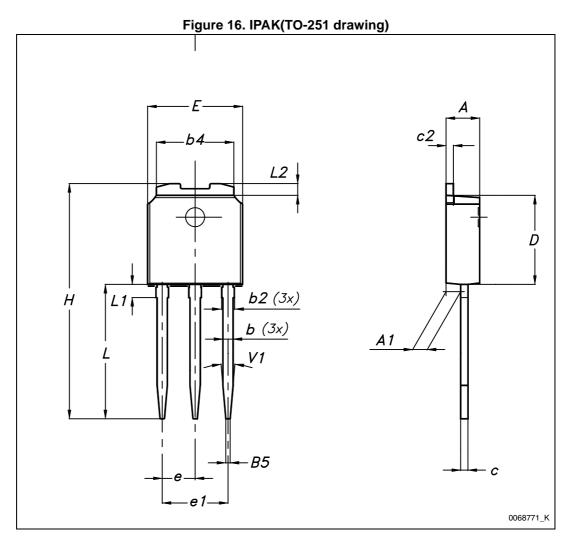




Table 5. IPAK (TO-251) mechanical data				
DIM	mm.			
DIM	min.	typ.	max.	
А	2.20		2.40	
A1	0.90		1.10	
b	0.64		0.90	
b2			0.95	
b4	5.20		5.40	
B5		0.30		
С	0.45		0.60	
c2	0.48		0.60	
D	6.00		6.20	
E	6.40		6.60	
е		2.28		
e1	4.40		4.60	
Н		16.10		
L	9.00		9.40	
L1	0.80		1.20	
L2		0.80	1.00	
V1		10°		

Table 5. IPAK (TO-251) mechanical data



5 Revision history

Table 6. Document	revision	history
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Date	Revision	Changes
20-Feb-2012	1	First release.
09-May-2014	2	Updated Table 1: Device summary and updated Figure 4: Package mechanical data



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