

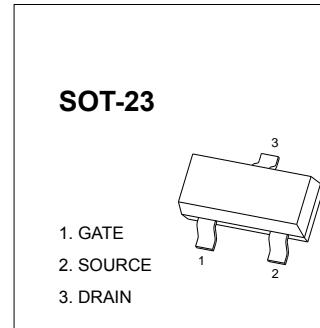


# AO3401

## SOT-23 Plastic-Encapsulate MOSFETs

### AO3401 P-Channel Enhancement Mode Field Effect Transistor

V <sub>(BR)DSS</sub>	R <sub>DS(on)MAX</sub>	I <sub>D</sub>
-30 V	65mΩ@-10V	-4.2A
	75mΩ@-4.5V	
	90mΩ@-2.5V	



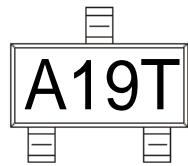
#### FEATURE

- High dense cell design for extremely low R<sub>DS(ON)</sub>.
- Exceptional on-resistance and maximum DC current capability

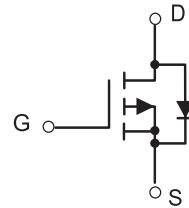
#### APPLICATION

- Load/Power Switching
- Interfacing Switching

#### MARKING



#### Equivalent Circuit



#### Maximum ratings ( T<sub>a</sub>=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Continuous Drain Current	I <sub>D</sub>	-4.2	A
Power Dissipation	P <sub>D</sub>	350	mW
Thermal Resistance from Junction to Ambient (t<5s)	R <sub>θJA</sub>	357	°C/W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55~+150	°C



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## MOSFET ELECTRICAL CHARACTERISTICS

$T_a=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Off characteristics</b>						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = -250\mu\text{A}$	-30			V
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}} = -24\text{V}, V_{\text{GS}} = 0\text{V}$			-1	$\mu\text{A}$
Gate-source leakage current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 12\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On characteristics</b>						
Drain-source on-resistance (note 1)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_{\text{D}} = -4.2\text{A}$		50	65	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -4\text{A}$		60	75	$\text{m}\Omega$
		$V_{\text{GS}} = -2.5\text{V}, I_{\text{D}} = -1\text{A}$		75	90	$\text{m}\Omega$
Forward transconductance (note 1)	$g_{\text{FS}}$	$V_{\text{DS}} = -5\text{V}, I_{\text{D}} = -5\text{A}$	7			S
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = -250\mu\text{A}$	-0.7	-0.9	-1.3	V
<b>Dynamic characteristics</b> (note 2)						
Input capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		954		pF
Output capacitance	$C_{\text{oss}}$			115		pF
Reverse transfer capacitance	$C_{\text{rss}}$			77		pF
<b>Switching characteristics</b> (note 2)						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, V_{\text{DS}} = -15\text{V}, R_{\text{L}} = 3.6\Omega, R_{\text{GEN}} = 6\Omega$			6.3	ns
Turn-on rise time	$t_{\text{r}}$				3.2	ns
Turn-off delay time	$t_{\text{d}(\text{off})}$				38.2	ns
Turn-off fall Time	$t_{\text{f}}$				12	ns
<b>Drain-source diode characteristics and maximum ratings</b>						
Diode forward voltage (note 1)	$V_{\text{SD}}$	$I_{\text{S}} = -1\text{A}, V_{\text{GS}} = 0\text{V}$			-1	V

**Note :**

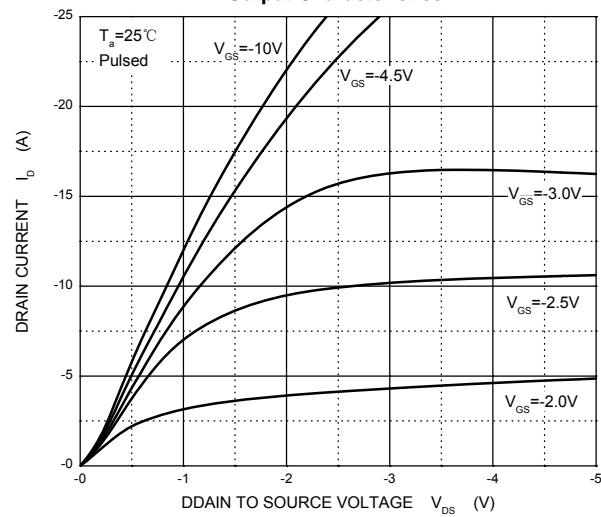
1. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
2. These parameters have no way to verify.



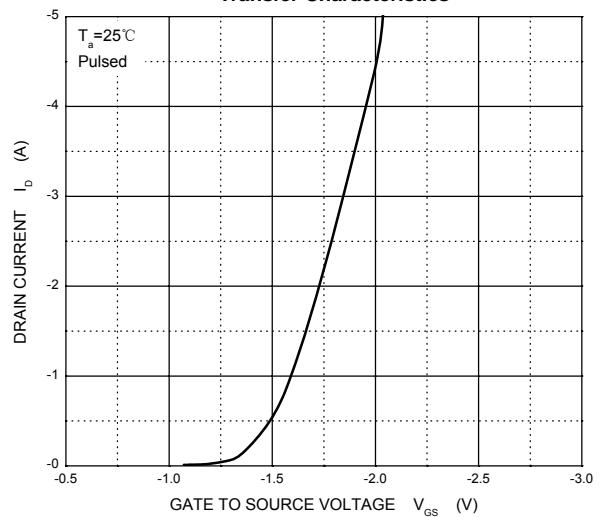
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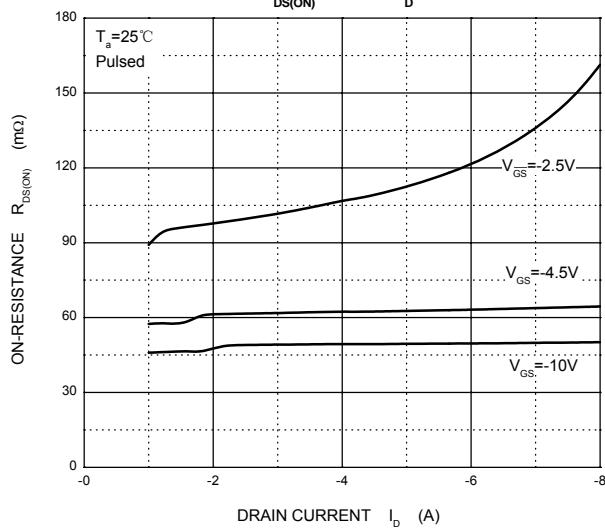
Output Characteristics



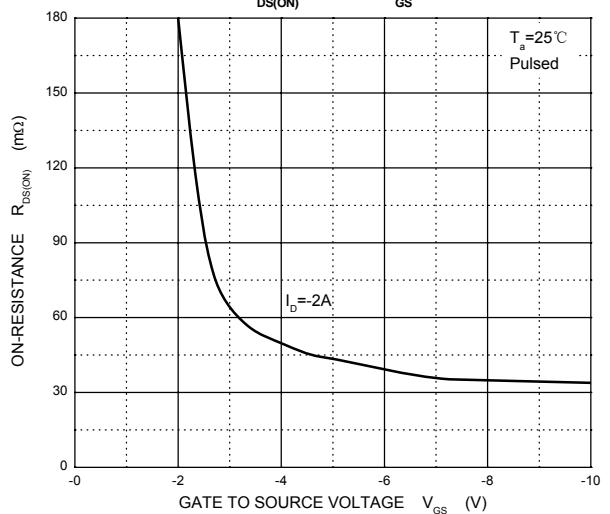
Transfer Characteristics



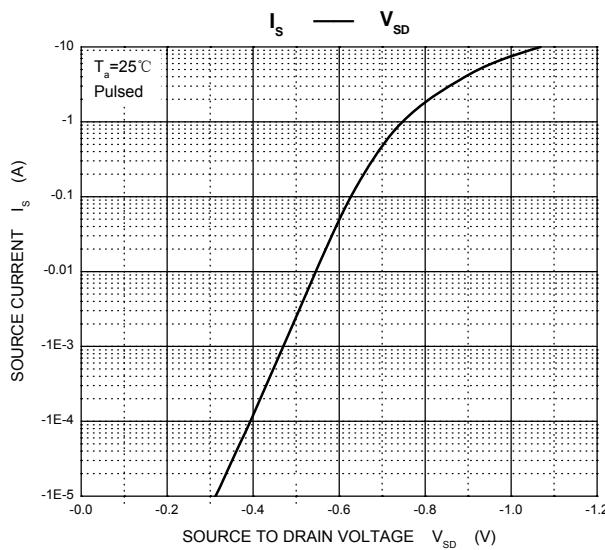
$R_{DS(ON)}$  —  $I_D$



$R_{DS(ON)}$  —  $V_{GS}$



$I_S$  —  $V_{SD}$

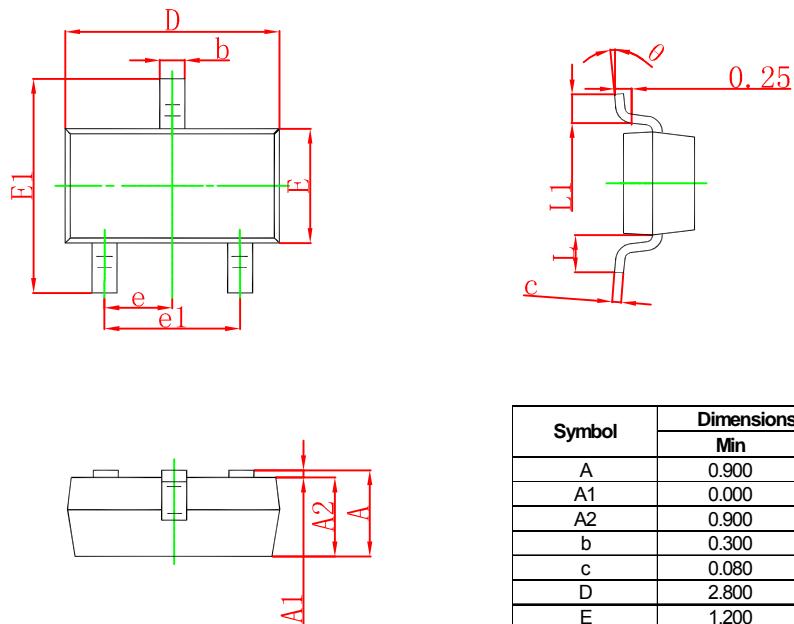




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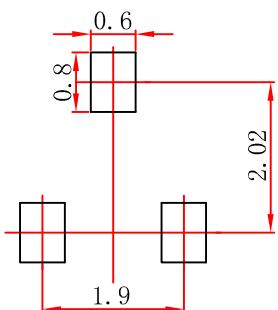
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### SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

### SOT-23 Suggested Pad Layout



#### Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.