

CMD63P04L/CMU63P04L

40V P-Channel MOSFET

General Description

The 63P04L uses advanced technology and design to provide excellent RDS(ON) .

This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

Features

- Max $r_{DS(on)}$ = 11mΩ at $V_{GS} = 10V$
- Fast Switching
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current	-63	A
$I_D @ T_c = 100^\circ C$		-50	A
I_{DM}	Pulsed Drain Current	-190	A
E_{AS}	Drain-Source Avalanche Energy ¹	225	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	75	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	50	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	1.7	°C/W

Product Summary

BVDSS	RDS(ON)	ID
-40V	11mΩ	-63A

Applications

- Inverters
- Power Supplies

TO-252/251 Pin Configuration



Type	Package	Marking
CMD63P04L	TO-252	CMD63P04L
CMU63P04L	TO-251	CMU63P04L

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-40	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-20\text{A}$	---	---	11	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-15\text{A}$	---	---	17	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1	---	-3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-32\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	±100	nA
g_{fs}	Forward Transconductance ²	$V_{\text{DS}}=-10\text{V}$, $I_{\text{D}}=-20\text{A}$	---	26	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, f=1MHz	---	4.3	---	Ω
Q_g	Total Gate Charge	$I_{\text{D}}=-20\text{A}$	---	61	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-20\text{V}$	---	21	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=-4.5\text{V}$	---	28	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-20\text{V}$, $I_{\text{D}}=-10\text{A}$	---	14	---	ns
T_r	Rise Time	$R_L=2\Omega$	---	11	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$R_G=1\Omega$	---	71	---	
T_f	Fall Time	$V_{\text{GSE}}=-10\text{V}$	---	17	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, f=1MHz	---	3600	---	pF
C_{oss}	Output Capacitance		---	250	---	
C_{rss}	Reverse Transfer Capacitance		---	210	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-63	A
I_{SM}	Pulsed Source Current		---	---	-190	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=-20\text{A}$	---	---	-1.2	V

Notes:

1.Starting $T_J = 25^\circ\text{C}$, $L=0.5\text{mH}$, $I_{\text{AS}} = -30\text{A}$, $V_{\text{DD}} = -30\text{V}$, $V_{\text{GS}} = -10\text{V}$.

2.Pulse Test: Pulse Width < 300μs, Duty cycle < 2.0%.

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.