## MSKSEMI















**ESD** 

TVS

TSS

MOV

GDT

**PLED** 

# Broduct data sheet



## VOLTAGE RANGE 20 to 100 Volts CURRENT 5.0 Ampere



## **FEATURES**

- \* Ideal for surface mount applications
- \* Easy pick and place
- \* Built-in strain relief
- \* Low forward voltage drop

## **MECHANICAL DATA**

- \* Case: Molded plastic
- \* Epoxy: UL 94V-0 rate flame retardant
- \* Metallurgically bonded construction
- \* Polarity: Color band denotes cathode end
- \* Mounting position: Any

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwies specified. Single phase half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

P/N(MARK)	SS52F	SS53F	SS54F	SS55F	SS56F	SS58F	SS59F	SS510F	UNITS
Maximum Recurrent Peak Reverse Voltage		30	40	50	60	80	90	100	V
Maximum RMS Voltage		21	28	35	42	56	63	70	V
Maximum DC Blocking Voltage		30	40	50	60	80	90	100	V
Maximum Average Forward Rectified Current									
At T <sub>L</sub> =100°C	5.0			Α					
Peak Forward Surge Current, 8.3 ms single half sine-wave									
superimposed on rated load (JEDEC method)		120							Α
Maximum Instantaneous Forward Voltage at 5.0A		0.55		0.70		0.85		V	
Maximum DC Reverse Current Ta=25°C	0.1 0.02			mA					
at Rated DC Blocking Voltage Ta=100°C	5 2				mA				
Typical Junction Capacitance (Note1)	380				pF				
Typical Thermal Resistance R JL (Note 2)		10							°C/W
Operating Temperature Range T <sub>J</sub>	-65 —+150				°C				
Storage Temperature Range Tsтg	-65—+150				°C				

#### NOTES

- 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
- 2. Thermal Resistance Junction to Lead.



## RATING AND CHARACTERISTIC CURVES (SS52F THRU SS510F)

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

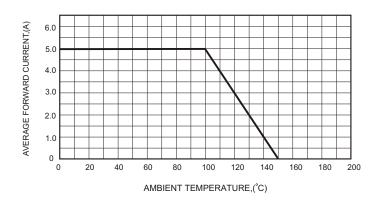
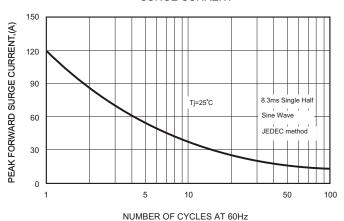


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT



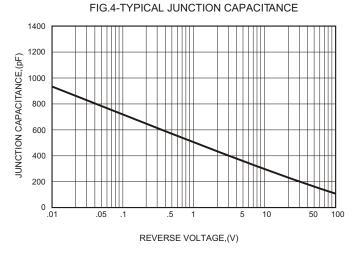


FIG.2-TYPICAL FORWARD

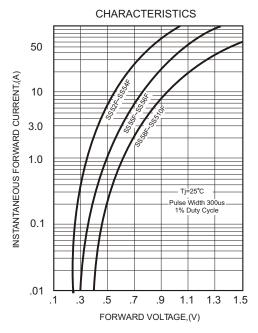
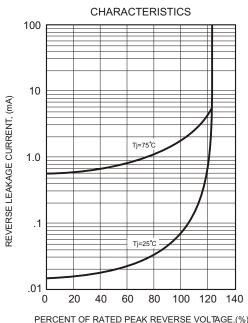
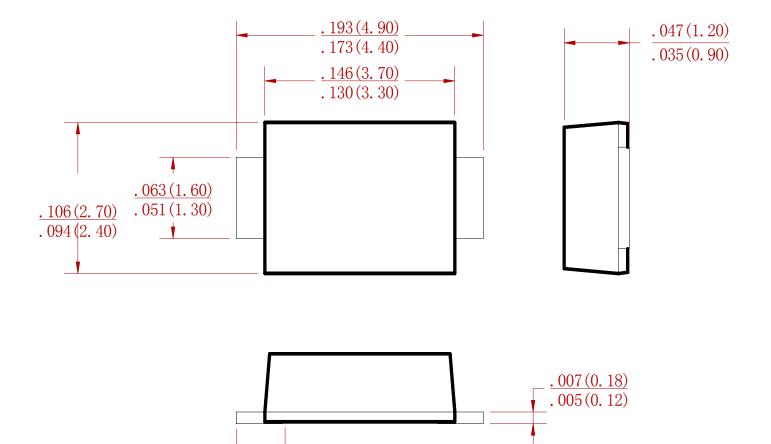


FIG.5 - TYPICAL REVERSE





## **PACKAGE MECHANICAL DATA**



Dimensions in inches and (millimeters)

-  $\frac{.051(1.30)}{.031(0.80)}$ 

## **REEL SPECIFICATION**

P/N	PKG	QTY
SS52F THRU SS510F	SMAF	3000



## Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specificationsof any andall MSKSEMI Semiconductor products described orcontained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringementsof intellectual property rights or other rightsof third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.