



Product data sheet

www.msksemi.com







SOT - 23



2. EMITTER

3. COLLECTOR

MMBTA06 TRANSISTOR (NPN)

FEATURES

- For Switching and Amplifier Applications
- Complementary Type PNP Transistor MMBTA56

MARKING: 1GM

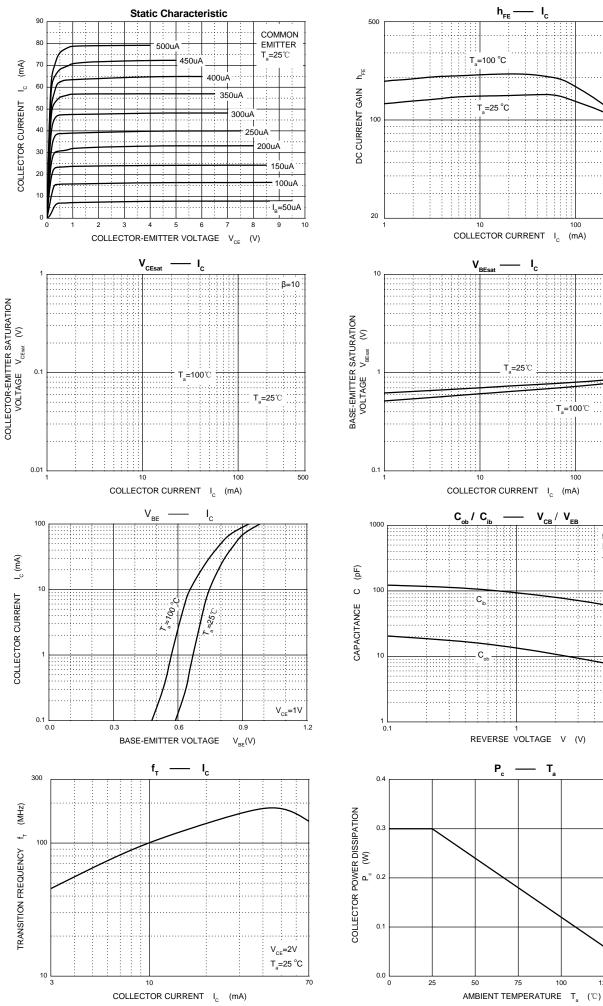
Symbol	Parameter	Value	Unit
V _{сво}	Collector-Base Voltage	80	V
VCEO	Collector-Emitter Voltage	80	V
V _{EBO}	Emitter-Base Voltage	4	V
lc	Collector Current	500	mA
Pc	Collector Power Dissipation	300	mW
R _{oja}	Thermal Resistance From Junction To Ambient	416	°C/W
T _J ,T _{stg}	Operation Junction and Storage Temperature Range	-55~+150	°C

MAYIMUM DATINGS (T

ELECTRICAL CHARACTERISTICS (T_a=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =0.1mA, I _E =0	80			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =1mA, I _B =0	80			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =0.1mA, I _C =0	4			V
Collector cut-off current	I _{СВО}	V _{CB} =80V, I _E =0			0.1	μA
Collector cut-off current	I _{CEO}	V _{CE} =60V, I _B =0			1	μA
Emitter cut-off current	I _{EBO}	V _{EB} =3V, I _C =0			0.1	μA
DC current gain	$h_{FE(1)}$	V _{CE} =1V, I _C =10mA	100		400	
	h _{FE(2)}	V _{CE} =1V, I _C =100mA	100			
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =100mA, I _B =10mA			0.25	V
Base-emitter saturation voltage	V _{BE(sat)}	I _C =100mA, I _B =10mA			1.2	V
Transition frequency	f⊤	V _{CE} =2V,I _C =10mA, f=100MHz	100			MHz



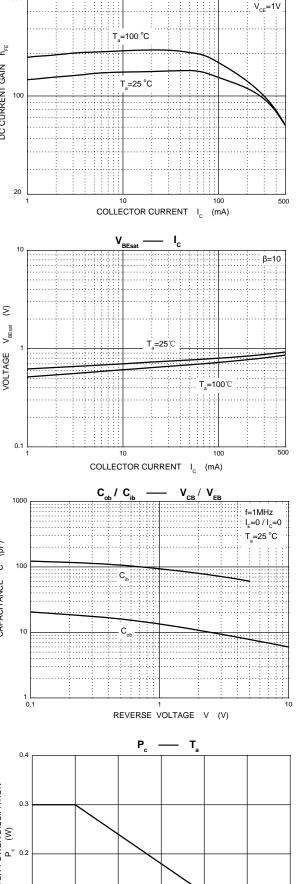




Semiconductor

h_{FE}

– I_c



150

100

50

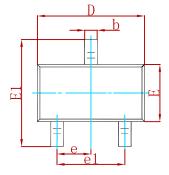
75

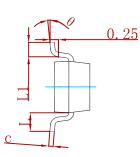
125

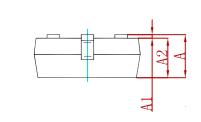




PACKAGE MECHANICAL DATA

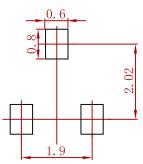






Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
с	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037	7 TYP	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

Suggested Pad Layout



Note:

Controlling dimension:in millimeters.
General tolerance:± 0.05mm.
The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
MMBTA06	SOT-23	3000





<u>Attention</u>

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

■ MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications f any and all MSKSEMI Semiconductor products described orcontained herein.

■ Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuits for safedesign, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.