

## 双路通用运算放大器

 查询样品: **MC1558-DIE**

### 特性

- 短路保护功能
- 宽共模和差分电压范围
- 无需频率补偿
- 低功耗
- 无锁存

### 说明

MC1558-DIE 是一款双路通用运算放大器, 除了没有提供偏移消除性能外, 此运算放大器的每一路在电气性能上与  $\mu$ A741 相似。

高共模输入电压范围和无锁存使得这个放大器非常适合于电压随耦器应用。MC1558-DIE 具有短路保护功能, 并且内部频率补偿可在无需外部组件的情况下确保稳定性。

### ORDERING INFORMATION<sup>(1)</sup>

PRODUCT	PACKAGE DESIGNATOR	PACKAGE	ORDERABLE PART NUMBER	PACKAGE QUANTITY
MC1558	TD	Bare die in waffle pack <sup>(2)</sup>	MC1558TDC1	400
			MC1558TDC2	10

- (1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at [www.ti.com](http://www.ti.com).
- (2) Processing is per the Texas Instruments commercial production baseline and is in compliance with the Texas Instruments Quality Control System in effect at the time of manufacture. Electrical screening consists of DC parametric and functional testing at room temperature only. Unless otherwise specified by Texas Instruments AC performance and performance over temperature is not warranted. Visual Inspection is performed in accordance with MIL-STD-883 Test Method 2010 Condition B at 75X minimum.



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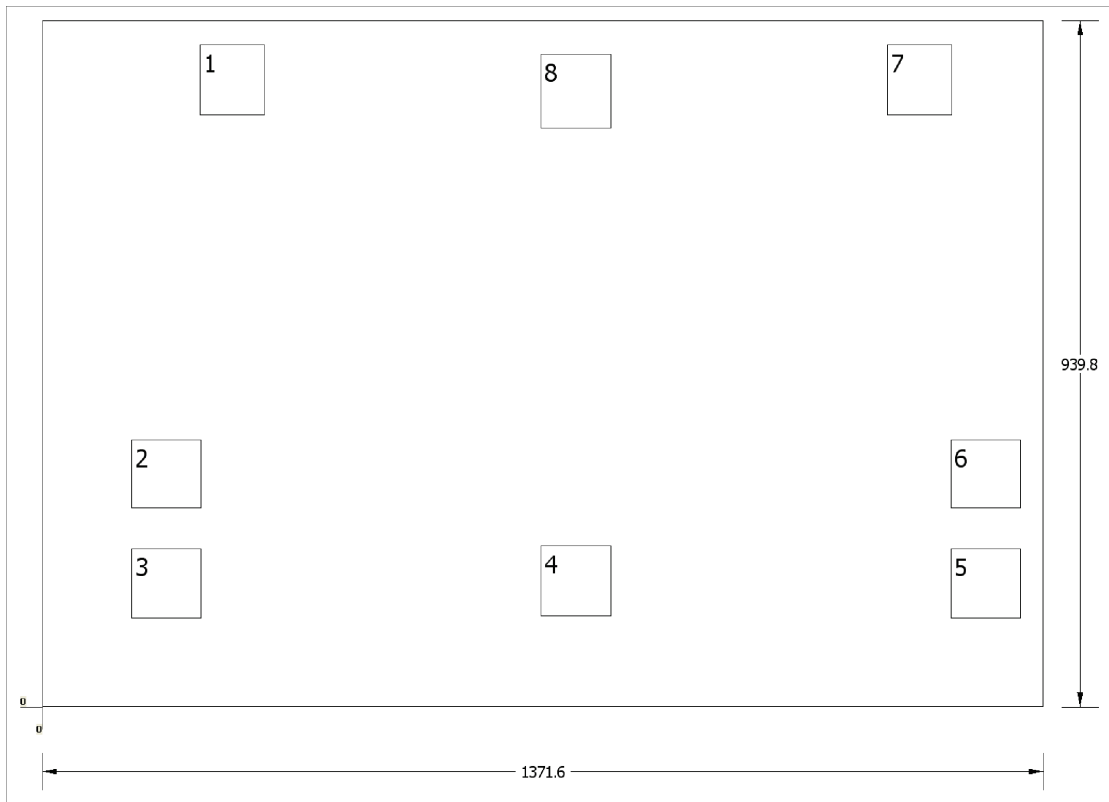


This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

**BARE DIE INFORMATION**

DIE THICKNESS	BACKSIDE FINISH	BACKSIDE POTENTIAL	BOND PAD METALLIZATION COMPOSITION	BOND PAD THICKNESS
10.5 mils.	Silicon with backgrind	Floating	AlCuTiW	1627 nm



**Table 1. Bond Pad Coordinates in Microns**

DESCRIPTION	PAD NUMBER	X MIN	Y MIN	X MAX	Y MAX
1OUT	1	215.9	810.26	304.8	906.78
1IN-	2	121.92	271.78	218.44	365.76
1IN+	3	121.92	120.65	218.44	217.17
VCC-	4	683.26	124.46	779.78	220.98
2IN+	5	1244.6	120.65	1341.12	217.17
2IN-	6	1244.6	271.78	1341.12	365.76
2OUT	7	1158.24	810.26	1247.14	906.78
VCC+	8	683.26	792.48	779.78	894.08

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
MC1558TDC1	ACTIVE			0	374	RoHS & Green	Call TI	N / A for Pkg Type	0 to 0		<a href="#">Samples</a>
MC1558TDC2	ACTIVE			0	10	RoHS & Green	Call TI	N / A for Pkg Type	0 to 0		<a href="#">Samples</a>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSELETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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