

N-Ch MOSFET

ID

60A

General Description

The WSR60N06 uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

Application

BVDSS

60V

Product Summery

Power switching application

RDSON

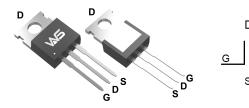
 $12m\Omega$

- LED backlighting
- Uninterruptible power supply

Features

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

TO-220AB Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	60	V	
V_{GS}	Gate-Source Voltage	±20	V	
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	60	Α	
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V ¹	41	Α	
I _{DM}	Pulsed Drain Current ²	120	Α	
EAS	Single Pulse Avalanche Energy ³	290	mJ	
P _D @T _C =25℃	Total Power Dissipation ⁴	85	W	
T _J T _{STG}	Operating Junction Temperature Range	-55 to 150	$^{\circ}$	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
$R_{ heta JA}$	Thermal Resistance Junction-Ambient ¹		62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		0.57	°C/W	



Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	60			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25℃ , I _D =1mA		0.057		V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =30A		12	14	mΩ
		V _{GS} =4.5V , I _D =20A		15	20	
$V_{GS(th)}$	Gate Threshold Voltage		2.0	3.0	4.0	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient			-5.68		mV/℃
	Drain Source Leakage Current	V _{DS} =48V , V _{GS} =0V , T _J =25°C			1	uA
I _{DSS}	Drain-Source Leakage Current	V_{DS} =48V , V_{GS} =0V , T_J =55 $^{\circ}{ m C}$			5	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20V$, V_{DS} = $0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =15A		30		S
R_g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7	3.4	Ω
Q_{g}	Total Gate Charge (4.5V)	V _{DS} =30V , V _{GS} =4.5V , I _D =30A		36	45	nC
Q_{gs}	Gate-Source Charge			9.9	18	
Q_{gd}	Gate-Drain Charge			6.6	15	
$T_{d(on)}$	Turn-On Delay Time	V_{DS} =30V , V_{GS} =10V , I_{D} =2A , R=1 Ω .		12	14.4	
T _r	Rise Time			5.2	10	no
T _{d(off)}	Turn-Off Delay Time			38	55	ns
T _f	Fall Time			27	32	
C _{iss}	Input Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		2498	2925	
C _{oss}	Output Capacitance			185	203	pF
C _{rss}	Reverse Transfer Capacitance			80	136	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V _G =V _D =0V , Force Current			38	Α
I _{SM}	Pulsed Source Current ^{2,6}				90	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃			1.2	V
t _{rr}	Reverse Recovery Time	IF=1A ,dl/dt=100A/µs,TJ=25 C		35		nS
Q _{rr}	Reverse Recovery Charge			47		nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. E_{AS} condition: Tj=25 $^{\circ}\text{C}$,V_DD=30V,V_G=10V,L=0.5mH,Rg=25

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Typical Characteristics

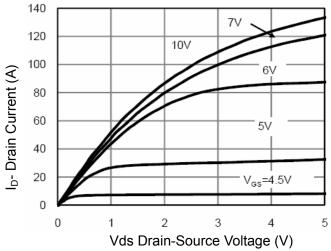


Figure 1 Output Characteristics

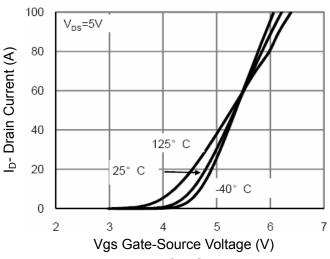


Figure 2 Transfer Characteristics

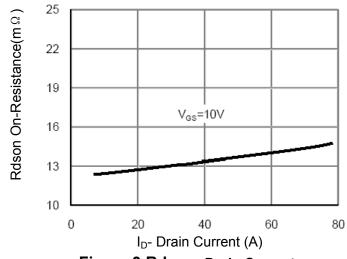


Figure 3 Rdson- Drain Current

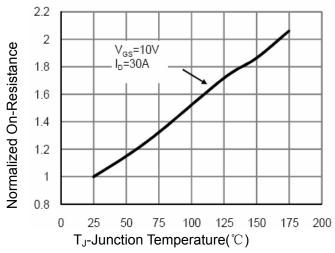


Figure 4 Rdson-JunctionTemperature

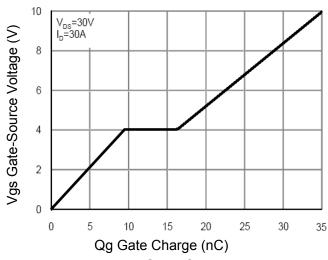


Figure 5 Gate Charge

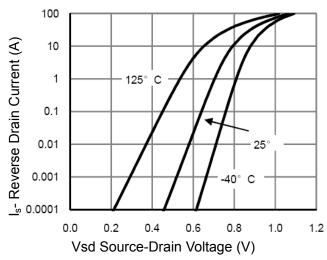


Figure 6 Source- Drain Diode Forward



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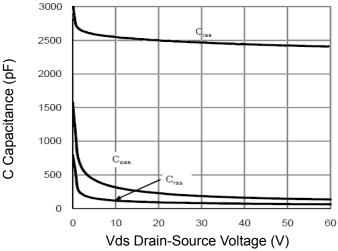


Figure 7 Capacitance vs Vds

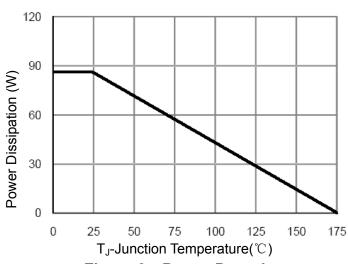


Figure 9 Power De-rating

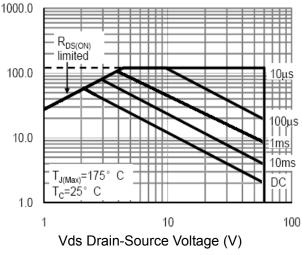


Figure 8 Safe Operation Area

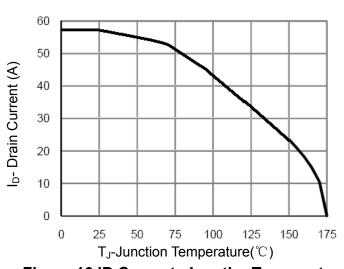


Figure 10 ID Current- JunctionTemperature

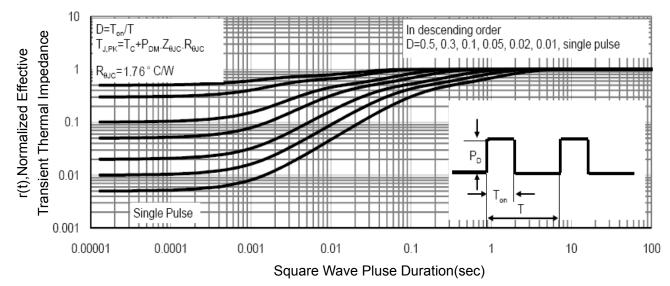


Figure 11 Normalized Maximum Transient Thermal Impedance



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