

### GENERAL DESCRIPTION

The SGM7SZ244 is an octal buffer/line driver with 3-state outputs which accepts a wide supply voltage range of 1.8V to 5.0V. The device is organized as two 4-bit line drivers with separate output enable inputs ( $\overline{1OE}$  and  $\overline{2OE}$ ). When  $\overline{nOE}$  is held low, data transmits from the nAn inputs to the nYn outputs. When  $\overline{nOE}$  is held high, the outputs are in the high-impedance state.

The SGM7SZ244 is available in a Green TSSOP-20 package. It operates over an ambient temperature range of -40°C to +125°C.

### FEATURES

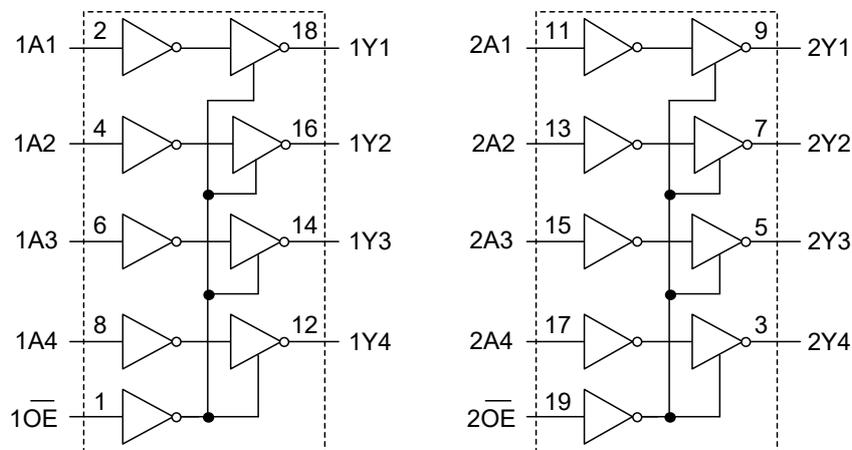
- **Wide Supply Voltage Range: 1.8V to 5.0V**
- **+20mA/-20mA Output Current at  $V_{CC} = 5.0V$**
- **Low Power Consumption,  $I_{CC}: 10nA$  (TYP)**
- **High Current Output Drive Capability: 15 LSTTL Loads**
- **3-State Outputs Drive Bus Lines Directly**
- **$t_{PD} = 8.9ns$  (TYP) at  $V_{CC} = 5.0V$**
- **Low Input Current:  $\pm 10nA$  (TYP)**
- **-40°C to +125°C Operating Temperature Range**
- **Available in a Green TSSOP-20 Package**

### FUNCTION TABLE

CONTROL INPUT	INPUT	OUTPUT
$\overline{nOE}$	nAn	nYn
L	H	H
L	L	L
H	X	Z

H = High Voltage Level  
 L = Low Voltage Level  
 Z = High-Impedance State  
 X = Don't Care

### LOGIC DIAGRAM



## SGM7SZ244

### PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM7SZ244	TSSOP-20	-40°C to +125°C	SGM7SZ244XTS20G/TR	SGM7SZ244XTS20 XXXXX	Tape and Reel, 4000

### MARKING INFORMATION

NOTE: XXXXX = Date Code and Vendor Code.

**XXXXX**



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

### ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup>

Supply Voltage Range, $V_{CC}$ .....	-0.3V to 6.0V
Input Clamp Current, $I_{IK}^{(2)}$ ( $V_I < 0$ or $V_I > V_{CC}$ ) .....	$\pm 20$ mA
Output Clamp Current, $I_{OK}^{(2)}$ ( $V_O < 0$ or $V_O > V_{CC}$ ) .....	$\pm 20$ mA
Output Current, $I_O$	
Continuous Output Current .....	$\pm 40$ mA
Continuous Output Current through $V_{CC}$ or GND .....	$\pm 70$ mA
Junction Temperature <sup>(3)</sup> .....	+150°C
Storage Temperature Range .....	-65°C to +150°C
Lead Temperature (Soldering, 10s) .....	+260°C
ESD Susceptibility	
HBM .....	6000V
CDM .....	1000V

### RECOMMENDED OPERATING CONDITIONS

Supply Voltage Range, $V_{CC}$ .....	1.8V to 5.0V
Input Voltage Range, $V_I$ .....	0V to $V_{CC}$
Output Voltage Range, $V_O$ .....	0V to $V_{CC}$
Input Transition Rise or Fall Rate, $\Delta t/\Delta V$	
$V_{CC} = 1.8$ V .....	500ns (MAX)
$V_{CC} = 3.3$ V .....	250ns (MAX)
$V_{CC} = 5.0$ V .....	200ns (MAX)
Operating Temperature Range .....	-40°C to +125°C

### OVERSTRESS CAUTION

- Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.
- The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.
- The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability.

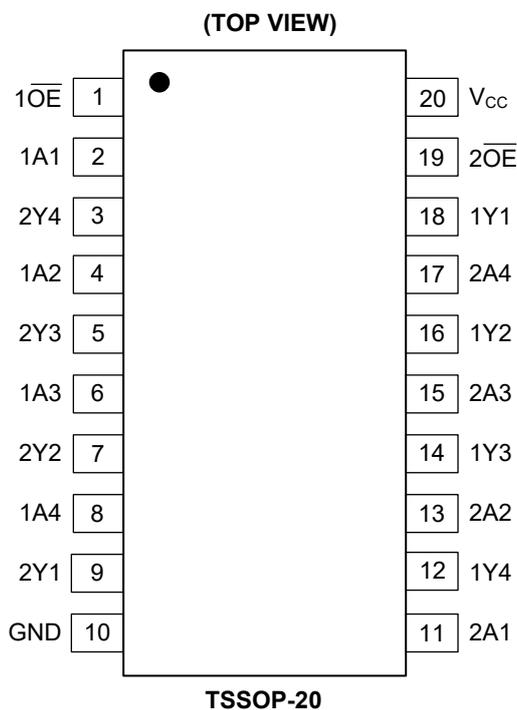
### ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

### DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

**PIN CONFIGURATION**



**PIN DESCRIPTION**

NAME	PIN	FUNCTION
1OE, 2OE	1, 19	Output Enable Inputs (Active Low).
1A1, 1A2, 1A3, 1A4	2, 4, 6, 8	Data Inputs.
2Y4, 2Y3, 2Y2, 2Y1	3, 5, 7, 9	Data Outputs.
GND	10	Ground.
2A1, 2A2, 2A3, 2A4	11, 13, 15, 17	Data Inputs.
1Y4, 1Y3, 1Y2, 1Y1	12, 14, 16, 18	Data Outputs.
V <sub>CC</sub>	20	Supply Voltage.

**ELECTRICAL CHARACTERISTICS**(Full = -40°C to +125°C, typical values are at  $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		TEMP	MIN	TYP	MAX	UNITS	
High-Level Input Voltage	$V_{IH}$	$V_{CC} = 1.8\text{V}$		Full	1.2			V	
		$V_{CC} = 3.3\text{V}$		Full	1.75				
		$V_{CC} = 5.0\text{V}$		Full	2.2				
Low-Level Input Voltage	$V_{IL}$	$V_{CC} = 1.8\text{V}$		Full			0.4	V	
		$V_{CC} = 3.3\text{V}$		Full			0.65		
		$V_{CC} = 5.0\text{V}$		Full			0.65		
High-Level Output Voltage	$V_{OH}$	$V_I = V_{IH}$	$I_{OH} = -20\mu\text{A}$	$V_{CC} = 1.8\text{V}$	Full	1.79	1.799	V	
				$V_{CC} = 3.3\text{V}$	Full	3.29	3.299		
				$V_{CC} = 5.0\text{V}$	Full	4.99	4.999		
			$I_{OH} = -6\text{mA}, V_{CC} = 3.3\text{V}$		Full	3.20	3.25		
			$I_{OH} = -7.8\text{mA}, V_{CC} = 5.0\text{V}$		Full	4.88	4.95		
Low-Level Output Voltage	$V_{OL}$	$V_I = V_{IL}$	$I_{OL} = 20\mu\text{A}$	$V_{CC} = 1.8\text{V}$	Full		0.002	0.01	V
				$V_{CC} = 3.3\text{V}$	Full		0.001	0.01	
				$V_{CC} = 5.0\text{V}$	Full		0.001	0.01	
			$I_{OL} = 6\text{mA}, V_{CC} = 3.3\text{V}$		Full		0.06	0.15	
			$I_{OL} = 7.8\text{mA}, V_{CC} = 5.0\text{V}$		Full		0.07	0.15	
Input Leakage Current	$I_I$	$V_{CC} = 5.0\text{V}, V_I = V_{CC}$ or $0\text{V}$		Full		$\pm 0.01$	8	$\mu\text{A}$	
Off-State Output Current	$I_{OZ}$	$V_{CC} = 5.0\text{V}, V_O = V_{CC}$ or $0\text{V}$		Full		$\pm 0.01$	8	$\mu\text{A}$	
Supply Current	$I_{CC}$	$V_{CC} = 5.0\text{V}, V_I = V_{CC}$ or $0\text{V}, I_O = 0\text{A}$		Full		0.01	25	$\mu\text{A}$	
Power-Off Leakage Current	$I_{OFF}$	$V_{CC} = 0\text{V}, V_I$ or $V_O = 0\text{V}$ to $5.0\text{V}$		Full		$\pm 0.01$	10	$\mu\text{A}$	
Input Capacitance	$C_I$	$V_{CC} = 1.8\text{V}$ to $5.0\text{V}$		+25°C		15.6		pF	

**SWITCHING CHARACTERISTICS**(Full = -40°C to +125°C, all typical values are at T<sub>A</sub> = +25°C, unless otherwise noted.)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V <sub>CC</sub>	MIN	TYP	MAX	UNITS
<b>(C<sub>L</sub> = 50pF)</b>							
t <sub>PD</sub>	nAn	nYn	1.8V		26.7		ns
			3.3V		11.0		
			5.0V		8.9		
t <sub>EN</sub>	n $\overline{\text{OE}}$	nYn	1.8V		36.6		ns
			3.3V		13.6		
			5.0V		10.1		
t <sub>DIS</sub>	n $\overline{\text{OE}}$	nYn	1.8V		39.5		ns
			3.3V		43.4		
			5.0V		44.9		
<b>(C<sub>L</sub> = 150pF)</b>							
t <sub>PD</sub>	nAn	nYn	1.8V		31.8		ns
			3.3V		11.8		
			5.0V		8.3		
t <sub>EN</sub>	n $\overline{\text{OE}}$	nYn	1.8V		41.9		ns
			3.3V		16.8		
			5.0V		14.0		
t <sub>DIS</sub>	n $\overline{\text{OE}}$	nYn	1.8V		60		ns
			3.3V		62		
			5.0V		64.3		

**SWITCHING CHARACTERISTICS (continued)**(Full = -40°C to +125°C, all typical values are at T<sub>A</sub> = +25°C, unless otherwise noted.)

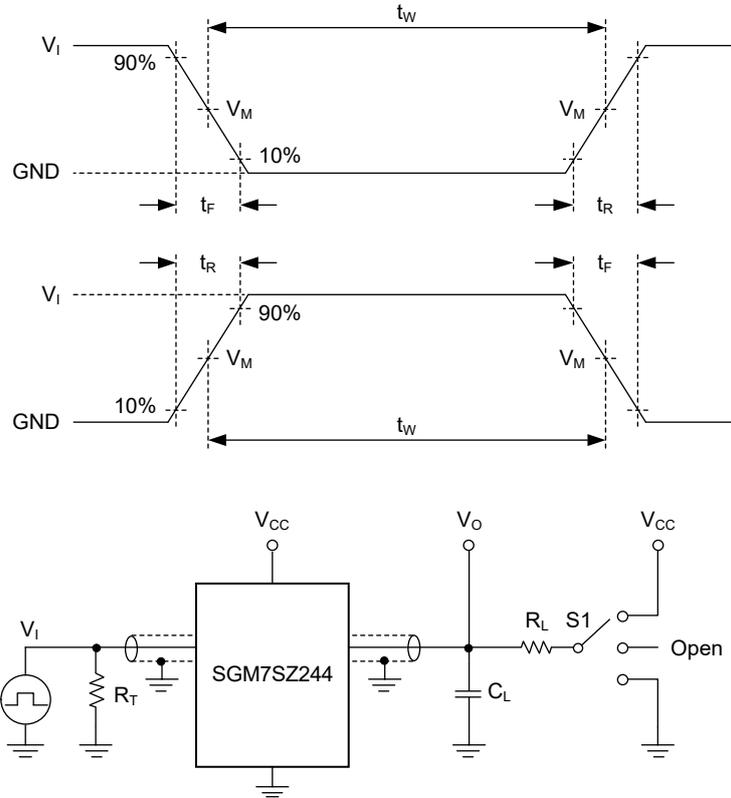
PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Input/Output Capacitance (C <sub>IO</sub> )				10.8		pF
Output Rise and Fall Times (t <sub>R</sub> , t <sub>F</sub> )	C <sub>L</sub> = 15pF	V <sub>CC</sub> = 1.8V		17.0		ns
		V <sub>CC</sub> = 3.3V		11.9		
		V <sub>CC</sub> = 5.0V		9.9		
	C <sub>L</sub> = 30pF	V <sub>CC</sub> = 1.8V		17.6		
		V <sub>CC</sub> = 3.3V		11.4		
		V <sub>CC</sub> = 5.0V		9.6		
	C <sub>L</sub> = 50pF	V <sub>CC</sub> = 1.8V		18.2		
		V <sub>CC</sub> = 3.3V		10.8		
		V <sub>CC</sub> = 5.0V		9.4		
Power Dissipation Capacitance per Transceiver (C <sub>PD</sub> ) <sup>(1)(2)</sup>	No load			63.3		pF

## NOTES:

- Power dissipation capacitance per transceiver.
- C<sub>PD</sub> is used to determine the dynamic power dissipation (P<sub>D</sub> in μW).  

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$$
 where:  
 f<sub>i</sub> = Input frequency in MHz. f<sub>o</sub> = Output frequency in MHz.  
 C<sub>L</sub> = Output load capacitance in pF. V<sub>CC</sub> = Supply voltage in Volts.  
 N = Number of inputs switching.  $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = Sum of the outputs.

TEST CIRCUIT



Test conditions are given in Table 1.

Definitions for test circuit:

$R_L$ : Load resistance.

$C_L$ : Load capacitance (includes jig and probe).

$R_T$ : Termination resistance (equals to output impedance  $Z_O$  of the pulse generator).

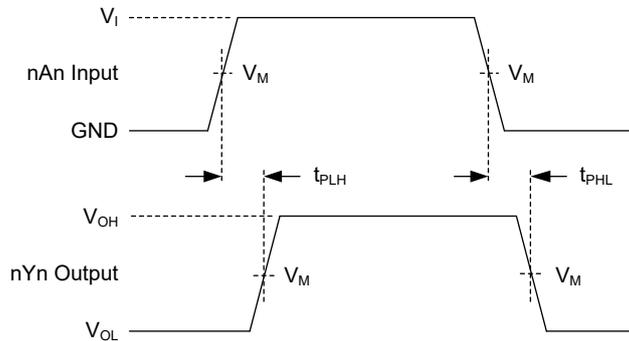
S1: Test selection switch.

Figure 1. Test Circuit for Measuring Switching Times

Table 1. Test Conditions

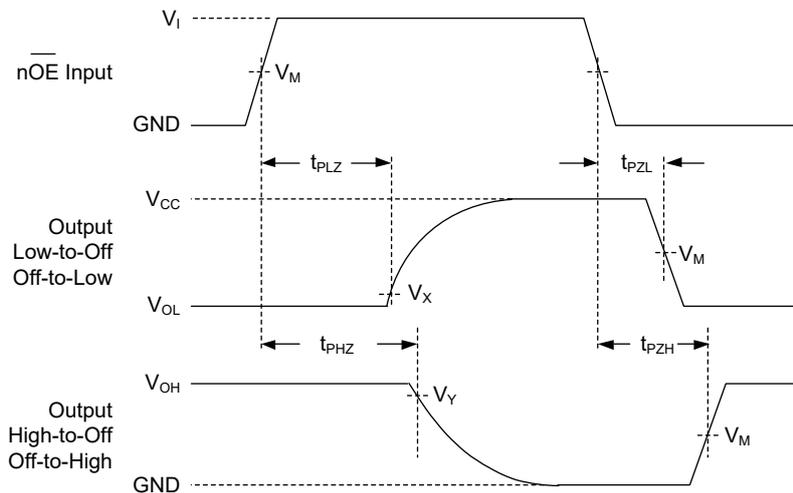
SUPPLY VOLTAGE	INPUT		LOAD		S1 POSITION		
$V_{CC}$	$V_I$	$t_R, t_F$	$C_L$	$R_L$	$t_{PHL}, t_{PLH}$	$t_{PZH}, t_{PHZ}$	$t_{PZL}, t_{PLZ}$
1.8V to 5.0V	$V_{CC}$	6.0ns	50pF, 150pF	1k $\Omega$	Open	GND	$V_{CC}$

WAVEFORMS



Test conditions are given in Table 1.  
Measurement points are given in Table 2.  
Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

Figure 2. Input (nAn) to Output (nYn) Propagation Delay Times



Test conditions are given in Table 1.  
Measurement points are given in Table 2.  
Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

Figure 3. Enable and Disable Times

Table 2. Measurement Points

SUPPLY VOLTAGE	INPUT <sup>(1)</sup>		OUTPUT		
$V_{CC}$	$V_I$	$V_M$	$V_M$	$V_X$	$V_Y$
1.8V to 5.0V	$V_{CC}$	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.3V$	$V_{OH} - 0.3V$

NOTES:

1. The measurement points should be  $V_{IH}$  or  $V_{IL}$  when the input rising or falling time exceeds 6.0ns.

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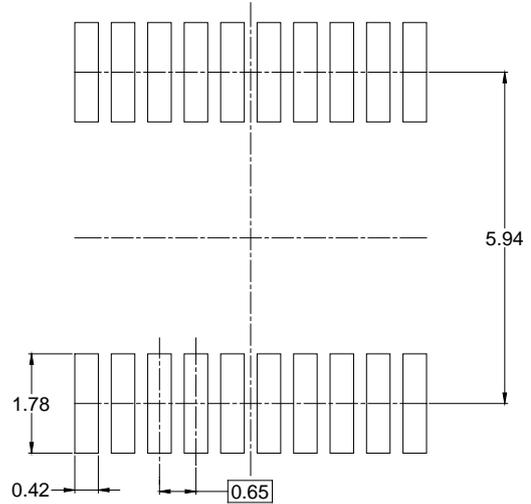
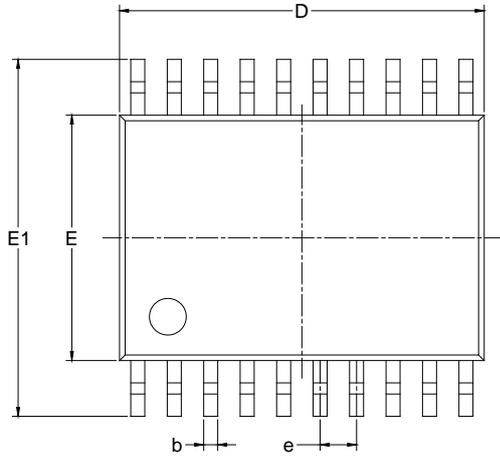
## REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

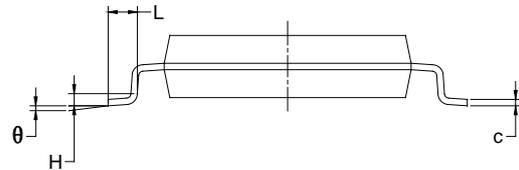
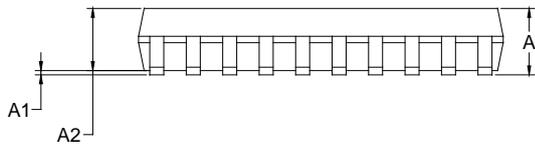
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<b>MARCH 2021 – REV.A.2 to REV.A.3</b>	
Updated Tape and Reel Information section .....	9
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<b>SEPTEMBER 2020 – REV.A.1 to REV.A.2</b>	Page
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<b>JUNE 2020 – REV.A to REV.A.1</b>	Page
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<b>Changes from Original (APRIL 2020) to REV.A</b>	Page
Changed from product preview to production data .....	All
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PACKAGE OUTLINE DIMENSIONS

TSSOP-20



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		
	MIN	MOD	MAX
A		-	1.200
A1	0.050	-	0.150
A2	0.800	-	1.050
b	0.190	-	0.300
c	0.090	-	0.200
D	6.400	-	6.600
E	4.300	-	4.500
E1	6.250	-	6.550
e	0.650 BSC		
L	0.450	-	0.750
H	0.250 TYP		
$\theta$	0°	-	8°

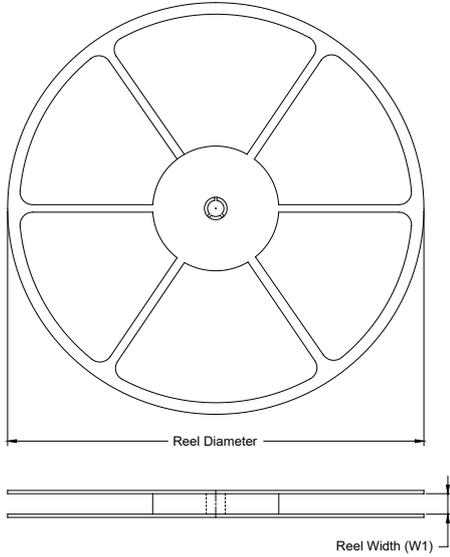
NOTES:

1. Body dimensions do not include mode flash or protrusion.
2. This drawing is subject to change without notice.

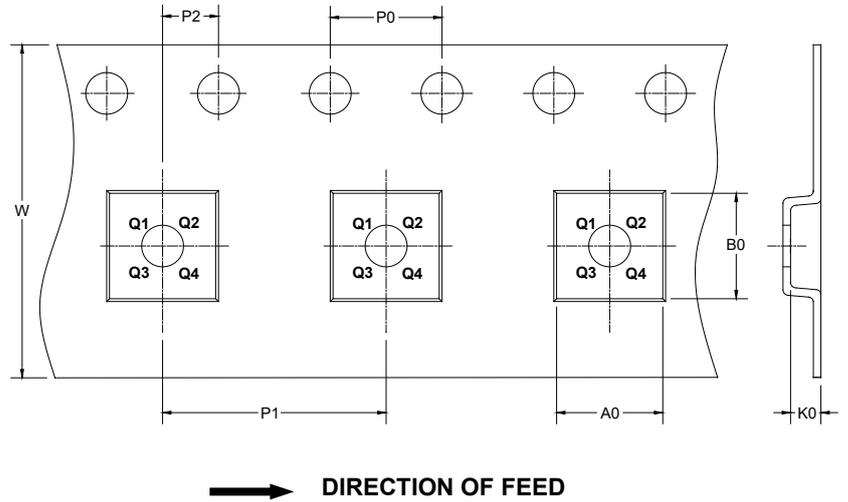
# PACKAGE INFORMATION

## TAPE AND REEL INFORMATION

### REEL DIMENSIONS



### TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TSSOP-20	13"	16.4	6.80	6.90	1.50	4.0	8.0	2.0	16.0	Q1

D00001

# PACKAGE INFORMATION

## CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

## KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5

DD0002