WISN	POE		POE-D01-00-E-15
50V, 100V, 500V, 1KV, 2KV TEMPERATURE COMPENSAT	TING CERAMIC DISC CAPACITOR	Ver: 15	Page: 1 of 19

PRODUCT SPECIFICATION
PRODUCT: CERAMIC DISC CAPACITOR
TYPE: 50V, 100V, 500V, 1KV, 2KV, TEMPERATURE COMPENSATING CAPACITOR
CUSTOMER:
DOC. NO.: POE-D01-00-E-15
Ver.: <u>15</u>
APPROVED BY CUSTOMER
VENDOR:
 WALSIN TECHNOLOGY CORPORATION 566-1, KAO SHI ROAD, YANG-MEI TAO-YUAN, TAIWAN PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD. NO.277, HONG MING ROAD, EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE, CHINA
MAKER : PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD. NO.277,HONG MING ROAD,EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE,CHINA

WISN	POE		POE-D01-00-E-15
50V, 100V, 500V, 1KV, 2KV TEMPERATURE COMPENSATI	NG CERAMIC DISC CAPACITOR	Ver: 15	Page: 2 of 19

Record of change

Date	Version	Description						
2008.6.3	1		ore) \rightarrow POE-D01-00-	-E-01 (1 st edition)				
2008.8.22	2	1.Revised diameter a		1				
		Before	Now	Before	Now			
		CH5000R5X040*	not available	SL500181X060*	SL500181X050*	8-9		
		CH1010R5X040*	not available	SL500241X070*	SL500241X060*			
		CH501360X050*	CH501360X060*	SL500361X080*	SL500361X070*			
		CH501620X080*	CH501620X060*	SL500391X080*	SL500391X070*			
		CH501680X080*	CH501680X060*	SL101181X060*	SL101181X050*			
		CH501750X080*	CH501750X060*	SL101241X070*	SL101241X060*			
		CH501820X080*	CH501820X070*	SL101361X080*	SL101361X070*			
		CH501101X080*	CH501101X070*	SL101391X080*	SL101391X070*			
		CH102080X060*	CH102080X050*	SL102680X060*	SL102680X050*	6-7		
		CH102100X060*	CH102100X050*	SL102121X100*	SL102121X060*	5		
		CH102120X060*	CH102120X050*	SL102151X100*	SL102151X070*			
		CH102620X080*	CH102620X070*	SL102181X100*	SL102181X070*			
		CH102820X100*	CH102820X080*	SL102201X100*	SL102201X080*			
				SL102221X100*	SL102221X080*			
		2. Complete lead cod	e					
		3.Add last SAP code	"H" for halogen and I	Pb free , epoxy resin				
2008.12.12	3		th to 17 th codes of SAP	P/N.		5-9		
		2. Page layout adju						
2000 0 10		-	when the coating resin		ee Epoxy.			
2009.8.19	4		E logo to Walsin & PC ture range change from		25°C - 125°C	13		
					$\sim +125$ °C, ge from +85°C to +125°C	15		
		-	erature didn't change).		ge from $+65 \oplus 10 \pm 125 \oplus$			
2010.8.24	5	· .			500V 62pF&68pF&75pF.	8		
_010101_1	C C		(Code of diameter dim			9		
2012/5/10	6	1). Review the size D	ϕ of the item CH/500	V/121&151 from "100)" to be "080":	8		
2012/0/10					be "070", CH/1000V/101	8		
		from "100" to be	"070".					
2012/12/5	7		emp of Allowable Volt			18-19		
2013/5/6	8		l diameter φ from 0.60			7,10		
			≤6.0mm shall be omitt			9 13		
		from 2 ± 0.5 s to $\frac{4}{5}$		10111 233(+3/-0) (- 10 2	$245\pm5^{\circ}$ C , Solderability time	15		
2013/10/18	9	Review the packing				11		
2015/8/31	10		s of the use of epoxy re	sin for 1KV products		8-9		
		-	ents of the temperature			5,		
			tion about"Old Part No			6,7		
			-391 pF (Code of d	iameter dimension i	s 110&120) for P/N CH	8		
2015/0/22	11	50V&100V.	$00 = \mathbf{E} \left(\mathbf{O} \cdot 1 - \mathbf{O} \cdot 1 \right)$	1		8		
2015/9/23	11		ension is 080) for P/N		and 120 pF &150 pF (Code	8		
					070) for P/N CH 1KV.	8		
		6. Delete 4pF~22p	F (Code of diameter	dimension is 060)	and 24pF~47pF (Code of			
		diameter dimens	ion is 070) for P/N CH	1 2KV.				
			lable lead code of Lead			6-7		
2016/3/2	12		bient Temp of Allowal		0Vdc to 2kVdc)	17-18 19		
		3. Review 9. Draw	ing of internal structure	e and material list		17		

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Date	Version		Desc
		1. Revised diameter	as below :
		Before	Now
		SL202181J100*	SL202181J080*
		SL202201J100*	SL202201J080*
2016/5/3	13	SL202221J100*	SL202221J080*
		SL202241J100*	SL202241J080*
		SL202271J100*	SL202271J080*
		SL202301J120*	SL202301J110*
		SL202331J120*	SL202331J110*
2016/11/3	14	1. Delete "CH" serie	es.
2016/12/21	15	1. Revised the produ	ict diameter for SL 50V

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50V, 100V, 500V, 1KV, 2KV TEMPERATURE COMPENSATING CERAMIC DISC CAPACITOR	Ver: 15	Page: 4 of 19

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5	Taping Format	9/19
6	Packing specification	10/19
7	Specification and test method	11/19~14/19
8	Cautions & notices	15/19~18/19
9	Drawing of internal structure and material list	19/19

	W	SN	Ĺ			POFile	manic POE				POE-D01-00-E-
) V, 100V, 500V, 1 1	XV, 2KV TI	EMPE	RATUI	RE COMI	PENSAT	ING CER	AMIC D	ISC CAPA	CITOR	Ver: 15	Page: 5 of 19
Part number	for SAP	svst	em(te	ntal eig	ohteen	code)	:				
<u>SL</u> <u>102</u>		•		050 6		-		<u>5</u>	H		
U 8	6	,	9	Ð	6	V	e	, e	, w		
Temperature c	haracteri	istic	:								
Temperature c SL: +350~-100			:								
	00ppm/°C		:								
SL: +350~-100 Rated voltage	00ppm/°C			<u>00V</u>	5		1		20)00V	7
SL: +350~-100	00ppm/℃ (Vdc):		1	00V 101		600V 501		000V 102)00V 202]
SL: +350~-100 Rated voltage Voltage Code	00ppm/°C (Vdc) : 500		1]
SL: +350~-100 Rated voltage Voltage Code	00ppm/°C (Vdc) : 50V 500 F) :		1				<u> </u>				
SL: +350~-100 Rated voltage Voltage Code Capacitance(pl	00ppm/°C (Vdc) : 50V 500 F) : (pF)		1	101		501	<u> </u>]
SL: +350~-100 Rated voltage Voltage Code Capacitance(p) Capacitors	00ppm/°C (Vdc): 50V 500 F): (pF)	47 470	1 100 101	330 331	470 471	501 820 821		102		202]
SL: +350~-100 Rated voltage Voltage Code Capacitance(pl Capacitors Code	00ppm/°C (Vdc) : 50V 500 F) : (pF) 4 lerance :	47 470 5 D:	100 101 : ±0.5	101 330 331 5pF (Fo	470 471	501 820 821		102		202]
SL: +350~-100 Rated voltage Voltage Code Capacitance(pl Capacitors Code Capacitance to	00ppm/°C (Vdc) : 50V 500 F) : (pF) 4 lerance : diamete	47 470 : D: er din	100 101 : ±0.5 mensi	101 330 331 5pF (Fc on :	470 471	501 820 821	: ±5%	102	pove 10	202 0pF)]

Packing mode and lead's length (identified by 2-figure code)

Taping Code	Description
AN	Ammo / Pitch of component:12.7 mm

Bulk Code	Description
3E	Lead's length L : 3.5mm
04	Lead's length L : 4mm
4E	Lead's length L : 4.5mm
20	Lead's length L : 20mm

8Length tolerance

Code	Description
А	±0.5 mm(Only for short kink lead code)
В	±1.0 mm
С	Min.
D	Taping special purpose

OPitch

Code	Description	Code	Description
5	5.0±0.8mm (For Bulk)	7	7.5 ±1mm
5	5.0+0.8mm-0.2mm (For Taping)	0	10.0 ±1mm
2	2.5 ±0.8 mm		

@Coating code

Code Description					
Р	Phenolic resin -Pb free				
А	Halogen free and Pb free, phenolic resin				
В	Epoxy Resin, Pb free				
Н	Halogen free and Pb free, epoxy resin				



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2. Mechanical:

Available lead code: (unit: mm)

Lead type	d code: (unit SAP P/N (13-17) digits	Pitch (F)	Lead length (L)	Available rated voltage	Packing	Lead configuration
	B20C2	2.5 ± 0.8	20 MIN.	50V&100V		D max. T max.
-	B20C2 B20C5	2.3 ± 0.8 5.0 ± 0.8	20 MIN. 20 MIN.	507@1007		
Lead style : B Straight long lead	B20C5 B20C6	5.0 ± 0.3 6.4 ± 1.0	20 MIN. 20 MIN.	-	Bulk	
	B20C0 B20C0	0.4 ± 1.0 10 ± 1.0	20 MIN. 20 MIN.	50V&100V, 500V,	Duik	()
	B20C0 B20C7	7.5 ± 1.0	20 MIN. 20 MIN.	1KV,2KV		
	B20C7 BAND5	5.0 ^{+0.8} -0.2	Taping Spec. (Ref.			
-	BAND2	2.5 ± 0.8	to page.10)	50V&100V	Tap. Ammo	ødədə 🕹
-	L05B2	2.5 ± 0.8	5.0 ± 1.0			Dmax. Tmax.
-	L05B5	5.0 ± 0.8	5.0 ± 1.0	-		
	L05B0 L05B6	$\begin{array}{c} 10 \pm 1.0 \\ 6.4 \pm 1.0 \end{array}$	5.0 ± 1.0 5.0 ± 1.0	-		
Lead style : L	L05B0	0.4 ± 1.0 7.5 ± 1.0	5.0 ± 1.0 5.0 ± 1.0	50110 10011 50011		
Straight short	L4EB5	5.0 ± 0.8	4.5 ± 1.0	50V&100V, 500V, 1KV, 2KV	Bulk	
lead -	L4EB7	7.5 ± 1.0	4.5 ± 1.0	1KV, 2KV		[®] ┲╠╾╒╶╅╡┲╼┟╽╽
	L4EB0	10 ± 1.0	4.5 ± 1.0			ø d L
	H3EA5	5.0 ± 0.8	3.5 ± 0.5			
-	H04A5	5.0 ± 0.8	4.0 ± 0.5			
ſ	H4EB5	5.0 ± 0.8	4.5 ± 1.0	50X & 100X 500X	Bulk	
	H05B5	5.0 ± 0.8	5.0 ±1.0	50V&100V, 500V, 1KV		
Lead style : H	H20C5	5.0 ± 0.8	20 MIN.			D max. T max. I+I I+I
	HAND5	5.0 +0.8 -0.2	Taping SPEC. (Ref. to page.10)		Tap. Ammo	
	H05B7	7.5 ± 1.0	5.0 ±1.0	-		
Inside kink	H05B0 H20C0	$\begin{array}{r} 10 \pm 1.0 \\ 10 \pm 1.0 \end{array}$	5.0 ±1.0 20 MIN.	-		
lead	H20C0 H04A7	10 ± 1.0 7.5 ± 1.0	4.0 ± 0.5		Bulk	┋┯ڲੂ┎_≦ੑੑੑੑੑੑੑੑੑੑ
-	H04A0	10 ± 1.0	4.0 ± 0.5 4.0 ± 0.5	50V&100V, 500V,		
	H3EA7	7.5 ± 1.0	3.5 ± 0.5	1KV,2KV		
	H3EA0	10 ± 1.0	3.5 ± 0.5			
	H4EB7	7.5 ± 1.0	4.5 ± 1.0			
	H4EB0	10 ± 1.0	4.5 ± 1.0			
	X3EA5	5.0±0.8				
	X3EA7	7.5±1.0	3.5 ± 0.5			D max. T max.
	X3EA0	10±1.0				
Lead style : X	X04A5	5.0±0.8				
Outside kink	X04A7	7.5±1.0	4.0 ± 0.5	50V&100V, 500V,	Bulk	
lead	X04A0	10±1.0		1KV, 2KV		ž i kra n kri
	X05B5	5.0±0.8				
-	X05B7	7.5±1.0	5.0 ± 1.0			ød
	X05B0	10±1.0				
	D04A5	5.0±1.0				D max. ,T max,
	D04A7	7.5±1.0	4.0 ± 0.5			
Lead style : D	D04A7	10±1.0	1.0 ± 0.5			
Vertical kink				50V&100V, 500V,	Bulk	
short lead	D3EA5	5.0±0.8		1KV, 2KV		
short lead	D3EA7	7.5±1.0	3.5 ± 0.5			
	D3EA0	10±1.0				
	DAND5	5.0 ^{+0.8} -0.2	Taping SPEC. (Ref. to page.10)		Tap. Ammo	



Lead type	SAP P/N (13-17) digits	Lead length (L)	Available rated voltage	Packing	Lead configuration	l
	M05B5				D max. T ma	
	M05B7	5.0 ± 1.0	50V&100V, 500V, 1KV, 2KV			1
	M05B0					
Lead style : M	M04B5					
Double outside	M04B7			Bulk		Ņ
kink lead	M04B0	4.0 ± 1.0				

* Lead diameter φ = 0.55 +/-0.05mm

X Phenolic resin coating for 50V/500V/1KV product; Epoxy resin coating for 1KV or 2KV product.

※ e (Coating **extension** on leads):

For straight lead style: 1.5mmMax when the rated voltage is 50Vdc & 100Vdc;

2.0mmMax when the rated voltage is 500Vdc and 1KVdc;

3.0mmMax when the rated voltage is 2KVdc.

For kink lead style: not exceed the kink.

When Dφ≥11mm, only for bulk, but Dφ≤10mm can do Bulk or Taping.

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3. Capacitance value vs. rated voltage, product diameter:

T.C							.9.71	-		S	L									
Rate voltage			5	0V/100	V					500V				11	ζV			21	ζV	
Dφ	040	050	060	070	080	090	100	050	060	070	080	100	050	060	070	080	060	070	080	110
D max. (mm)	5.0	6.0	7.0	8.0	9.0	10.0	11.0	6.0	7.0	8.0	9.0	11.0	6.0	7.0	8.0	9.0	7.5	8.5	9.5	12.5
T max.	3.5	3.5	3.5	3.5	3.5	3.5	3.5	4.0	4.0	4.0	4.0	4.0	4.5	4.5	4.5	4.5	4.5	4.5	4.5	4.5
(mm) 2	0.0	0.0	0.0	0.0	0.0	0.0	0.0													
3																				
4																				
5																				
6																				
7																				
8	080							080					100							
10 12	100 120					+		100 120				+	100 120							
12	120							120					120				150			
13	130							130					130				130			
20	200							200					200				200			
22	220							220					220				220			
24	240							240					240				240			
27	270							270					270				270			
30	300							300					300				300			
33	330							330					330				330			
36 39	360 390							360 390					360 390				360 390			
47	470							470					470				470			
51	510							510					510				510			
56	560							560					560				560			
68	680							680					680				680			
75	750							750						750			750			
82	820							820						820			820			
100	101	101						101						101				101		
120		121							121					121					121	
150 180		151 181							151	181					151 181				151 181	
200		101	201							201					101	201			201	
220			201							201						201			201	
240			241								241							1	241	
270				271							271								271	
300				301							301									301
330				331							331									331
360				361								361								
390 470				391	471							391								
470 500					471	501														
510	1					501														
560						561						1						1		
680	-						681													
750							751													
820							821													
PACKING			TAP	ING or E	ULK				TAP	ING or B	ULK				or BULI		ТАР	ING or E		BULK
COATING						Phenol	ic resin						Pheno	lic resin	or Epoxy	Resin		Epoxy	Resin	

4. Marking:

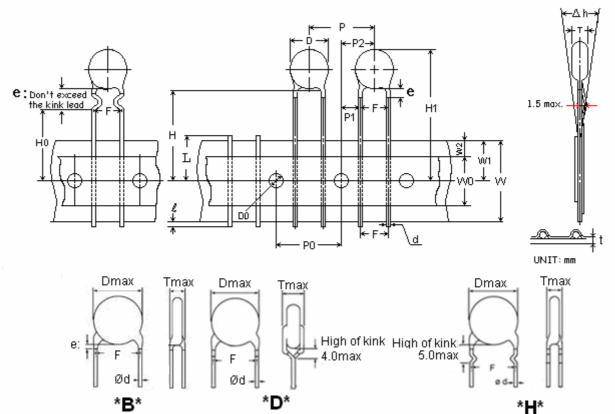
Marking Remarks		SL 47J 1KV (4) (5) (5)				
(1). Temp. char.	SL: No markir	g.				
(2). Rated capacitance	Identified by 3-Figure Code. Ex. $47pF \rightarrow "47"$, $470pF \rightarrow "471"$					
	50V&100V	Marked with code "" under the rated capacitance.				
(3). Rated voltage	500V	No any marking under the rated capacitance.				
	1000V&2000V	Marked with code: 1000V→"1KV", 2000V→"2KV"				
(4). Capacitance tolerance	C: ±0.25pF (For below 5pF) 、 D: ±0.5pF (For6~10pF) 、 J: ±5% (For above 10pF)					
(5). Manufacturer's identification	Shall be marked as " \lor ", but D $\Phi \leq 060$ shall be omitted.					
(6). Halogen and Pb free	There is a "" Pb free Epoxy.	There is a ""marking under the code "V" when the coating resin is Halogen and				

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5. Taping specifications:

* Lead spacing: **F**=5.0 ^{+0.8}_{-0.2} (**mm**)

• 12.7mm pitch/lead spacing 5.0mm taping Lead code: *BAND5 & *DAND5 & *HAND5

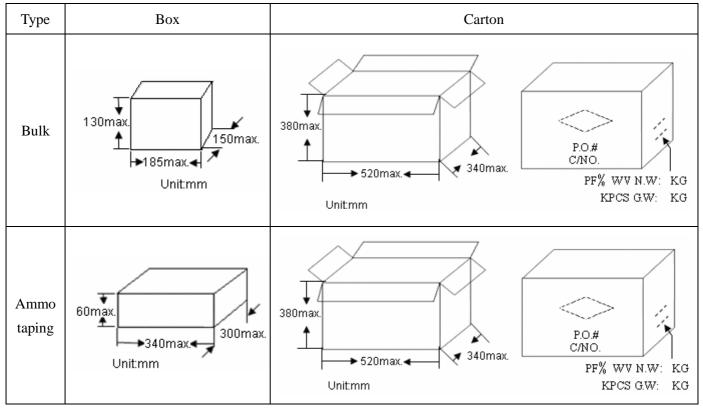


Item			Specification		Remarks	
			Value	Tolerance	NCHIAIKS	
Body diameter		D	*	max.	See Section"3. Capacitance value vs. rated	
Body thickness		Т	*	max.	voltage, product diameter".	
Lead-wire diameter		d	0.55	±0.05		
Pitch of component		Р	12.7	±1.0		
Feed hole pitch		P0	12.7	±0.3	Cumulative pitch erroe:1.0mm/20 pitch	
Feed hole center to lead		P1	3.85	±0.7	To be measured at bottom of clinch	
Hole center to component center		P2	6.35	±1.3		
Lead-to-lead distance		F	5.0	+0.8, -0.2		
Component alignment, F-R			0	±2.0		
Tape width		W	18.0	+1.0, -0.5		
Hole-down tape width		W0	8.0	min.		
Hole position		W1	9.0	+0.75, -0.5		
Hole-down tape position		W2	3.0	max.		
Height of component form tape	For straight lead type	Н	20.0	+1.0 - 0.5		
center	For kinked lead type	H0	16.0	±0.5		
Component height			32.25	max.		
Lead-wire protrusion			2.0	max.	Or the end of lead wire may be inside the tape.	
Food hole diameter			4.0	±0.2		
Total tape thickness			0.7	±0.2	Ground paper:0.5±0.1mm	
Length of sniped lead			11.0	max.		
Coating rundown on leads		e	Please refer to page 6 "e(Coating extension on leads)".			

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6. Packing Baggage :

6.1 Packing size:



6.2 Packing quantity:

Packing Type	The	e code of 14th to15th in SAP P/N	MPQ (Kpcs/Box)	Remark
Taping		AN	2		Phenolic resin
Taping		AN	1.5		Epoxy resin
Packing Type	Lead length Size code of 10th to 12th in SAP P/N		MPQ (Kpcs/Bag)	Kpcs/Box	Remark
		040~070	1	3	Phenolic resin
	Long lead	080~100	1	2	Phenolic resin
	$(L \ge 16mm)$	050~100	1	2	Epoxy resin
Bulk		110~120	0.5	1.5	
Биік		040~060	1	6	
	Short lead	070~080	1	4	
	(L<16mm)	090~100	1	3	
		110~120	1	2	

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7. Specification and test method:

7.1 SCOPE: THIS SPECIFICATION APPLIES TO TEMPERATURE COMPENSATING CERAMIC DISC CAPACITOR.

7.2 TEST CONDITIONS :

UNLESS OTHERWISE SPECIFIED, ALL TESTS SHALL BE OPERATED AT THE STANDARD TEST CONDITIONS OF TEMPERATURE 5°C TO 35°C AND RELATIVE HUMIDITY 45% TO 85%. WHEN FAILS A TEST, RETEST BE OPERATED AT THE CONDITIONS OF TEMPERATURE $25^{\circ}C \pm 2^{\circ}C$, RELATIVE HUMIDITY OF 60% TO 70% AND BAROMETRIC PRESSURE 860 TO 1060 MBAR.

- 7.3 HANDLE PROCEDURE : TO AVOID UNEXPECT TESTING RESULTS FROM OCCURRING, THE TESTED CAPACITOR MUST BE KEPT AT ROOM TEMPERATURE FOR AT LEAST 30 MINUTES AND COMPLETELY DISCHARGED.
- 7.4 TEST ITEMS :

ITEM POST-TEST REQUIREMENTS		TESTING PROCEDURE		
APPEARANCE STRUCTURE SIZE	NO ABNORMALITIES	AS SECTION 3.		
MARKING		AS STATED IN SECTION 4		
	BETWEEN TERMINALS: NO ABNORMALITIES	 A. BELOW 1KV: 300% RATED VOLTAGE WITH 50mA MAX. CHARGING CURRENT FOR 1~5 SEC. B. 1KV & ABOVE: 200% RATED VOLTAGE WITH 50mA MAX. CHARGING CURRENT FOR 1~5 SEC. 		
WITHSTAND VOLTAGE	BETWEEN TERMINAL AND ENCLOSURE : NO ABNORMALITIES	SMALL METALLIC BALLS WITH 1mm DIAMETERS SHALL BE PUT ON A VESSEL AND THE TEST CAPACITOR SHALL BE SUBMERGED EXCEPT 2mm FROM THE TOP OF ITS COMPONENT BODY. THE TEST VOLTAGE SHALL BE APPLIED BETWEEN THE SHORT-CIRCUITED TERMINALS AND THE METALLIC BALLS. (APPLY 1.3KV DC OF RATED VOLTAGE BETWEEN TERMINALS AND ENCLOSURE FOR 1~5 SEC)		
INSULATION RESISTANCE	10000 MΩ MIN	INSULATION RESISTANCE SHALL BE MEASURED AT 60±5 SECONDS AFTER APPLIED VOLTAGE (RATED) RATED VOLTAGE: 50V=50V, 100V=100V, 500V & ABOVE=500V		
CAPACITANCE	$\begin{array}{llllllllllllllllllllllllllllllllllll$	TESTING FREQUENCY : 1 MHZ ± 20% TESTING VOLTAGE : 1.0 VRMS		
OPERATING TEMPERATURE RANGE	$-25^{\circ}\text{C} \sim +125^{\circ}\text{C}$			
Q FACTOR	$ \begin{array}{c c} 30 \ \text{PF} \\ \& \ \text{ABOVE} \\ \hline \\ BELOW \\ 30 \ \text{PF} \\ \end{array} \begin{array}{c} Q \geq 1000 \\ Q \geq 400 + 20 \times C \end{array} $	AS ABOVE STIPULATION OF CAPACITANCE		



ITEM	POST-TEST REQUIREMENTS		TEST	ING	PROC	EDURI	E	
		ACCORDING TO STEP 1 TO 5 IN ORDER, MEASURED CAPACITANCE WHEN TEMPERATURE REACH BALANCE AND TEMPERATURE COEFFICIENT SHALL BE CALCULATED ON THE FOLLOWING FORMULA : PPM/°C =(C2-C1)×10E6/C1(T2-T1)						
	TEMPERATURE COEFFICIENT : SL :+350~-1000 ppm/°C	Step	1	2	3	4	5	
	FOR (+20°C ~+85°C)	Temp. (°C)	20±2	-25±3	20±2	85±2	20±2	
TEMPERATURE CHARACTERISTIC		NOTE : C1 = CAPACITANCE AS STEP 3 C2 = CAPACITANCE AS STEP 2 OR 4 T1 = TEMPERATURE AS STEP 3 T2 = TEMPERATURE AS STEP 2 OR 4						
	CAPACITANCE TOLERANCE : WITHIN ±0.2% OR ±0.05PF, WHICHEVER IS LARGE	ACCORDING TO ABOVE STEP 1,3 & 5, CAPACITANCE TOLERANCE SHALL BE CALCULATED ON THE FOLLOWING FORMULA : $\triangle C\% = (G - S)/C1$ NOTE : G = GREATEST CAPACITANCE AS TESTING RESULT OF STEP 1,3 & 5 S = LEAST CAPACITANCE AS TESTING RESULT OF STEP 1,3 & 5 C1 = CAPACITANCE AS STEP 3						
TERMINAL	TENSIBLE STRENGTH : NO BREAKDOWN	WIRE DIA.0.5 M/M. LOADING WEIGHT 0.5 KGS, FOR 10±1 SECONDS. WIRE DIA.0.6 M/M. LOADING WEIGHT 1.0 KGS, FOR 10±1 SECONDS.						
STRENGTH	BENDING STRENGTH : NO BREAKDOWN	WIRE DIA.0.5 mm, LOADING WEIGHT 0.25 KGS. WIRE DIA.0.6 mm, LOADING WEIGHT 0.5 KGS. (BENDING BACK AND FORTH 90 DEGREE TWICE)						
	APPEARANCE : NO ABNORMALITIES	 LEAD WIRE OR TERMINALS SHALL BE IMMERSED TO 2.0 M/M FORM BODY. (A) BODY DIA. ≤ 5.0mm: INTO THE MOLTEN SOLDEF WHICH TEMPERATURE: 260(+5/-0)°C FOR 3.0- SECONDS. (B) BODY DIA. > 5.0mm: INTO THE MOLTEN SOLDEF WHICH TEMPERATURE 260(+5/-0)°C FOR 5~10 SECONDS. THEN LEAVE AT STANDARD TEST CONDITIONS FOR 1~2 HOURS, THEN MEASURED. 				ED UP DER OF		
SOLDERING	CAP.CHANGE : WITHIN ±2.5% OR ±0.25PF, WHICHEVER IS LARGE.					DER OF		
HEAT RESISTANCE	WITHSTAND VOLTAGE : (BETWEEN TERMINALS)					FOR		
	NO ABNORMALITIES	WHEN SOLDERING CAPACITOR WITH A SOLDERING						
		IRON, IT SHOULD BE PERFORMED IN FOLLOWING CONDITIONS.						
		TEMPERATURE OF IRON-TIP: 350~400 °C						
		SOLDERING IRON WATTAGE : 50W MAX. SOLDERING TIME : 3.5 SEC. MAX.						
SOLDERABILITY LEAD WIRE SHALL BE TO COMPLY WITH JIS-C-5102 8.4 SOLDER SOLDERABILITY SOLDERED OVER 75% OF THE TEMPERATURE245±5°C AND DIPPING TIME 5± CIRCUMFERENTIAL DIRECTION. SECONDS FLUX : WEIGHT RATIO OF ROSIN 2			±0.5					



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ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE		
HUMIDITY CHARACTERISTIC	$Q \ge 200 + 10 \times C$ MORE THAN 10PF AND LESS THAN 30PF => $Q \ge 275 + 5 \times C / 2$ MORE THAN 30PF => $Q \ge 350$ INSULATION RESISTANCE :	CAPACITORS SHALL BE SUBJECTED TO A RELATIVE HUMIDITY OF 90 ~ 95% AT 40 ± 2°C FOR 500(+24/-0) HOURS, THEN DRIED FOR 1~2 HOURS AND MEASURED.		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$		CAPACITORS SHALL BE SUBJECTED TO A RELATIVE HUMIDITY OF 90 \sim 95% AT 40±2°C FOR 500(+24/-0) HOURS WITH RATED VOLTAGE APPLIED (LESS THAN 50mA), THAN DRIED FOR 1 \sim 2 HOURS AND MEASURED.		
$\begin{array}{c} 500 \text{M}\Omega \text{ MIN.} \\ \\ \text{APPEARANCE :} \\ \text{NO ABNORMALITIES} \\ \text{CAP. CHANGE :} \\ \text{SL :} \\ \text{WITHIN $\pm 3\%$ OR $\pm 0.3 \text{PF,}$ \\ \text{WHICHEVER IS LARGE} \\ \\ \text{Q FACTOR :} \\ \text{SL :} \\ \text{LOADING} \\ \begin{array}{c} \text{Q FACTOR :} \\ \text{SL :} \\ \text{LESS THAN 10 \text{PF }=>} \\ \text{Q $\geq 200 + 10 \times \text{C}$ \\ \text{MORE THAN 10 \text{PF } \& \text{LESS THAN}$ \\ 30 \text{PF }=> \\ \text{Q $\geq 275 + 5 \times \text{C} / 2$ \\ \text{MORE THAN 30 \text{PF }=> \text{Q ≥ 350 \\ \hline \text{INSULATION RESISTANCE :} \\ 1000 \text{M}\Omega \text{ MIN.} \end{array}$		 CAPACITORS SHALL BE SUBJECTED TO A TEST OF: (A) BELOW 1KV: 200% RATED VOLTAGE WITH 50mA MAX. (B) 1KV & ABOVE: 150% RATED VOLTAGE WITH 50mA MAX. FOR 1000(+48/-0) HOURS AT 125°C ± 2°C (FOR CH & SL) AND THEN DRIED FOR 1~2 HOURS AND MEASURED. 		



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ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE
	APPEARANCE :	CAPACITORS SHALL BE SUBJECTED TO: -25 \pm 3°C (30 \pm 3min) \rightarrow 25°C (3min) \rightarrow 125 \pm 3°C (30 \pm 3min) \rightarrow
	NO ABNORMALITIES	25°C (3min) FOR 5 CYCLE.
	CAP. CHANGE :	
	WITHIN ±5% OR ±0.5PF,	
TEMPERATURE	WHICHEVER IS LARGE	
CYCLING	D.F.	
	$C < 30 pF : Q \ge 275 + (5/2)C$	
	$C \ge 30 pF : Q \ge 350$	
	INSULATION RESISTANCE :	
	1000 ΜΩ ΜΙΝ.	

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8. Cautions & notices:

- 8.1. Caution (Rating)
- I. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional measurement	Vo-p	V0-p	Vp-p	Vp-p	Vp-p

II. Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. The frequency of the applied sine wave voltage should be less than 100kHz. The applied voltage load (*) should be such that the capacitor's self-generated heat is within 20°C at an atmosphere temperature of 25°C. When measuring, use a thermocouple of small thermal capacity-K of \emptyset 0.1mm in conditions where the capacitor is not affected by radiant heat from other components or surrounding ambient fluctuations.

Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

III. Fail-Safe

When capacitor is broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

- 8.2. Caution (Storage and operating condition)
- I. Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 degrees centigrade and 15 to 85 % for 6 months maximum and use within the period after receiving the capacitors.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

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8.3.Caution (Soldering and Mounting)

I. Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

II. Soldering

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor.

Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element. When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage : 50W max.

Soldering time : 3.5 sec. max.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

8.4. Caution (Handling)

Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRDUCT IS USED.

8.5. Notice

8.5.1. Notice (Soldering and Mounting)

Cleaning (ultrasonic cleaning)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity : Output of 20 watts per liter or less.

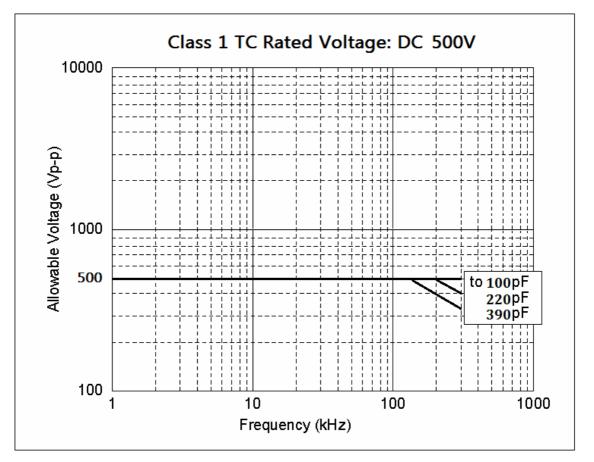
Rinsing time : 5 min. maximum.

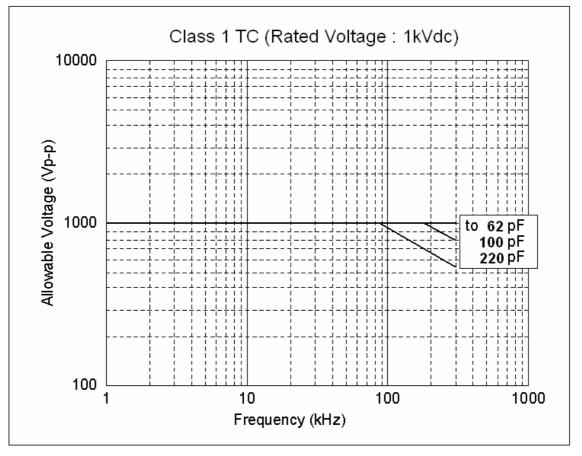
Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

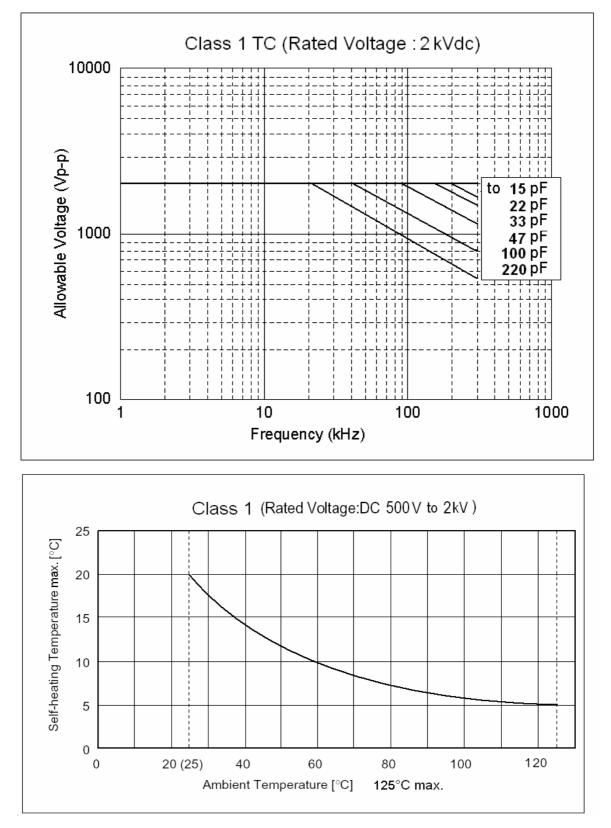


8.6. Ambient Temp of Allowable Voltage Graph (500Vdc to 2kVdc)





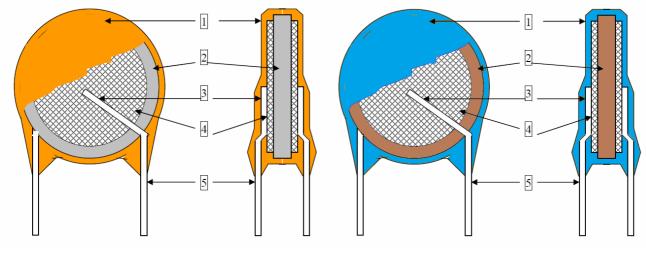




The ambient temperature and the surface temperature of capacitor must be 125° C or lower. (Including self-heating.)

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9. Drawing of internal structure and material list:



(phenolic resin)

(epoxy resin)

NO.	部位	材質	構成部份	供應商
NO.	Part name	Material	Component	Vendor
1	Inculation Coating	Phenolic resin	Phenolic resin, Filler, Pigment	Namics
1	Insulation Coating	Epoxy resin	Epoxy resin Epoxy resin, SiO2, TiO2	
				Hua Xing
2	Dielectric Element	Ceramic	BaTiO3	Wang Feng
				Fenghua
3	Solder	Tin-silver	Sn97.5-Ag2.5	Huajun
5	Solder		51197.3-Ag2.3	Haili
4	Electrodes	Ag	Silver,Glass frit	Daejoo
4	Electiones		Silver, Olass III	Xinguang
5	Leads wire	Tinned copper	Substrate metal:Fe&Cu	Hengtai
5		clad steel wire	Surface plating:Sn 100%	Wuhu Taililai