

P-Channel Enhancement Mode Power MOSFET

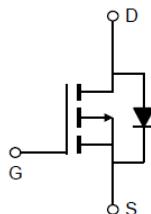
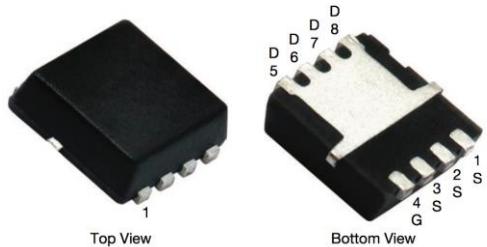
● Features

$V_{DS} = -30V$,
 $I_D = -44A$
 $R_{DS(ON)} @ V_{GS} = -10V$, TYP $9m\Omega$
 $R_{DS(ON)} @ V_{GS} = -4.5V$, TYP $14.7m\Omega$

● General Description

- Backlighting
- Power Management Functions
- DC-DC Converters

● Pin Configurations



PDFN3*3-8L

● Absolute Maximum Ratings @ $T_A=25^\circ C$ unless otherwise noted

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current (Continuous) *AC	$T_C=25^\circ C$	I_D	-44	A
	$T_C=70^\circ C$		-35	
Drain Current (Pulse) *B		I_{DM}	-176	A
Power Dissipation	$T_C=25^\circ C$	P_D	33.7	W
Operating Temperature/ Storage Temperature		T_J/T_{STG}	-55~150	°C

● Thermal Resistance Ratings

Parameter		Symbol	Maximum	Unit
Maximum Junction-to-Ambient	Steady State	R_{thJA}	66	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	3.7	

● **Electrical Characteristics @ $T_A=25^\circ\text{C}$** unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = -250 \mu\text{A}$	-30	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30\text{ V}, V_{GS} = 0\text{V}$	--	--	-1	μA
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{GS} = V_{DS}, I_{DS} = -250 \mu\text{A}$	-1	-1.5	-3	V
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	--	--	± 100	nA
Drain-Source On-state Resistance	$R_{DS(\text{on})}$	$V_{GS} = -10\text{V}, I_D = -15\text{A}$	--	9	14	$\text{m}\Omega$
	$R_{DS(\text{on})}$	$V_{GS} = -4.5\text{V}, I_D = -10\text{A}$	--	14.7	20	$\text{m}\Omega$
Diode Forward Voltage	V_{SD}	$I_{SD} = -1\text{A}, V_{GS} = 0\text{V}$	--	-0.7	--	V
Diode Forward Current	I_S	$T_C = 25^\circ\text{C}$	--	--	-44	A
Switching						
Total Gate Charge	Q_g	$V_{GS} = -10\text{V}, V_{DS} = -15\text{V}, I_D = -11.5\text{A}$	--	51	--	nC
Gate-Source Charge	Q_{gs}		--	6.6	--	nC
Gate-Drain Charge	Q_{gd}		--	15	--	nC
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -15\text{V}, V_{GS} = -10\text{V}, R_G = 6\Omega, I_D = -11.5\text{A}$	--	7.8	--	ns
Turn-on Rise Time	t_r		--	19.9	--	ns
Turn-off Delay Time	$t_{d(off)}$		--	57.5	--	ns
Turn-Off Fall Time	t_f		--	42.8	--	ns
Dynamic						
Input Capacitance	C_{iss}	$V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	--	2147	--	pF
Output Capacitance	C_{oss}		--	407	--	pF
Reverse Transfer Capacitance	C_{rss}		--	358	--	pF

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The current rating is based on the $t \leq 10\text{s}$ junction to ambient thermal resistance rating

- **Typical Performance Characteristics (($T_J = 25^\circ\text{C}$, unless otherwise noted))**

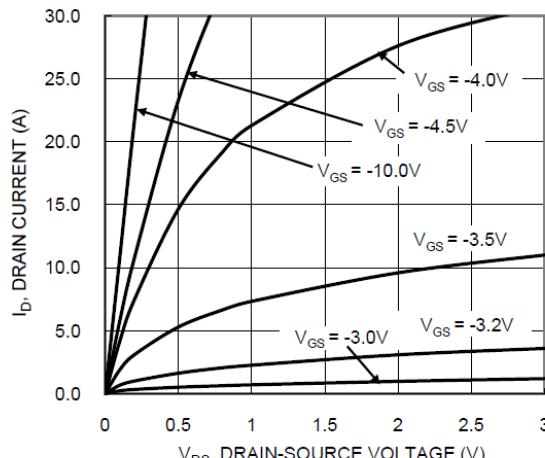


Figure 1. Typical Output Characteristic

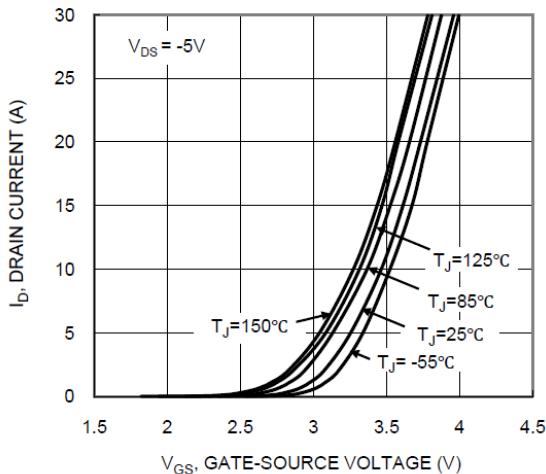


Figure 2. Typical Transfer Characteristic

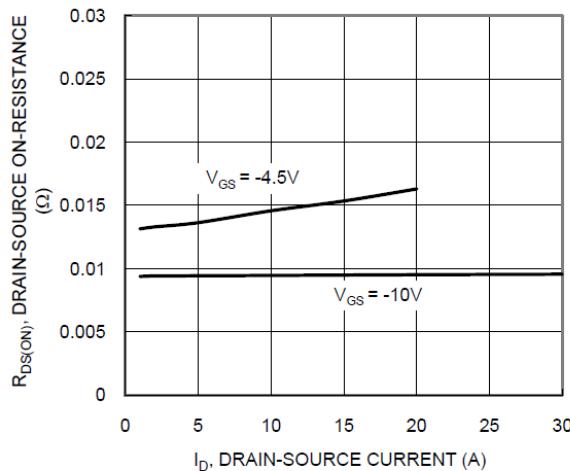


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

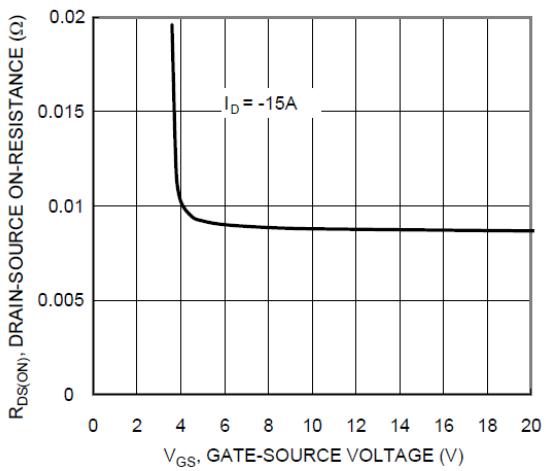


Figure 4. Typical Transfer Characteristic

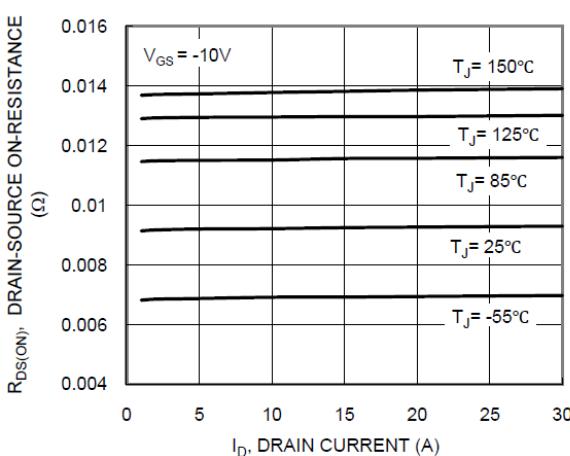


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

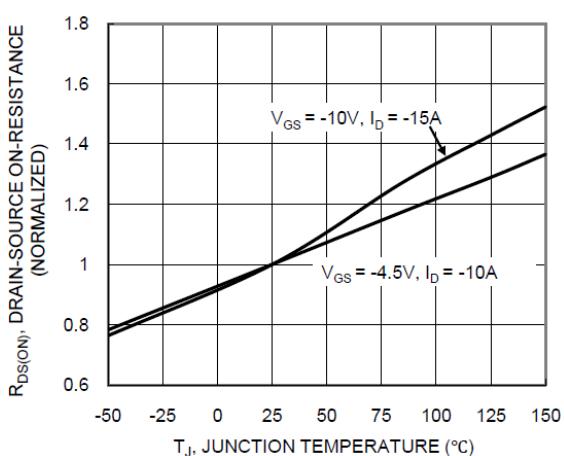


Figure 6. On-Resistance Variation with Junction Temperature

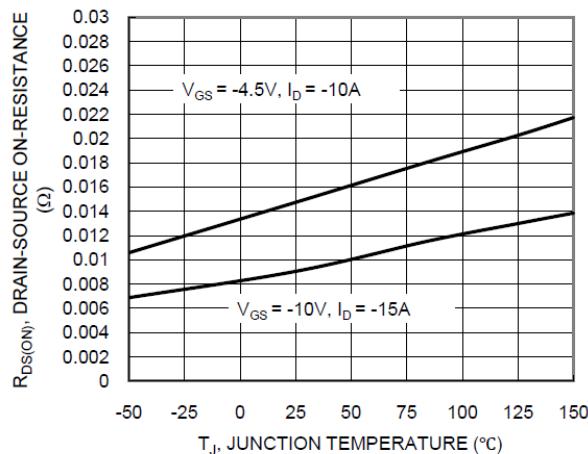


Figure 7. On-Resistance Variation with Junction Temperature

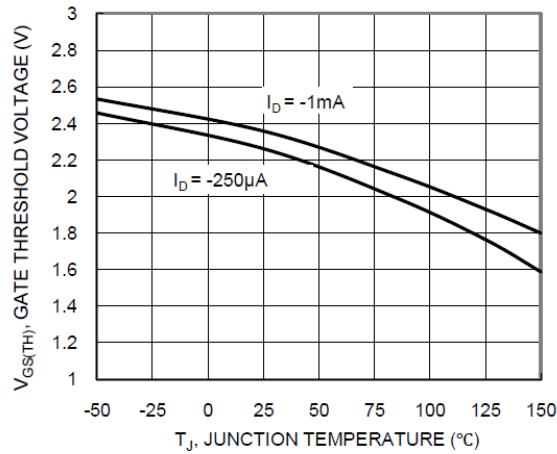


Figure 8. Gate Threshold Variation vs. Junction Temperature

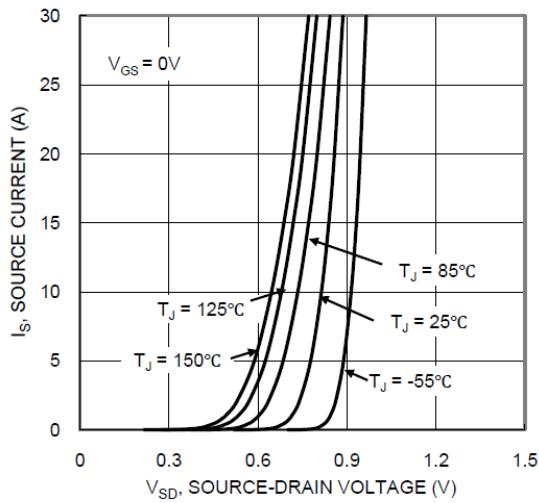


Figure 9. Diode Forward Voltage vs. Current

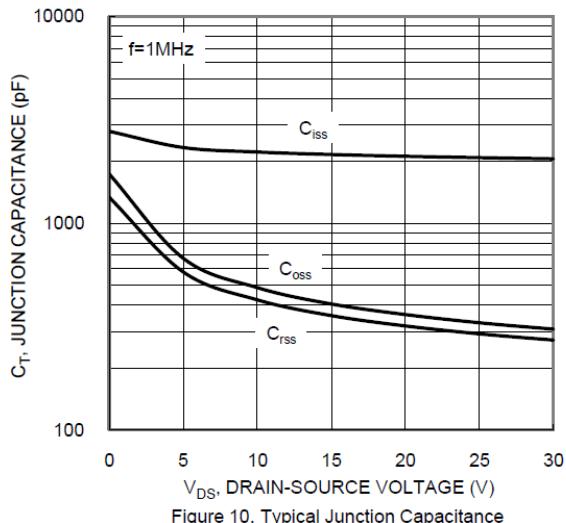


Figure 10. Typical Junction Capacitance

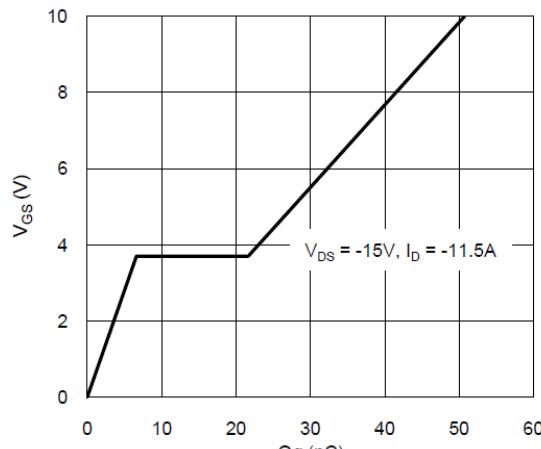


Figure 11.Gate Charge

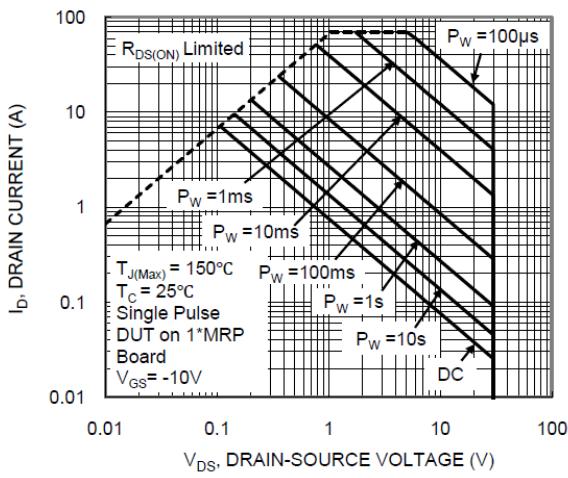


Figure 12. SOA, Safe Operation Area

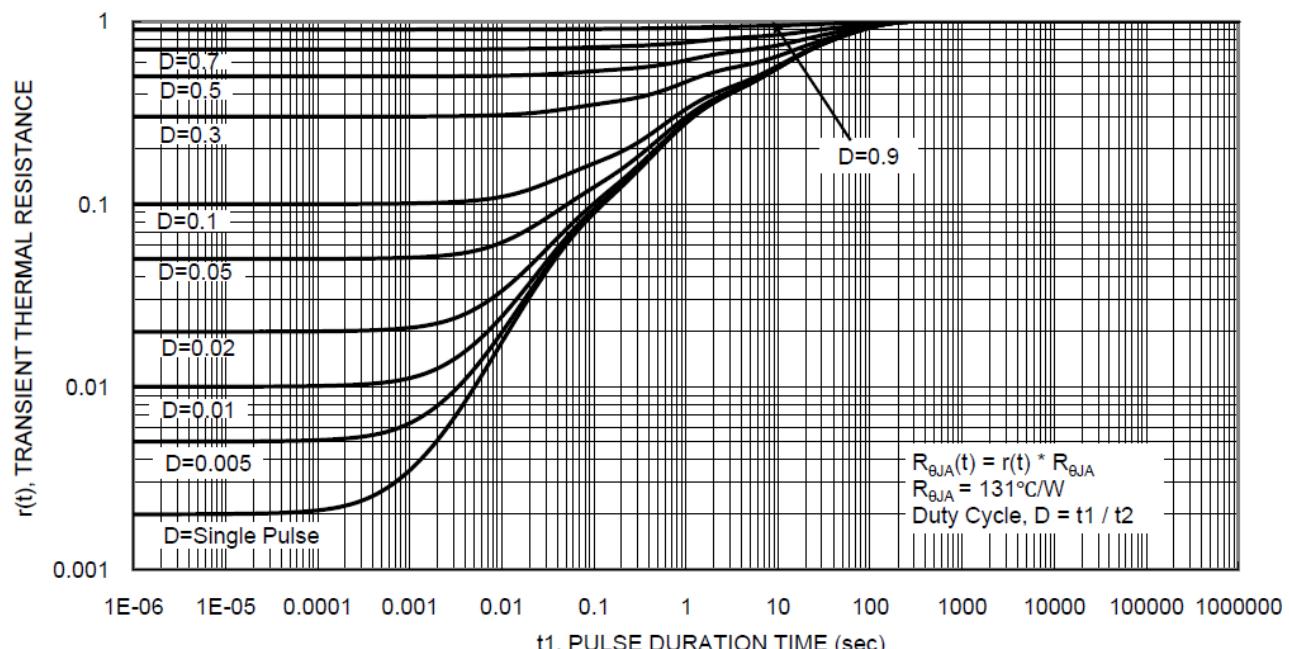
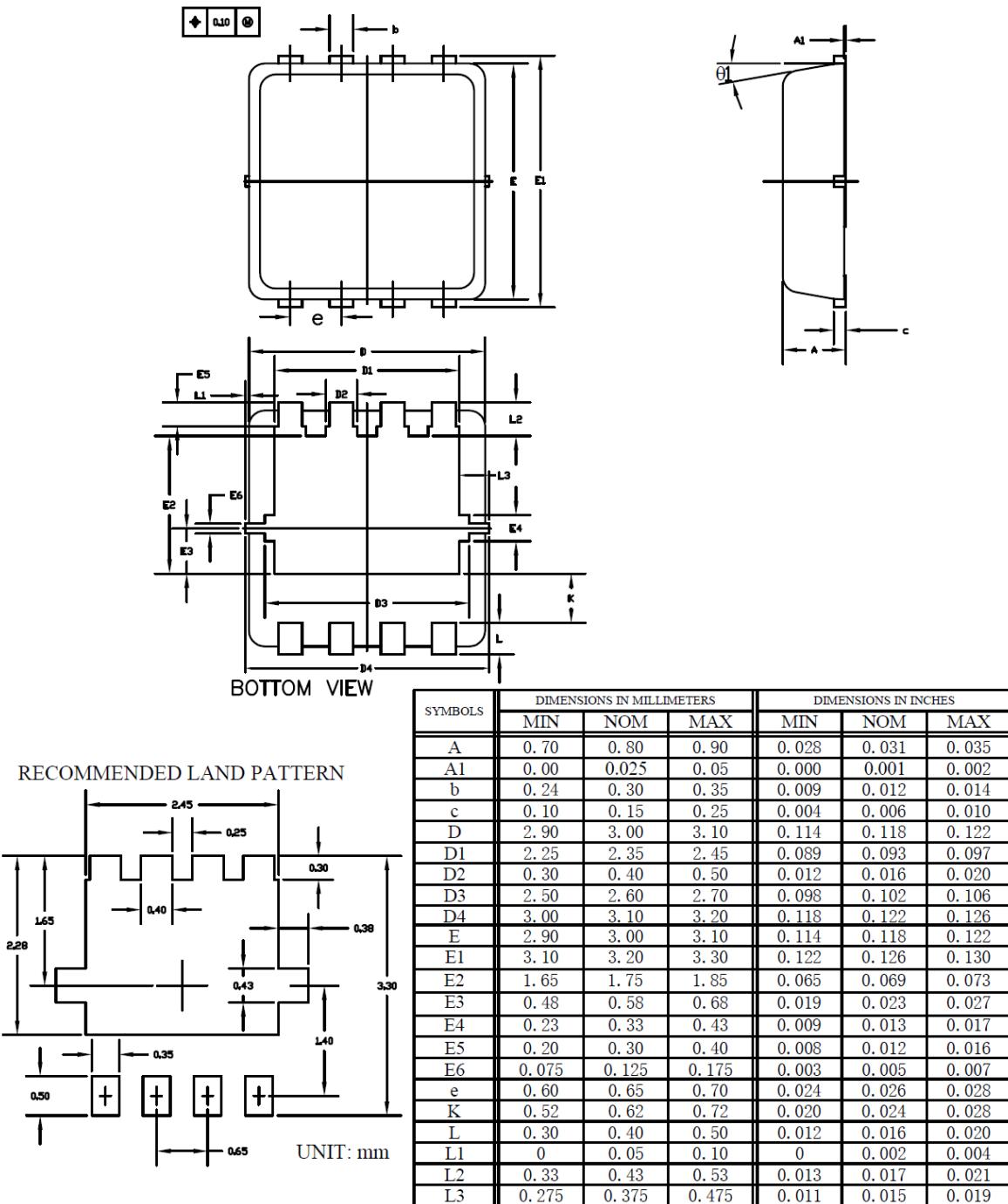


Figure 13. Transient Thermal Resistance

- Package Information



NOTE

1. PACKAGE DIMENSION IS EXCLUSIVE OF MOLD GATE BURR
2. PACKAGE DIMENSION IS EXCLUSIVE OF MOLD FLASH AND CUTTING BURR
3. CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.